Available at Digi-Key

www.digikey.com



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Low ADEV OCXO Series VCOCXO: OH320-LA-71003CV VCOCXO: OH320-LA-61003CV OCXO: OH320-LA-61003CF

Description:

Connor-Winfield's OH320-LA-series is a 3.3Vdc LVCMOS OCXO series with very low Allan Deviation (ADEV). These devices are available as VCOCXO or fixed OCXO, and packaged in a 22x25.4mm surface mount footprint.



Features:

- Output Frequency: 10.0 or 12.8 MHz
- 3.3 Vdc Operation
- 22 x 25.4 mm SMT Package
- Frequency Stability: ±10 ppb
- Temperature Range: -20 to 70°C or -40 to 85°C
- LVCMOS Output
- Grounded Metal Cover
- RoHS Compliant / Lead Free
 RoHS

Absolute Maximum Ratings

Parameter	Minimum	Nominal	Maximum	Units	Notes
Storage Temperature	-55	-	105	°C	
Supply Voltage (Vcc)	-0.5	-	4.5	Vdc	
Control Voltage (Vc)	-1.0	-	5.0	Vdc	
Operating Supply Voltage	3.145	3.30	3.465	Vdc	

Absolute Ratings: Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only. The functional operation of the device at those or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to conditions outside the "recommended operating conditions" for any extended period of time may adversely impact device reliability and result in failures not covered by warranty.

Operating Specifications

Parameter	Minimum	Nominal	Maximum	Units	Notes
Center Frequency (Fo)	-	10.0 or 12.8	-	MHz	NOLES
Operating Temperature		10.0 01 12.0		101112	
OH320-LA-71003CV	-20		70	°C	
OH320-LA-61003CF/CV	-40		85	°Č	
Frequency Calibration @ 25°C	-100	_	100	ppb	
Frequency Stability vs. Temperature		_	10	ppb ppb	1
Frequency vs. Supply Voltage	-0.5	_	0.5	dqq	Vcc ±5%
Frequency vs. Load	-0.5	-	0.5	<u>dqq</u>	Load ±5%
Aging: Daily	-0.5	-	0.5	ppb/day	2
Aging: First Year	-50	-	50	dqq	
Lifetime Tolerance: (20 years)	-300	-	300	dqq	3
Supply Voltage (Vcc)	3.13	3.30	3.47	Vdc	4
Power Consumption: Vcc = 3.30 \	/dc				
OH320-LA-71003CV Steady State @ 25°C		-	1.15	W	5
Turn On @ -20°C	-	-	3.20	W	5
OH320-LA-61003CF/CV Steady State @ 2	5°C -	-	1.50	W	5
Turn On @ -40°C	_	-	3.63	W	5
Phase Jitter: (BW: 10 Hz to Fo/2)	-	-	1.0	ps rms	
Allan Deviation (Tau=1s)	-	2.7E-12	5.0E-12		
Start-Up Time:	-	-	500	ms	
Warm Up Time: @ 25°C	-	-	5	minutes	6

Phase Noise

Parameter	Minimum	Nominal	Maximum	Units	Notes
SSB Phase Noise at 1Hz offset	-	-	-85	dBc/Hz	
SSB Phase Noise at 10Hz offset	-	-	-115	dBc/Hz	
SSB Phase Noise at 100Hz offset	t –	-	-140	dBc/Hz	
SSB Phase Noise at 1KHz offset	-	-	-145	dBc/Hz	
SSB Phase Noise at 10KHz offse	t -	-	-150	dBc/Hz	
SSB Phase Noise at 100KHz offs	et -	-	-150	dBc/Hz	

Voltage Control Input Characteristics (VCOCXO only)							
Parameter Minimum Nominal Maximum Units Notes							
0.0	1.65	3.3	V	7			
±0.4	±0.6	-	ppm				
Input Impedance 100K Ohm							
	Minimum 0.0 ±0.4	Minimum Nominal 0.0 1.65 ±0.4 ±0.6	Minimum Nominal Maximum 0.0 1.65 3.3 ±0.4 ±0.6 -	Minimum Nominal Maximum Units 0.0 1.65 3.3 V ±0.4 ±0.6 - ppm			



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Attention: System Designers please review Application Note AN2093: System Design Information and Printed Circuit Board Layout Guidelines for OCXO Oscillators. @ www.conwin.com/support.html



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Ordering Information

OH320-LA-71003CV-012.8M, OH320-LA-61003CV-010.0M, OH320-LA-61003CF-010.0M

CMOS Output Characteristics

Parameter	Minimum	Nominal	Maximum	Units	Notes
Load	-	15	-	pF	
Output Voltage: High (Voh)	3.0	-	-	V	
Low (Vol)	-	-	0.4	V	
Output Current: High (Ioh)	-4	-	-	mA	
Low (Ìol)	-	-	4	mA	
Duty Cycle at 50% of Vcc	45	50	55	%	
Rise / Fall Time: 10% to 90%	-	-	6	ns	
Spurious Output	-	-	-80	dBc	

Notes:

1. Referenced to the frequency measured @ 25°C, VCOCXO Control Voltage (Vc) = Nominal Voltage.

2. At time of shipment after 48 hours of operation.

3. Inclusive of Calibration, Operating Temperature, Supply Voltage change, Load change, and 20 Year Aging.

4. Supply voltage must reach Vcc levels monotonically within a ramp-up time of <12 ms.

5. Measured with Vcc = Nominal in calm air.

6 Measured @ 25°C, within ±100 ppb, referenced one hour after turn-on.

7. To ensure proper operation of VCOCXO's, the control voltage input must be biased at the nominal control voltage. Failure to bias the Vc input will result in unstable operation.

Re-Stabilization Time

Off Time	Re stabilization Time	
<1 Hour	<2 Hours *	
<6 Hour	<12 Hours *	
<24 Hour	<48 Hours *	
1 to 16 Days	48 Hours + 1/4 Off Time *	
>16 Days	<6 Days *	

* For a given off time, the time required to meet daily aging, short term stability requirements.

Package Characteristics

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Caution: Do not submerg	ge the device!		
Drying Time:	Time will vary depending on the board size.		
Drying Temperature:	Between 85 to 100°C.		
Note: If saponifier is used ma	ake sure the device is rinsed properly to insure all residues are removed. PH of s	saponifier should not ex	ceed 10.
	Device must be dried properly prior to use!		
	circulated oven. Boards should be placed in the oven vertically	for good water runof	f
	with air knife and drying capabilities. If cleaner does not have c	drying capability, then	i use hot air
	moisture to be removed during the drying cycle. We recommen	nd in-line warm water	wash
	Device is non-hermetic, water resistance with four weep holes,	one in each corner to	allow
Recommended Cleaning F			
	device is properly pre-heated prior to relow. Refer to IPC 610E	for inspection guidelin	nes.
	repair station with profile monitoring capabilities. Following a mo		
Removal of device:	If for any reason the device needs to be removed from the boar		controlled
	device to insure that the internal package temperature does no		
In-line oven profile:	We recommend using KIC profiler or similar device placing one	e of the thermocouple	s on the
	eutectic alloy with melting point of 221 deg C.	0 1	
	consists of metal cover with FR4 substrate. Component solder i		
In-line reflow:	Refer to recommended reflow pre-heat and reflow temperatures	s on page 4. Package	e material
Solder Process Recommer	dations: RoHS compliant, lead free. See solder profile on page 4.		
Marking Permanency	Per MIL-STD-202G, Method 215J.		
Moisture	10 cycles, 95% RH, Per MIL-STD-202G, Method 112.	,	
Random Vibration	5.35 G's rms. 20 to 2000 Hz per MIL-STD-202G, Method 214,Test C		
Sinusoidal Vibration	0.06" D.A. or 10G's Peak, 10 to 500 Hz, per MIL-STD-202G, Me		
Shock	500 G's 1ms, Halfsine, 3 shocks per direction, per MIL-STD 2020	G. Method 213B Test (Condition D
	Environmental Characteristics		
OH320 Package	Package consisting of a FR4 substrate and Folded Metal cover.	Non-hermetic seal.	

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Package Outline



Pad Termination Finish: Gold Flash <10 micro inches

Marking Information

1935

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12.8 MHZ

0123456789012

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Date Code (YYWW) Model Number OH320-LA-71003CV **Output Frequency** Serial # Barcode Serial Number

Suggested Pad Layout



* Do not route any traces in the keep out area. It is recommended the next layer under the keep out area is to be ground plane.

Pad Connections

Pad	Connection			
1:	Control Voltage (VCOCXO)			
	or N/C (OCXO)			
2:	Do not Connect			
3:	Supply Voltage (Vcc)			
4:	RF Output			
5:	Do Not Connect			
6:	Do Not Connect			
7:	Ground			
DO NOT connect "DNC" pads to ground or supply rails.				

Test Circuit



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100

MTIE (ns) 0

> 1-0.01

0.10

1.00

10.00

Observation Interval (sec)

100.00

1000.00

Passed

100000.00

10000.00

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Solder Profile



Meets IPC/JEDEC J-STD-020C



 Orderable Part Numbers:

 OH320-LA-71003CV-012.8M

 OH320-LA-61003CV-010.0M

 OH320-LA-61003CF-010.0M

TDEV & MTIE 4-Day Stability Measurement Through HPF (1MHz)



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Tape and Reel Information



Revision History

Revision	Date	Changes
00	05/17/19	Initial Release
01	05/23/19	Updated Output & Center Frequencies,
02	07/16/19	Updated Package Height Dimensions
03	07/31/19	P/N change to add LA (low ADEV) designation and updated Pin 1 description in test circuit
04	08/07/19	Updated package height and tape dimensions.
05	09/17/19	Removed T&R Dimensions
06	10/07/20	Added p/n OH320-LA-61003CF and updated for sale at DigiKey
07	12/15/20	Added Tape & Reel Information
08	04/20/22	Added p/n OH320-LA-61003V

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