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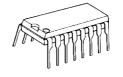


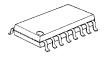
2-INPUT 3CHANNEL VIDEO SWITCH

■ GENERAL DESCRIPTION

NJM2283 is a switching IC for switching over from one audio or video input signal to another. Internalizing 2 inputs and I output, and then each set of 3 can be operated independently. It is a higher efficiency video switch, featuring the supply voltage range 4.75 to 13.0V, the frequency feature 10MHz, and then Crosstalk 75dB (at 4.43MHz).

■ PACKAGE OUTLINE





NJM2283D

NJM2283M



NJM2283V

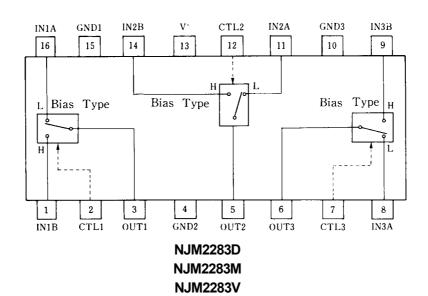
■ FEATURES

- 2 Input-1 Output 3 Circuits internalizing
- Wide Operating Voltage (4.75V to 13V)
- Crosstalk 75dB (at 4.43MHz)
- Wide Operating Supply Range 10MHz (2V_{P-P} Input)
- Wide Bandwidth Frequency
- Package Outline
 DIP16, DMP16, SSOP16

■ APPLICATIONS

• VCR, Video Camera, AV-TV, Video Disk Player.

■ BLOCK DIAGRAM



■ MAXIMUM RATINGS

 $(T_a = 25^{\circ}C)$

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V ⁺	14	V
Power Dissipation	P _D	(DIP16) 700 (DMP16) 350 (SSOP16) 300	mW mW mW
Operating Temperature Range	T _{opr}	-40 to +85	℃
Storage Temperature Range	T _{stg}	-40 to +125	°C

■ ELECTRICAL CHARACTERISTICS

 $(V^+ = 5V, T_a = 25^{\circ}C)$

PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Current (1)	I _{CC1}	V ⁺ = 5V (Note1)	8.3	11.8	15.3	mA
Operating Current (2)	I _{CC2}	V ⁺ = 9V (Note1)	10.4	14.8	19.2	mA
Voltage Gain	G_V	$V_{I} = 100kHz, 2V_{P-P}, V_{O} / V_{I}$	-0.6	-0.1	+0.4	dB
Frequency Gain	G_{F}	$V_{I} = 2V_{P-P}, V_{O} (10MHz) / V_{O} (100kHz)$	-1.0	0	+1.0	dB
Differential Gain	DG	V _I = 2V _{P-P} , Standerd Staircase Signal	-	0.3	-	%
Differential Phasa	DP	V _I = 2V _{P-P} , Standerd Staircase Signal	-	0.3	-	deg
Output offset Voltage	Vos	(Note2)	-10	0	+10	mV
Crosstalk	CT	$V_{I} = 2V_{P-P}, 4.43MHz, V_{O} / V_{I}$	-	-75	-	dB
Switch Change Over Voltage	V_{CH}	All inside Switch ON	2.5	-	-	V
Switch Change Over Voltage	V_{CL}	All inside Switch OFF	-	-	1.0	V

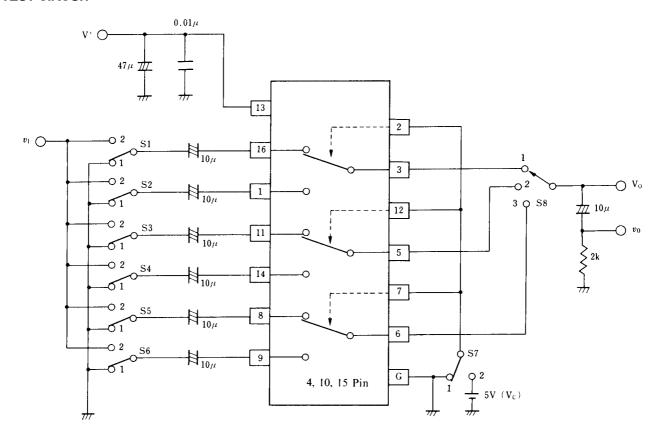
(Note1) S1 = S2 = S3 = S4 = S5 = S6 = S7 = 1

(Note2) S1 = S2 = S3 = S4 = S5 = S6 = 1, S7 = $1 \rightarrow 2$ Measure the output DC voltage difference

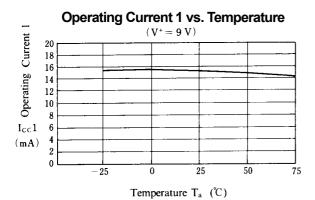
■ TERMINLAL EXPLANATION

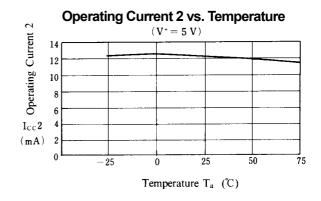
PIN No.	PIN NAME	VOLTAGE	INSIDE EQUIVALENT CIRCUIT
16 1 11 14 8 9	IN 1A IN 1B IN 2A IN 2B IN 3 A IN 3 B [Input]	2.5V	500 15k 2.5V
2 12 7	CTL 1 CTL 2 CTL 3 [Switching]		2.3V 1.9V 20k 8k
3 5 6	OUT1 OUT2 OUT3 [Output]	1.8V	O OUT
13	V ⁺	5V	
15 4 10	GND 1 GND 2 GND 3		

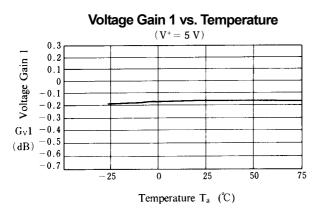
■ TEST CIRCUIT

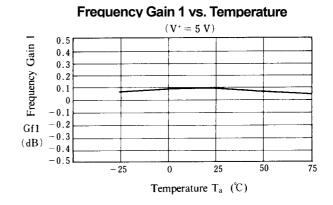


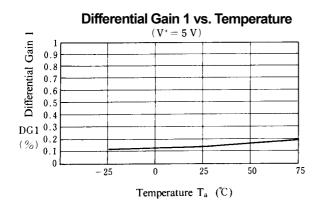
Parameter	S1	S2	S3	S4	S5	S6	S7	S8	Test Part
I _{CC1}	1	1	1	1	1	1	1	1	V ⁺
I_{CC2}	1	1	1	1	1	1	1	1	
G _{v1}	2	1	1	1	1	1	1	1	V _o
G _{f1}	2	1	1	1	1	1	1	1	
DG ₁	2	1	1	1	1	1	1	1	
DP_1	2	1	1	1	1	1	1	1	
CT 1	2	1	1	1	1	1	2	1	<i>V</i> ₀
CT 2	1	2	1	1	1	1	1	1	
CT3	1	1	2	1	1	1	2	2	
CT 4	1	1	1	2	1	1	1	2	
CT 5	1	1	1	1	2	1	2	3	
CT 6	1	1	1	1	1	2	1	3	
V _{OS1}	1	1	1	1	1	1	1/2	1	Vo
V_{C1}	1/2	2/1	1	1	1	1	Vc	1	Vc
THD	2	1	1	1	1	1	1	1	V _o

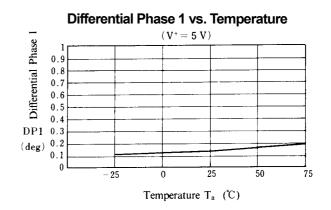




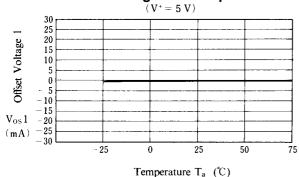




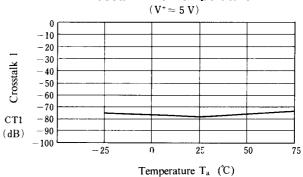




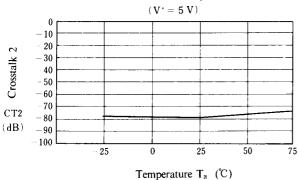




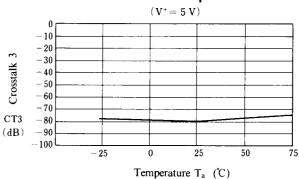
Crosstalk 1 vs. Temperature



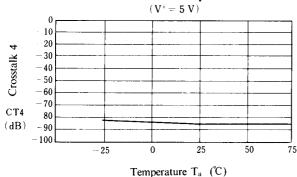
Crosstalk 2 vs. Temperature



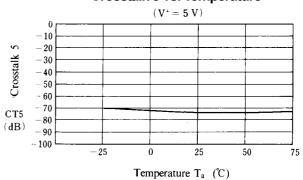
Crosstalk 3 vs. Temperature



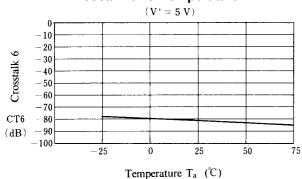
Crosstalk 4 vs. Temperature



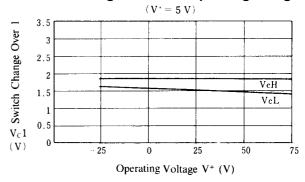
Crosstalk 5 vs. Temperature



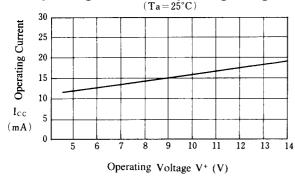
Crosstalk 6 vs. Temperature



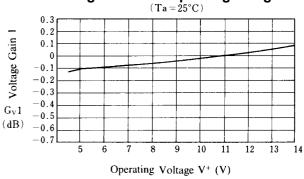
Switch Change Over 1 vs. Operating Voltage



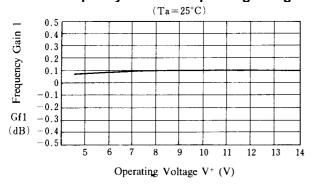
Operating Current vs. Operating Voltage



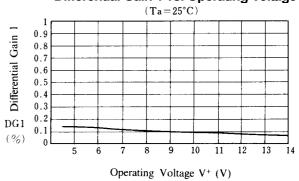
Voltage Gain 1 vs. Operating Voltage



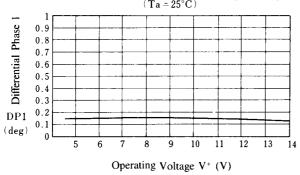
Frequency Gain 1 vs. Operating Voltage



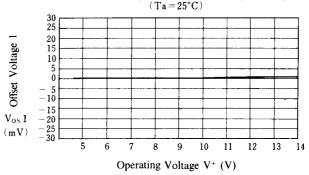
Differential Gain 1 vs. Operating Voltage



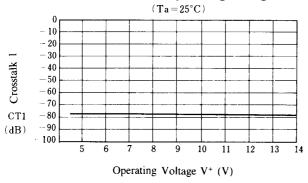
Differential Phase 1 vs. Operating Voltage $(T_a = 25^{\circ}C)$



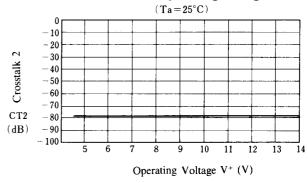
Offset Voltage 1 vs. Operating Voltage



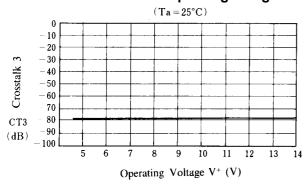
Crosstalk 1 vs. Operating Voltage



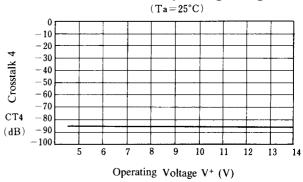
Crosstalk 2 vs. Operating Voltage



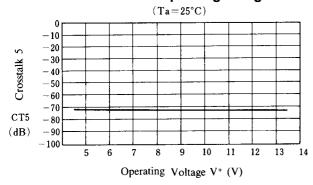
Crosstalk 3 vs. Operating Voltage



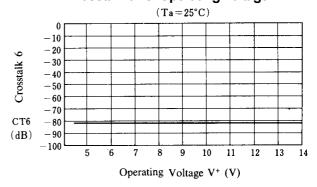
Crosstalk 4 vs. Operating Voltage



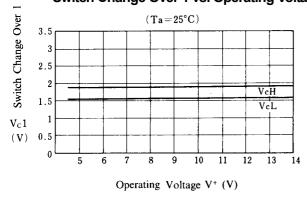
Crosstalk 5 vs. Operating Voltage



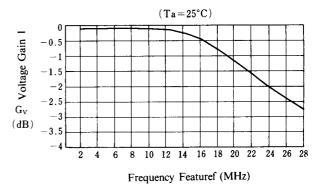
Crosstalk 6 vs. Operating Voltage



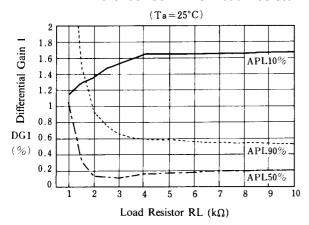
Switch Change Over 1 vs. Operating Voltage



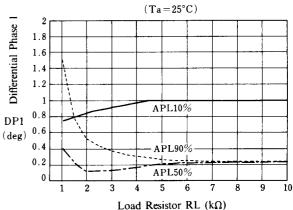
Voltage Gain 1 vs. Frequency Feature



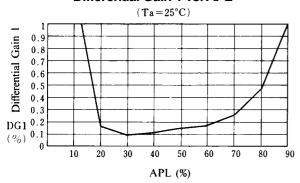
Differential Gain 1 vs. Load Resistor



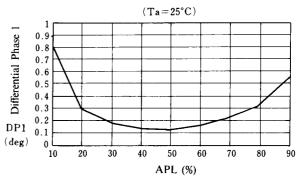
Differential Phase 1 vs. Load Resistor



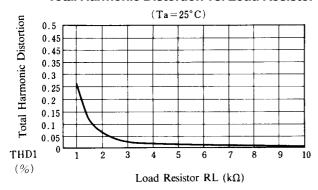
Differential Gain 1 vs. APL



Differential Phase 1 vs. APL

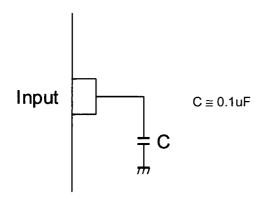


Total Harmonic Distortion vs. Load Resistor



■ APPLICATION

This IC requires 0.1µF capacitor between INPUT and GND for bias type input at mute mode.



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