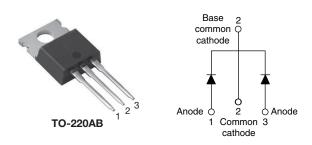


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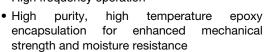
High Performance Schottky Rectifier, 2 x 15 A



PRIMARY CHARACTERISTICS							
I _{F(AV)} 2 x 15 A							
V_R	80 V						
V _F at I _F	0.67 V						
I _{RM} max.	7.0 mA at 125 °C						
T _J max.	175 °C						
E _{AS}	7.50 mJ						
Package	TO-220AB						
Circuit configuration	Common cathode						

FEATURES

- 175 °C T_J operation
- · Low forward voltage drop
- · High frequency operation





COMPLIANT HALOGEN FREE

- Guard ring for enhanced ruggedness and long term reliability
- AEC-Q101 qualified, meets JESD 201, class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL CHARACTERISTICS VALUES UI								
I _{F(AV)}	Rectangular waveform	30	Α					
V_{RRM}		80	V					
I _{FSM}	t _p = 5 μs sine	850	Α					
V _F	15 A _{pk} , T _J = 125 °C (per leg)	0.67	V					
T _J	Range	-55 to +175	°C					

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-30CTQ080HN3	UNITS				
Maximum DC reverse voltage	V_{R}	80	V				
Maximum working peak reverse voltage	V_{RWM}	80	V				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS				
Maximum average forward per device			30					
current, see fig. 5 per leg	I _{F(AV)}	50 % duty cycle at T _C = 129 °C, rectangular waveform		15	Α			
Maximum peak one cycle non-repetitive	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	850	А			
surge current per leg, see fig. 7		10 ms sine or 6 ms rect. pulse	V _{RRM} applied	275				
Non-repetitive avalanche energy per leg E_{AS} $T_{J} = 25$ °C, $I_{AS} = 0.50$ A, L		$T_J = 25 ^{\circ}\text{C}, I_{AS} = 0.50 \text{A}, L = 60 \text{C}$) mH	7.50	mJ			
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.50	Α			



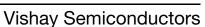
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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
		15 A	T _{.1} = 25 °C	0.86				
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	30 A	1j=25 C	1.05	V			
See fig. 1	V _{FM} ('')	15 A	T _{.1} = 125 °C	0.67				
		30 A	1j = 125 C	0.82				
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.55	mΛ			
See fig. 2		T _J = 125 °C	VR = nateu VR	7.0	mA			
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		500	pF			
Typical series inductance per leg	L _S	Measured lead to lead 5 m	8.0	nH				
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

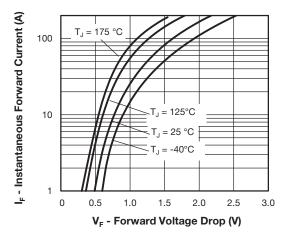
Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		-55 to +175	°C				
Maximum thermal resistance, junction to case per leg	В	DC eneration	3.25					
Maximum thermal resistance, junction to case per package	R _{th} JC	DC operation	1.63	°C/W				
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth, and greased	0.50					
Approximate weight			2	g				
Approximate weight			0.07	OZ.				
	minimum		6 (5)	kgf ⋅ cm				
Mounting torquen	naximum		12 (10)	(lbf \cdot in)				
Marking device		Case style TO-220AB	30CT0	H080Ç				









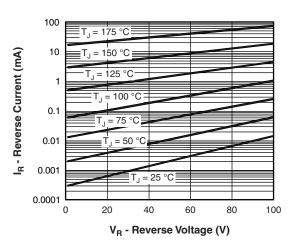


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

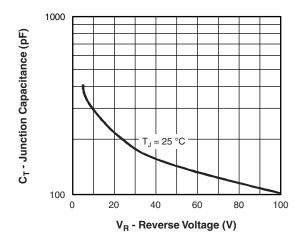


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

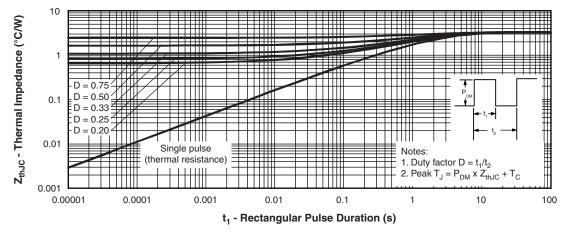


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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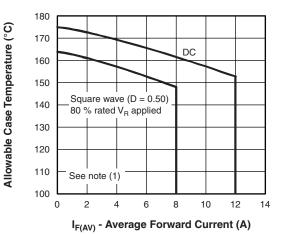


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

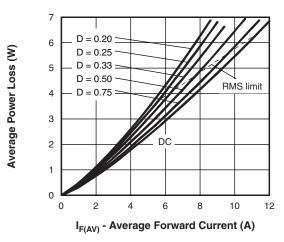


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

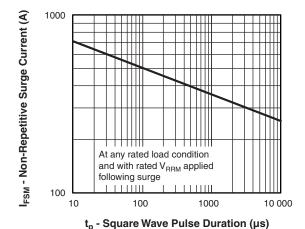


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

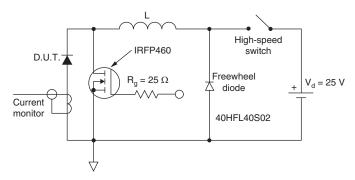


Fig. 8 - Unclamped Inductive Test Circuit

Note

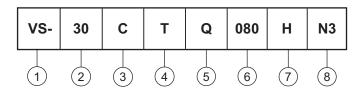
¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 10$ V



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ORDERING INFORMATION TABLE





- 1 Vishay Semiconductors product
- 2 Current rating (30 = 30 A)
- 3 Circuit configuration:

C = common cathode

4 - Package:

T = TO-220

- 5 Schottky "Q" series
- 6 Voltage ratings 80 = 80 V
- 7 H = AEC-Q101 qualified
- 8 Environmental digit
 - -N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)								
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION								
VS-30CTQ080HN3	50	1000	Antistatic plastic tube					

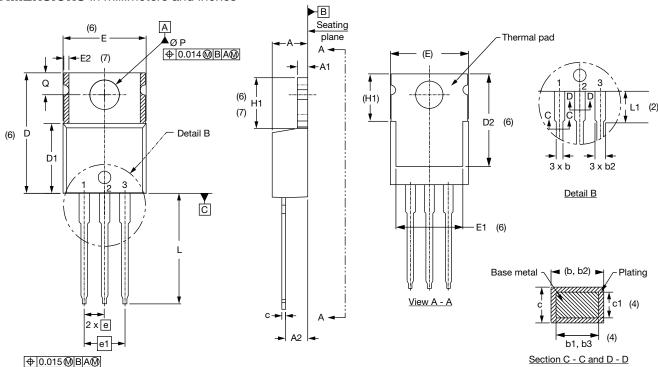
LINKS TO RELATED DOCUMENTS						
Dimensions <u>www.vishay.com/doc?95222</u>						
Part marking information	www.vishay.com/doc?95028					

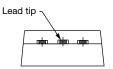


Vishay Semiconductors

TO-220AB

DIMENSIONS in millimeters and inches





Conforms to JEDEC® outline TO-220AB

SYMBOL	MILLIMETERS		INC	HES	NOTES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	12.88	0.460	0.507	6
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6
A2	2.56	2.92	0.101	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			E2	-	0.76	-	0.030	7
b1	0.38	0.97	0.015	0.038	4		е	2.41	2.67	0.095	0.105	
b2	1.20	1.73	0.047	0.068			e1	4.88	5.28	0.192	0.208	
b3	1.14	1.73	0.045	0.068	4		H1	5.84	6.86	0.230	0.270	6, 7
С	0.36	0.61	0.014	0.024			L	13.52	14.02	0.532	0.552	
c1	0.36	0.56	0.014	0.022	4		L1	3.32	3.82	0.131	0.150	2
D	14.85	15.25	0.585	0.600	3		ØΡ	3.54	3.73	0.139	0.147	
D1	8.38	9.02	0.330	0.355			Q	2.60	3.00	0.102	0.118	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC® TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



Legal Disclaimer Notice

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