

LoRaWAN Modem Module Data Sheet

MP Part Number: **CMWX1ZZABZ-093**



Revision History

Revision Code	Date	Description	Comments
Α	Dec 20, 2016	Initial Draft	
В	Feb 23, 2017	Updated pin description according to MuRata LoRa modem command specification v0.5	
С	July 3, 2017	Added reference circuit	
D	Sep 29, 2017	Updated reference circuit	
E	Nov 23, 2017	Updated Electrical Characteristics and reference circuit	







TABLE OF CONTENTS
1. Features — 4 2. Part Number — 4
2. Part Number 4
3. Block Diagram ······ 4
4. Dimensions, Marking and Terminal Configurations 5
5. Label Information ······ 8
6. Absolute Maximum Ratings ······ 9
7. Operating Condition 9
8. Electrical Characteristics ······ 9
9. Power Sequences
10. Recommend Land Pattern 12
11. Reference circuit ·······13
12. Tape and Reel packing · · · · · · · · · · · · · · · · · · ·
13. Notice



1. Features

Interfaces : UART

Main ICs : STM32L, SX1276

Reference Clocks : Integrated 32MHz clock (TCXO with frequency error=±2 ppm)

and 32.768KHz clock (frequency error=±20 ppm)

Supported Frequencies : 868 MHz, 915 MHz

Module Size : 12.5 mm x 11.6 mm x 1.76 mm (Max.)

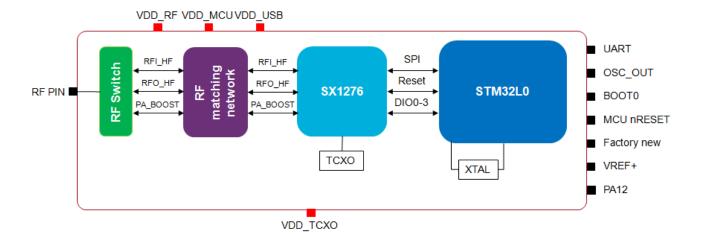
Weight : 0.48g (Typ.)
Package : Metal Shield can

RoHS : This module is compliant with the RoHS directive

2. Part Number

Ordering Part Number	Description
B-L072Z-LRWAN1	Evaluation board (owned by ST)
CMWX1ZZABZ-093	MP P/N

3. Block Diagram





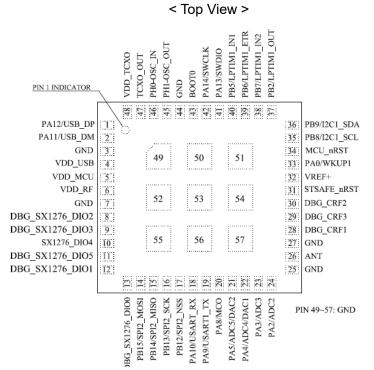


4. Dimensions, Marking and Terminal Configurations

Full datasheet with Terminal dimensions is available in my Murata. Check https://my.murata.com under the "LoRa/Sigfox Module, Type ABZ LoRa Support Site".



Terminal Configurations





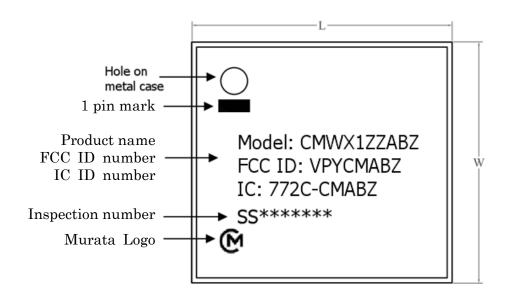
Preliminary Specification Number: SP-ABZ-093-E

7 / 20

22	PA4/ADC4/DAC1	I/O	STM32L072_PA4	Unused
23	PA3/ADC3	I/O	STM32L072_PA3	UART_RX for AT command interface
24	PA2/ADC2	I/O	STM32L072_PA2	UART_TX for AT command interface
25	GND	Ground	-	Ground
26	ANT	A,I/O	-	Transmit / Receive antenna
27	GND	Ground	-	Ground
28	DBG_CRF1	I/O	STM32L072_PA1	Unused
29	DBG_CRF3	I/O	STM32L072_PC1	Unused
30	DBG_CRF2	I/O	STM32L072_PC2	Unused
31	STSAFE_nRST	1	-	Unused
32	VREF+	Power	-	Reference Voltage for ADC and DAC
33	PA0/WKUP1	I/O	STM32L072_PA0	Input for bootloader mode. In normal work
				mode, this pin should be pulled up. If this pin is
				pulled down during power on, the module will
				enter bootloader mode.
34	MCU_nRST	1	STM32L072_ nRST	nRST
35	PB8/I2C1 SCL	I/O	STM32L072 PB8	Unused



5. Label Information





6. Absolute Maximum Ratings

Table 3 Maximum ratings

Take to Triantinatin	Table & Maximum radings								
	Parameters	Min	Тур	Max	Unit				
Storage Temperatu	re	-40	25	+90	degC				
Input RF Level	nput RF Level		-	10	dBm				
Supply Voltage	VDD_USB	-0.3	-	3.9	V				
	VDD_MCU, VDD_RF, VDD_TCXO	-0.3	-	3.9	V				
	VREF+	-0.3	-	V _{DD_MCU} +0.4	V				

7. Operating Condition

Table 4 Operating specification

Table 4 Operating openingation							
	Parameters	Min	Тур	Max	Unit		
Operating Temperat	ture	-40	25	+85	degC		
	VDD_USB (USB peripheral used) (1)	3.0	-	3.6	V		
Supply Voltage	VDD_USB(USB peripheral not used) (1)	V _{DD_MCU_min}	$V_{\text{DD_MCU}}$	$V_{DD_MCU_max}$	V		
Supply voltage	VDD_MCU,VDD_RF,VDD_TCXO	2.2 ⁽³⁾	-	3.6	V		
	VREF+ ⁽²⁾	1.8	-	VDD_MCU	V		

- (1) VDD_USB must respect the following conditions:

 - When VDD_MCU is powered on (VDD_MCU < VDD_MCU_min), VDD_USB should be always lower than VDD_MCU.
 When VDD_MCU is powered down (VDD_MCU < VDD_MCU_min), VDD_USB should be always lower than VDD_MCU.
 - In operating mode, VDD_USB could be lower or higher than VDD MCU.
 - If the USB is not used, VDD USB must be tied to VDD MCU to be able to use PA11 and PA12 as standard I/Os.
- (2) VREF+ is used to ensure a better accuracy on low-voltage inputs and outputs of ADC and DAC. Detailed information is on the STM32L072*** datasheet and user guider.
- (3) When module is on +20dBm operation, the supply of the voltage should be set from 2.4V to 3.6V.

8. Electrical Characteristics

8.1 FSK/OOK Transceiver Specification

Supply voltage VDD=3.3 V, temperature = 25 °C, FXOSC = 32 MHz, FRF =868/915 MHz, 2-level FSK modulation without pre-filtering, FDA = 5 kHz, Bit Rate = 4.8 kb/s and terminated in a matched 50 Ohm impedance, shared Rx and TX path matching, unless otherwise specified.

FSK/OOK Receiver Specification

Symbol	Description	Conditions	Min	Тур	Max	Unit
RFS_F_HF	LnaBoost is turned on	FDA = 5 kHz, BR = 4.8 kb/s		-117.5		dBm

FSK/OOK Transmitter Specification

Symbol	Description	Conditions		Min	Тур	Max	Unit
DE OD	RF output power in 50 ohms				14		dBm
RF_OP	on RFO pin (High efficiency PA)	steps	Min		-5		dBm
	RF output power in 50 ohms	Programmable with			18.5		dBm
RF_OPH	PA) on PA_BOOST pin(Regulated 1dB steps	Min		2		dBm	
ΔRF_ OPH_V	RF output power stability on PA_BOOST pin versus voltage supply.	VDD = 2.2 V to 3.6 V			+/-1		dB
ΔRF_T	RF output power stability versus temperature on PA_BOOST pin.	From T = -40 °C to +85 °C			+/-1.5		dB
Supply current in Transmit		RFOP setting = 20 dBm PA_BOOST	i, on		125		mA
IDDT	mode with impedance matching	RFOP setting = 17 dBm PA_BOOST	i, on		101		mA



Preliminary Specification Number: SP-ABZ-093-E

10 / 20

RFOP setting = 14 dBm, on RFO_HF pin	46	mA
RFOP setting = 7 dBm, on RFO_HF pin	34	mA

8.2 LoRa Transceiver Specification

Conditions:

The table below gives the electrical specifications for the transceiver operating with LoRaTM modulation. Following conditions apply unless otherwise specified: Supply voltage = 3.3 V, Temperature = 25° C, FXOSC = 32 MHz, Error Correction Code (EC) = 4/5, Packet Error Rate (PER)= 1%, CRC on payload enabled, Payload length = 10 bytes. With matched impedances

LoRa Receiver Specification

Symbol	Description	Conditions	Min	Тур	Max	Unit
IDDD I	O	868 band		12.5		mΑ
IDDR_L	Current in receiver	915 band		14.5		mΑ
		SF = 7		-122.5		dBm
	RF sensitivity, Long-Range	SF = 8		-125.5		dBm
DEC 1425 HE	RFS_L125_HF Mode, LnaBoost for Band1, using split Rx/Tx path 125 kHz bandwidth	SF = 9		-128.5		dBm
KF3_L125_HF		SF = 10		-131.0		dBm
		SF = 11		-133.5		dBm
		SF = 12		-135.5		dBm
	RF sensitivity Long-Range	SF = 7		-119.0		dBm
		SF = 8		-122.0		dBm
DEC 1350 HE	Mode, LnaBoost for Band1,	SF = 9		-125.0		dBm
KFS_L250_HF	125 kHz bandwidth RF sensitivity, Long-Range	SF = 10		-127.5		dBm
		SF = 11		-130.0		dBm
		SF = 12		-133.0		dBm

LoRa Transmitter Specification

Symbol	Description	Conditions	Min	Тур	Max	Unit
IDDT_L	Supply current in transmitter	RFOP setting = 14 dBm		36		mΑ
	mode	RFOP setting = 10 dBm		27.5		mΑ
IDDT_H_L	Supply current in transmitter mode	Using PA_BOOST pin RFOP setting = 20 dBm		118		mA

8.3 Low power mode current

Conditions:

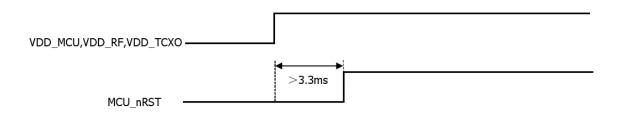
Power supply: 3.3V, Temp: Room,

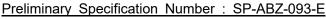
Description	Min	Тур	Max	Unit
STM32L0 in stop mode with RTC (Real Time Clock)		1.65		uA
SX1276 in sleep mode		1.05		uA



9. Power Sequences

9.1 Power Up Sequence









10. Recommend Land Pattern

Full datasheet with recommended land pattern is available in my Murata. Check https://my.murata.com under the "LoRa/Sigfox Module, Type ABZ LoRa Support Site".

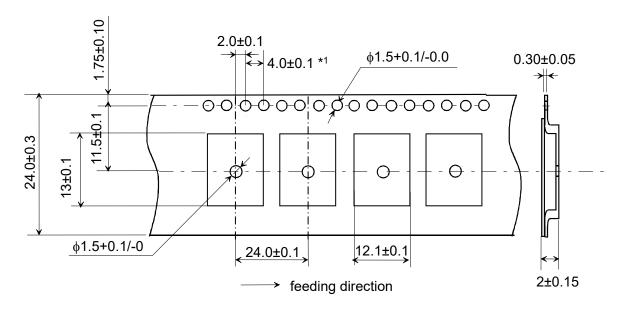


11. Reference circuit The VDD_USB is must.Because IL IS the power supply for PA11 and PA12. $\mbox{Pin} 14$ is as an input of factory new. For the detail information, please refer specification. to the Murata LoRa modem command CMWX1ZZABZ-093 9 5 5 5 5 DBG_SX1276_DISO PB14/SPI2_NISO PB14/ 53 52 51 50 49 13 14 15 16 17 18 19 20 21 22 23 24 VDD TCXO TCXO_OUT PH0-OSC_IN PH1-OSC_OUT GND BOOTO PA14/SWCLK/LPUART1_TX PA13/SWDIO/LPUART1_RX PB5/LPTIM1_IN1 RX PB6/LPTIM1_IN1 PB2/LPTIM1_IN1 PB2/LPTIM1_IN1 PB2/LPTIM1_OUT 48 47 46 45 44 43 42 41 40 39 38 37 ² C7 ᆘ ł§ Button3 282828232888 Module reset do not need external pull up resistor. Because there is an about R 2 45K permanent pull up resistor inside of module. SWD connector ADD. C1 0.1uF 118 SMA connector P łě 10 R3 C2 0.1uF 🖶 ABD Notes for VREF+: 1. VREF+ should be connected to or floating if module is powere external power supply. 2. VREF+ should be connected to if module is battery powered ar Pin 33 is as an input of bootload mod selection For the detail information, command specification. please refer to the Murata LoRa modem reports battery level powered bootload mode and to to VDD GND d by



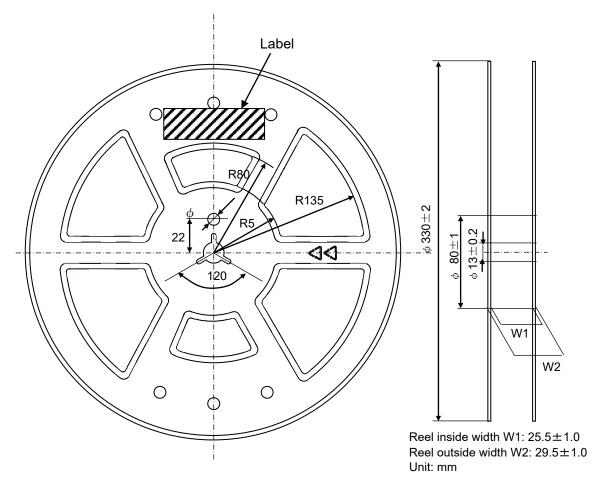
12. Tape and Reel packing

12.1 Dimension of Tape (Plastic tape)



(unit: mm)

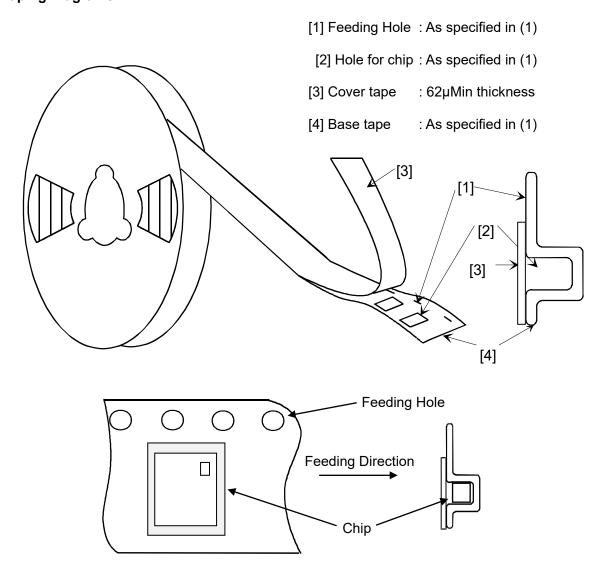
12.2 Dimensions of Reel



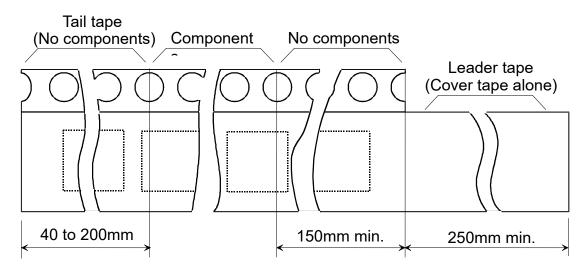
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12.3 Taping Diagrams



12.4 Leader and Tail tape

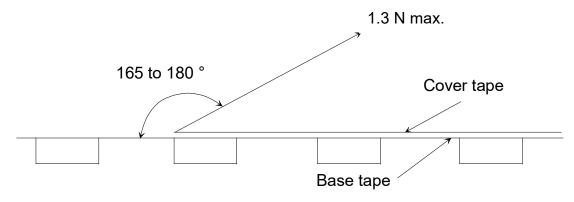


Feeding direction

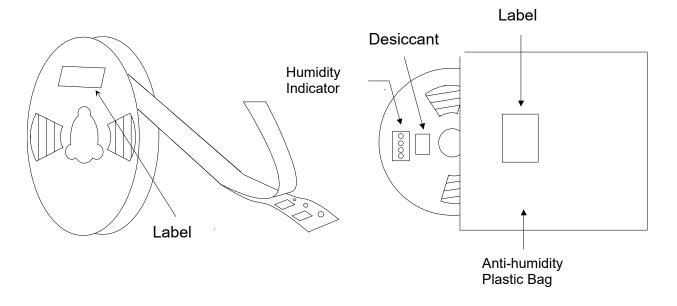
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- The tape for chips are wound clockwise, the feeding holes to the right side as the tape is pulled toward the user.
- The cover tape and base tape are not adhered at no components area for 250mm Min.
- Tear off strength against pulling of cover tape : 5N Min.
- Packaging unit: 1000 pcs/ reel
- Material
 - Base tape : PlasticReel : Plastic
 - Cover tape, cavity tape and reel are made the anti-static processing.
- Peeling of force: 1.3N max. in the direction of peeling as shown below.



- Packaging (HumidiTyproof Packing)



Tape and reel must be sealed with the anti-humidiTyplastic bag. The bag contains the desiccant and the humidity indicator.

17 / 20



13. Notice

13.1 Storage Conditions

Please use this product within 6month after receipt.

- The product shall be stored without opening the packing under the ambient temperature from 5 to $35~^{\circ}\text{C}$ and humidity from $20~^{\sim}70~^{\circ}\text{RH}$.

(Packing materials, in particular, may be deformed at the temperature over 40 °C)

- The product left more than 6months after reception, it needs to be confirmed the solderbility before used.
- The product shall be stored in non corrosive gas (CI2, NH3, SO2, Nox, etc.).
- Any excess mechanical shock including, but not limited to, sticking the packing materials by sharp object and dropping the product, shall not be applied in order not to damage the packing materials.

This product is applicable to MSL3 (Based on IPC/JEDEC J-STD-020)

- After the packing opened, the product shall be stored at <30 °C / <60 %RH and the product shall be used within 168 hours.
- When the color of the indicator in the packing changed, the product shall be baked before soldering.

Baking condition: 125 +5/-0 °C, 24 hours, 1 time

The products shall be baked on the heat-resistant tray because the material (Base Tape, Reel Tape and Cover Tape) are not heat-resistant.

13.2 Handling Conditions

Be careful in handling or transporting products because excessive stress or mechanical shock may break products.

Handle with care if products may have cracks or damages on their terMinals, the characteristics of products may change. Do not touch products with bear hands that may result in poor solderability.

13.3 Standard PCB Design (Land Pattern and Dimensions)

All the ground terMinals should be connected to the ground patterns. Furthermore, the ground pattern should be provided between IN and OUT terMinals. Please refer to the specifications for the standard land dimensions.

The recommended land pattern and dimensions is as Murata's standard. The characteristics of products may vary depending on the pattern drawing method, grounding method, land dimensions, land forMing method of the NC terMinals and the PCB material and thickness. Therefore, be sure to verify the characteristics in the actual set. When using non-standard lands, contact Murata beforehand.

13.4 Notice for Chip Placer:

When placing products on the PCB, products may be stressed and broken by uneven forces from a worn-out chucking locating claw or a suction nozzle. To prevent products from damages, be sure to follow the specifications for the maintenance of the chip placer being used. For the positioning of products on the PCB, be aware that mechanical chucking may damage products.

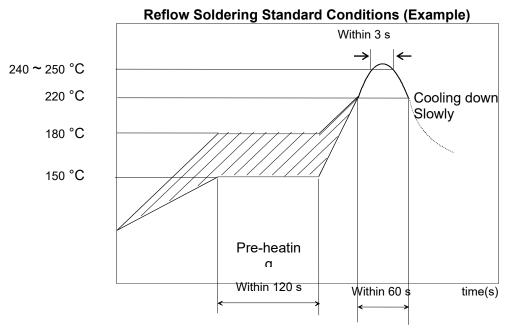
13.5 Soldering Conditions:

The recommendation conditions of soldering are as in the following figure.

When products are immersed in solvent after mounting, pay special attention to maintain the temperature difference within 100 °C. Soldering must be carried out by the above mentioned conditions to prevent products from damage. Set up the highest temperature of reflow within 260 °C.

Contact Murata before use if concerning other soldering conditions.





Please use the reflow within 2 times.

Use rosin Type flux or weakly active flux with a chlorine content of 0.2 wt % or less.

13.6 Cleaning:

Since this Product is Moisture Sensitive, any cleaning is not permitted.

13.7 Operational Environment Conditions:

Products are designed to work for electronic products under normal environmental conditions (ambient temperature, humidity and pressure). Therefore, products have no problems to be used under the similar conditions to the above-mentioned. However, if products are used under the following circumstances, it may damage products and leakage of electricity and abnormal temperature may occur.

- In an atmosphere containing corrosive gas (CI2, NH3, SOx, NOx etc.).
- In an atmosphere containing combustible and volatile gases.
- DusTyplace.
- Direct sunlight place.
- Water splashing place.
- Humid place where water condenses.
- Freezing place.

If there are possibilities for products to be used under the preceding clause, consult with Murata before actual use.

As it might be a cause of degradation or destruction to apply static electricity to products, do not apply static electricity or excessive voltage while assembling and measuring.

13.8 Input Power Capacity:

Products shall be used in the input power capacity as specified in this specifications. Inform Murata beforehand, in case that the components are used beyond such input power capacity range.





PLEASE READ THIS NOTICE BEFORE USING OUR PRODUCTS.

Please make sure that your product has been evaluated and confirmed from the aspect of the fitness for the specifications of our product when our product is mounted to your product.

All the items and parameters in this product specification/datasheet/catalog have been prescribed on the premise that our product is used for the purpose, under the condition and in the environment specified in this specification. You are requested not to use our product deviating from the condition and the environment specified in this specification.

Please note that the only warranty that we provide regarding the products is its conformance to the specifications provided herein. Accordingly, we shall not be responsible for any defects in products or equipment incorporating such products, which are caused under the conditions other than those specified in this specification.

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The product shall not be used in any application listed below which requires especially high reliability for the prevention of such defect as may directly cause damage to the third party's life, body or property. You acknowledge and agree that, if you use our products in such applications, we will not be responsible for any failure to meet such requirements.

Furthermore, YOU AGREE TO INDEMNIFY AND DEFEND US AND OUR AFFILIATES AGAINST ALL CLAIMS, DAMAGES, COSTS, AND EXPENSES THAT MAY BE INCURRED, INCLUDING WITHOUT LIMITATION, ATTORNEY FEES AND COSTS, DUE TO THE USE OF OUR PRODUCTS IN SUCH APPLICATIONS.

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- Aerospace equipment
- Undersea equipment.
- Power plant control equipment
- Medical equipment.
- Transportation equipment (vehicles, trains, ships, elevator, etc.).
- Traffic signal equipment.
- Disaster prevention / crime prevention equipment.
- Burning / explosion control equipment
- Application of similar complexity and/ or reliability requirements to the applications listed in the above.

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Please do not use our products, our technical information and other data provided by us for the purpose of developing of mass-destruction weapons and the purpose of military use. Moreover, you must comply with "foreign exchange and foreign trade law", the "U.S. export adMinistration regulations", etc.

Please note that we may discontinue the manufacture of our products, due to reasons such as end of supply of materials and/or components from our suppliers.

Customer acknowledges that Murata will, if requested by you, conduct a failure analysis for defect or alleged defect of Products only at the level required for consumer grade Products, and thus such analysis may not always be available or be in accordance with your request (for example, in cases where the defect was caused by components in Products supplied to Murata from a third party).

By signing on specification sheet or approval sheet, you acknowledge that you are the legal representative for your company and that you understand and accept the validity of the contents herein. When you are not able to return the signed version of specification sheet or approval sheet within 90 days from receiving date of specification sheet or approval sheet, it shall be deemed to be your consent



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on the content of specification sheet or approval sheet.

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 deviation or lapse in function of engineering sample,

• improper use of engineering samples.
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