



## Low-Voltage, Low R<sub>ON</sub>, SPDT Audio Switch with Negative Swing Capability

### **DESCRIPTION**

The DG2612, DG2613 is a low on-resistance, single-pole/double-throw monolithic CMOS analog switch with negative signal swing capability. It is designed for low voltage applications. The DG2612, DG2613 is ideal for portable and battery powered equipment, requiring high performance and efficient use of board space. In additional to the low on-resistance (1.0  $\Omega$  at 2.7 V), the DG2613 has a typical off isolation and crosstalk of - 67 dB and - 73 dB respectively.

The DG2612, DG2613 is built on Vishay Siliconix's low voltage process.

Break-before-make is guaranteed.

As a committed partner to the community and the environment, Vishay Siliconix manufactures this product with the lead (Pb)-free device terminations. For analog switching products manufactured with 100 % matte tin device terminations, the lead (Pb)-free "-E3" suffix is being used as a designator.

### **FEATURES**

- Low voltage operation (1.8 V to 5.5 V)
- Low on-resistance  $R_{ON}$ : 1.0  $\Omega$  at 2.7 V
- High bandwidth



ROHS

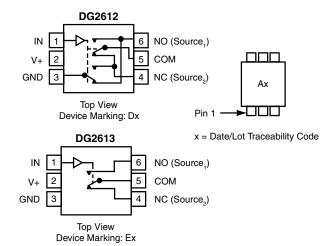
### **BENEFITS**

- · Negative signal swing capability
- Shunt switch to eliminate switching noise
- Simplified design with direct DC coupling
- Space saving SC-89 package

### **APPLICATIONS**

- Cellular phones
- · Portable multimedia players
- · PDAs and hand-held devices
- · Laptop computers

### **FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION**



TRUTH TABLE					
Logic	NC	NO			
0	ON	OFF			
1	OFF	ON			

COMMERCIAL ORDERING INFORMATION						
Temp Range	Package	Part Number				
- 40 °C to 85 °C	SC-89 (SOT-666) Lead (Pb)-free with Tape and Reel	DG2612DX-T1-E3 DG2613DX-T1-E3				

<b>ABSOLUTE MAXIMUM RATINGS</b> T <sub>A</sub> = 25 °C, unless otherwise noted						
Parameter		Symbol	Limit	Unit		
	V+		- 0.3 to + 6			
Reference GND	IN <sup>a</sup>		- 0.3 to (V+ + 0.3)	V		
	COM, NC, NO <sup>a</sup>	COM, NC, NO <sup>a</sup>				
Continuous Current (NO, NC, COM pins)			± 150			
Peak Current (Pulsed at 1 ms, 10 % duty cycle)		± 300		- mA		
Storage Temperature	rature D Suffix - 65 to 150 °C		°C			
Power Dissipation (Packages) <sup>b</sup>	SC-89 <sup>c</sup>		172	mW		

#### Notes

- a. Signals on NC, NO, or COM or IN exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.
- b. All leads welded or soldered to PC board.
- c. Derate 2.15 mW/°C above 70 °C.

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SPECIFICATIONS (V+	1	Test Conditions		1	Limits		
		Otherwise Unless Specified		- 40	°C to 85	s °C	
Parameter	Symbol	V+ = 3 V, ± 10 %,V <sub>IN</sub> = 0.5 V or 1.4 V <sup>e</sup>	Temp.a	Min.b	Typ.c	Max.b	Unit
Analog Switch						L	
Analog Signal Range <sup>d</sup>	$V_{NO}, V_{NC}, V_{COM}$		Full	V+ - 5.5		V+	V
On-Resistance	R <sub>ON</sub>		Room Full		1.0	1.4 1.6	
R <sub>ON</sub> Match <sup>d</sup>	$\Delta R_{ON}$	$V+ = 2.7 \text{ V}, V_{COM} = -1 \text{ V/0 V/1 V/2 V}$ $I_{NO}, I_{NC} = 10 \text{ mA}$	Room			0.1	Ω
R <sub>ON</sub> Flatness <sup>d</sup>	R <sub>ON</sub> Flatness	NO, INC = 10 mm	Room			0.3	
Shunt Switch Resistance	R <sub>SH</sub>	$I_{NO}$ or $I_{NC}$ = 10 mA, V+ = 2.7 V, DG2612 only	Full		150	300	Ω
Switch Off Leakage Current	I <sub>NO(off)</sub> I <sub>NC(off)</sub>	V+ = 3.3 V,	Room Full	- 2 - 100		2 100	
Switch on Educage Junetic	I <sub>COM(off)</sub>	$V_{NO}$ , $V_{NC} = 1 \text{ V/3 V}$ , $V_{COM} = 3 \text{ V/1 V}$	Room Full	- 2 - 100		2 100	nA
Channel-On Leakage Current	I <sub>COM(on)</sub>	$V+ = 3.3 \text{ V}, V_{NO}, V_{NC} = V_{COM} = 1 \text{ V/3 V}$	Room Full	- 2 - 100		2 100	
Digital Control							
		V+ = 1.8 V to 2.0 V		1.0			V
Input High Voltage	$V_{INH}$	V+ = 2.7 V to 3.6 V		1.4			
		V+ = 4.2 V to 5.5 V	Full	2.0			
Input Low Voltage		V+ = 1.8 V to 2.0 V	ı uıı			0.4	
	$V_{INL}$	V+ = 2.7 V to 3.6 V				0.5	
		V+ = 4.2 V to 5.5 V				0.8	
Input Capacitance	C <sub>in</sub>		Full		5		pF
Input Current	I <sub>INL</sub> or I <sub>INH</sub>	$V_{IN} = 0$ or $V+$	Full	1		1	μΑ
Dynamic Characteristics							
Turn-On Time	t <sub>ON</sub>		Room Full		34	60 63	
Turn-Off Time	t <sub>OFF</sub>	$V_{NO}$ or $V_{NC} = 1.5 \text{ V}$ , $R_L = 50 \Omega$ , $C_L = 35 \text{ pF}$	Room Full		10	35 37	ns
Break-Before-Make Time	t <sub>BBM</sub>		Room	4	16		
Charge Injection <sup>d</sup> (DG2613)	Q <sub>INJ</sub>	$C_L = 1 \text{ nF, } V_{GEN} = 0 \text{ V, } R_{GEN} = 0 \Omega$	Room		2.4		рС
Off-Isolation <sup>d</sup>	OIRR	$R_L = 50 \Omega$ , $C_L = 5 pF$ , $f = 100 kHz$	Room		- 61		dB
Crosstalk <sup>d</sup>	X <sub>TALK</sub>	DG2612	Room		- 67		uБ
Off-Isolation <sup>d</sup>	OIRR	$R_L = 50 \Omega$ , $C_L = 5 pF$ , $f = 100 kHz$	Room		- 67		٩D
Crosstalk <sup>d</sup>	X <sub>TALK</sub>	DG2613	Room		- 73		dB
N <sub>O</sub> , N <sub>C</sub> Off Capacitance <sup>d</sup>	$C_{NO(off)} $ $C_{NC(off)}$	V <sub>IN</sub> = 0 or V+, f = 1 MHz	Room		36		pF
Channel-On Capacitance <sup>d</sup>	C <sub>ON</sub>		Room		95		
Power Supply							
Power Supply Range	V+			1.8		5.5	V
Power Supply Current	I+	$V_{IN} = 0 \text{ or } V+$			0.01	1.0	μΑ

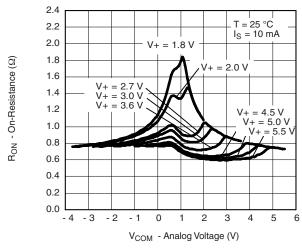
### Notes:

- a. Room = 25 °C, Full = as determined by the operating suffix.
- b. Typical values are for design aid only, not guaranteed nor subject to production testing.
- c. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
- d. Guarantee by design, nor subjected to production test.
- e.  $V_{IN}$  = input voltage to perform proper function.

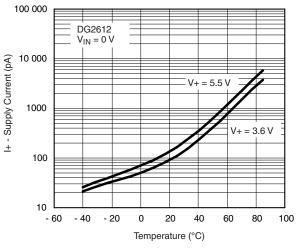
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



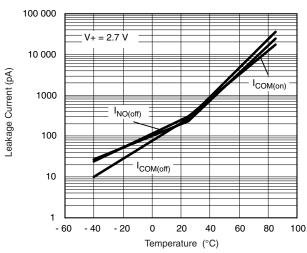
### **TYPICAL CHARACTERISTICS** $T_A = 25$ °C, unless otherwise noted



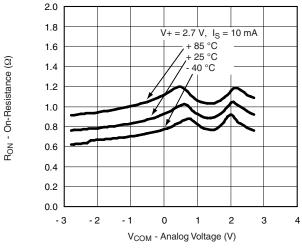
R<sub>ON</sub> vs. V<sub>COM</sub> and Supply Voltage



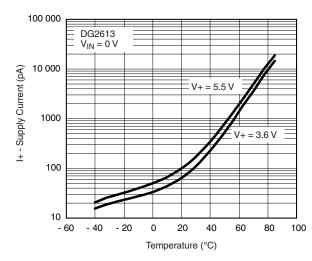
**Supply Current vs. Temperature** 



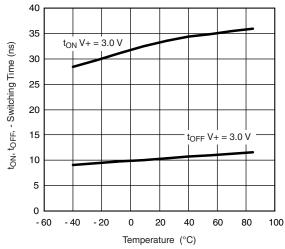
Leakage Current vs. Temperature



R<sub>ON</sub> vs. Analog Voltage and Temperature



Supply Current vs. Temperature

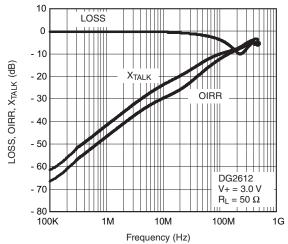


Switching Time vs. Temperature and Supply Voltage

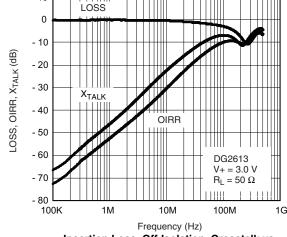
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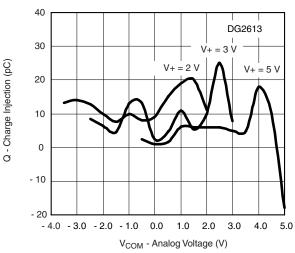
## **TYPICAL CHARACTERISTICS** $T_A = 25$ °C, unless otherwise noted



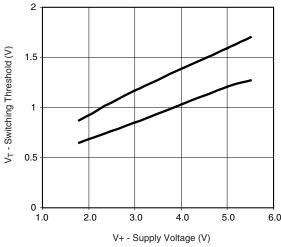
Insertion Loss, Off-Isolation, Crosstalk vs. Frequency



Insertion Loss, Off-Isolation, Crosstalk vs.
Frequency

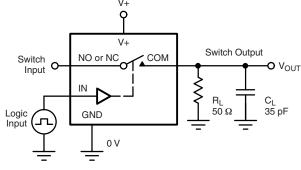


Charge Injection vs. Analog Voltage



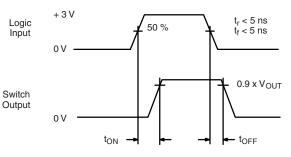
Switching Threshold vs. Supply Voltage

### **TEST CIRCUITS**



C<sub>L</sub> (includes fixture and stray capacitance)

$$V_{OUT} = V_{COM} \left( \frac{R_L}{R_L + R_{ON}} \right)$$



Logic "1" = Switch On Logic input waveforms inverted for switches that have the opposite logic sense.

Figure 1. Switching Time



### **TEST CIRCUITS**

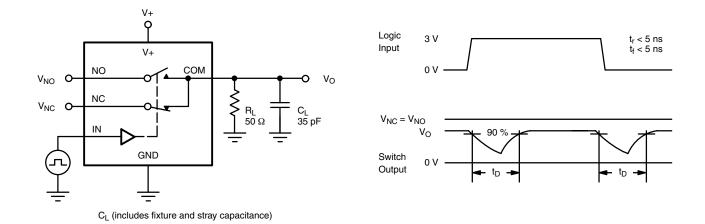


Figure 2. Break-Before-Make Interval

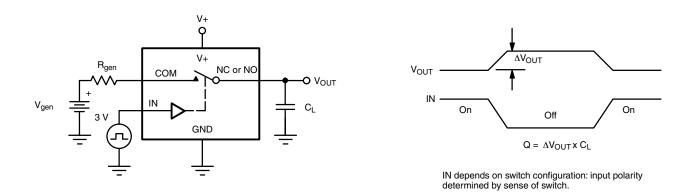


Figure 3. Charge Injection

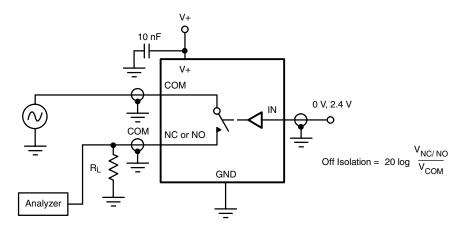


Figure 4. Off-Isolation

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## TEST CIRCUITS



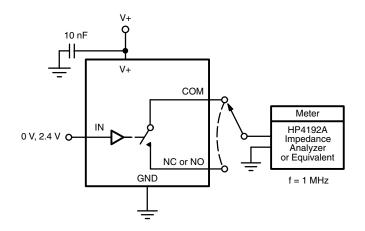
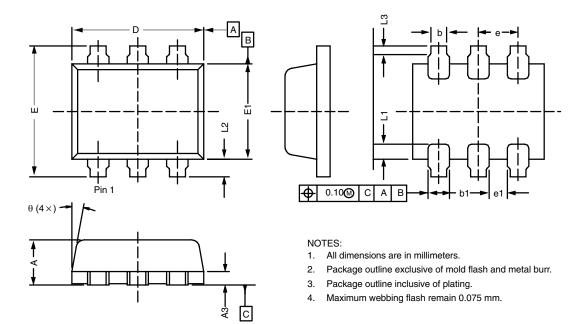


Figure 5. Channel Off/On Capacitance

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?74339">www.vishay.com/ppg?74339</a>.



## SC-89: 6-LEAD (SOT-666)



	МІІ	LLIMETE	RS*	INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.56	_	0.60	0.022	_	0.024	
А3	0.13	0.17	0.18	0.005	0.006	0.007	
b	0.17	-	0.25	0.006	_	0.010	
b1	-	0.27	0.34	-	0.011	0.013	
D	1.50	1.66	1.70	0.059	0.065	0.067	
E	1.50	1.65	1.70	0.059	0.065	0.067	
E1	1.10	1.20	1.30	0.043	0.047	0.051	
е	0.50 BSC			0.020 BSC			
e <sub>1</sub>	0.20	_	_	0.008	_	_	
L1	0.11	0.19	0.26	0.004	0.007	0.010	
L2	0.10	0.23	0.30	0.004	0.009	0.012	
L3	0.05	0.10	_	0.002	0.004	_	
θ	8°	10°	12°	8°	10°	12°	
ECN: S-52444—Rev. D, 28-Nov-05							

<sup>\*</sup>Use millimeters as the primary measurement

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