## onsemi

## Product Bulletin Document #:PB24498X Issue Date:13 Jan 2022

Title of Change:	AR1820HS Datasheet Update				
Effective date:	13 Jan 2022				
Contact information:	Contact your local onsemi Sales Office or Sonya.Yip@onsemi.com				
Type of notification:	This Product Bulletin is for notification purposes only. onsemi will proceed with implementation of this change upon publication of this Product Bulletin.				
Change Category:	Documentation				
Change Sub-Category(s):	Datasheet/Product Doc change				
Sites Affected:					
onsemi Sites		External Foundry/Subcon Sites			
None		None			
Description and Purpose: Updated "Minimum Row Time"	' section in AR1820HS Datasheet.				
Updated "Minimum Row Time"	' section in AR1820HS Datasheet.				
Updated "Minimum Row Time" Old Section: MINIMUM ROW TIME Enough time must be give output all data at the set free There are therefore three co programming line_length_p • Shutter Limited: 4188 pcl • Readout Limited for 10-to (x_output_size +160+4(0) • Dutput Limited: (4*(x_output_size))	en to the output FIFO so it can quency within one row time. thecks that must all be met when ock: ks bit: line_length_pck ≥ (x005E)) utput_size/Interface)+ t/op_pix_clkfreq_mhz)	<ul> <li>New Section:</li> <li>MINIMUM ROW TIME Enough time must be given to the output FIFO so it can output all data at the set frequency within one row time. There are therefore three checks that must all be met when programming line length_pck: <ul> <li>Shutter Limited 4632 pcks (12-bits)</li> <li>Readout Limited for 10-bit: line length_pck ≥ (x output size +160, 4(0x0064))</li> <li>Output Limited for HiSPi:</li> <li>4((x_output_size/Interface)+41)(clk_pixel_freq_mhz/o p_pix_clk_freq_mhz)</li> <li>Output Limited for MIPI:</li> <li>4((x_output_size/Interface)+154)(clk_pixel_freq_mhz/o p_pix_clk_freq_mhz)</li> </ul></li></ul>			
Updated "Minimum Row Time" Old Section: MINIMUM ROW TIME Enough time must be give output all data at the set free There are therefore three of programming line_length_p • Shutter Limited: 4188 pcl • Readout Limited for 10-t (x_output_size +160+4(0) • Dutput Limited: (4*(x_output_size))	en to the output FIFO so it can quency within one row time. thecks that must all be met when ock: ks bit: line_length_pck ≥ (x005E)) utput_size/Interface)+ t/op_pix_clkfreq_mhz)	<ul> <li>MINIMUM ROW TIME Enough time must be given to the output FIFO so it can output all data at the set frequency within one row time. There are therefore three checks that must all be met when programming line_length_pck: <ul> <li>Shutter Limited 4632 pcks (12-bits)</li> <li>Readout Limited for 10-bit: line_length_pck ≥ (x output size +160, 4(0x0064))</li> <li>Output Limited for HiSPi:</li> <li>4((x_output_size/Interface)+41)(clk_pixel_freq_mhz/o p_pix_clk_freq_mhz)</li> <li>Output Limited for MIPI:</li> <li>4((x_output_size/Interface)+134)(clk_pixel_freq_mhz/</li> </ul></li></ul>			

## List of Affected Standard Parts:

**Note**: Only the standard (off the shelf) part numbers are listed in the parts list. Any custom parts affected by this PCN are shown in the customer specific PCN addendum in the PCN email notification, or on the **PCN Customized Portal**.

AR1820HSSC12SHEA0-DP1	AR1820HSSC12SHEA0-DP	AR1820HSSC00SHEA0-DP1
AR1820HSSC12SHEA0-DR1	AR1820HSSC00SHEA0-DR	AR1820HSSC12SHEA0-DR
AR1820HSSC12SMD20		



## Appendix A: Changed Products

Product	Customer Part Number	Qualification Vehicle	New Part Number	Replacement Supplier
AR1820HSSC00SHEA0-DP1		NA		