

1 Description

The iW3690 is a single-stage, high performance AC/DC off-line power supply controller for intelligent LED luminaires. A master controller can communicate with the iW3690 through the I²C bus interface to control the LED driver output current. With advanced digital control, the iW3690 regulates a constant current to an LED load over a 1% to 100% dimming range without flicker or shimmer. The driver can support either wired or wireless links via the master controller. Moreover, the master digital controller can set the iW3690 to sleep mode to achieve remote light off function at low standby power.

The iW3690 uses Dialog's proprietary Dual-Dim technology, which enables intelligent LED luminaires that are compatible with wall dimmers while offering superior performance. When a wall dimmer only is used, the LED driver output current is controlled based on the phase conduction angle of the dimmer. This same information is used to set the high limit of the LED current when a digital dimming command is received from the I²C bus. While the I²C bus has priority over the wall dimmer, the iW3690 can respond to both dimmer interfaces at the same time, allowing the driver to enter a stable sleep mode state at any wall phase conduction angle. Dialog's Dual-Dim technology allows the light output to be dimmed to 1% without flicker.

The iW3690 offers all the advanced features of Dialog's industry leading phase-cut dimming technology, including high efficiency and superior dimmer compatibility at low BOM cost. The iW3690 offers high performance, a wide dimming range and excellent dimmer compatibility while minimizing the external component count through high system-level integration. Dialog's EZ-EMI[®] technology works to simplify the input EMI filter, further reducing circuit size and cost while providing excellent noise rejection capability to AC line distortions.

2 Features

- Isolated/non-isolated off-line 120V_{AC}/230V_{AC} intelligent LED driver up to 25W output power
- I²C digital link dimmable and phase-cut (TRIAC) dimmable (Dual-Dim)
 - » Digital link dimming at any phase-cut conduction angle or no dimmer
 - » Remote on/off at any phase-cut conduction angle or no dimmer
- Resonant control to achieve high efficiency (typical > 85% without dimmer)
- Excellent AC line distortion immunity ensures high quality of light under real-life circumstances
- Over-temperature LED current foldback and shutdown
- Tight LED current regulation (±5%)

3 Applications

- Dimmable LED intelligent lamps up to 25W
- Dimmable LED intelligent luminaires up to 25W

- Low standby power < 0.5W when I²C requests remotely off under no dimmer condition
- Wide 1% to 100% dimming range for both I²C dimming and phase-cut dimming
- Total harmonic distortion (THD) < 20%, PF > 0.92
- Advanced IC power management and voltage sensing enables use of off-the-shelf inductor
- Fast start-up (< 0.5s without dimmer)
- Multiple protection features include:
 - » LED open-circuit and short-circuit protection
 - » AC line over-voltage protection
 - » Over-current protection



Product	Summary
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Figure 3.1 : Simplified Application Circuit: iW3690 Supporting External MCU or Wireless Module Powered from LDO





Figure 3.2 : Simplified Application Circuit: iW3690 Supporting External MCU or Wireless Module Powered from Buck Converter



4 Pinout Description

	iW3690					
1	V _{IN}	V _{CB}	14			
2	V _{PP}	VD	13			
3	CFG	NC	12			
4	v _{cc}	SCL	11			
5	AGND	SDA	10			
6	PGND	NC	9			
7	ISENSE	vs	8			

Figure 4.1 : 14-Lead SOIC-14 Package

Pin Number	Pin Name	Туре	Pin Description
1	V _{IN}	Analog Input	Rectified AC line voltage input.
2	V _{PP}	Analog Input	Connects an $18k\Omega$ resistor to GND.
3	CFG	Analog Input	Configures OTP threshold on start-up. See the applications section for configuration information.
4	V_{CC}	Power	Power supply for control logic.
5	AGND	Ground	Signal ground. It should be connected to the power ground on PCB.
6	PGND	Ground	Power ground.
7	I _{SENSE}	Analog Input	Current sense.
8	Vs	Analog Input	Source voltage of MOSFET.
9	NC	No Connection	Not internally connected.
10	SDA	Digital Input/ Output	I ² C data signal, connected to external controller.
11	SCL	Digital Input/ Output	I ² C clock signal, connected to external controller.
12	NC	No Connection	Not internally connected.
13	V _D	Analog Input	Drain voltage of MOSFET.
14	V _{CB}	Analog Input	Input capacitor voltage after EMI filter.

5 Absolute Maximum Ratings

Absolute maximum ratings are the parameter values or ranges which can cause permanent damage if exceeded.

Parameter	Symbol	Value	Units
DC supply voltage range (pin 4)	V _{cc}	-0.3 to 6	V
V _{IN} input (pin 1)		-0.3 to 6	V
CFG input (pin 3)		-0.3 to 6	V
V _{PP} input (pin 2)		-0.3 to 20	V
I _{SENSE} input (pin 7)		-0.3 to 6	V
V _S input (pin 8)		-0.3 to 20	V
SDA input (pin 10)		-0.3 to 6	V
SCL input (pin 11)		-0.3 to 20	V
V _D input voltage (pin 13)		-0.3 to 6	V
V _{CB} input voltage (pin 14)		-0.3 to 6	V
Maximum V _D input current (pin 13)	I _{IN(VD)}	750	μA
Maximum V _{CB} input current (pin 14)	I _{IN(VCB)}	750	μA
Maximum junction temperature	T _{JMAX}	150	°C
Operating junction temperature	T _{JOPT}	-40 to 150	°C
Storage temperature	T _{STG}	-65 to 150	°C
Thermal resistance junction-to-PCB [gnd lead]	Ψ _{ЈВ}	45	°C/W
ESD rating per JEDEC JESD22-A114		±1,000	V
Latch-up test per JESD78A		±100	mA



6 Physical Dimensions

14-Lead	SOIC	Package
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Symbol	Inches		Millimeters	
Syr	MIN	MAX	MIN	MAX
Α	0.053	0.069	1.35	1.75
A1	0.004	0.010	0.10	0.25
b	0.013	0.020	0.33	0.51
С	0.007	0.010	0.19	0.25
D	0.337	0.344	8.55	8.75
E1	0.150	0.157	3.80	4.00
Е	0.228	0.244	5.80	6.20
е	0.050) BSC	1.27	BSC
L	0.016	0.050	0.40	1.27
h	0.010	0.020	0.25	0.50
θ	0°	8°	0°	8°
ссс	0.0	04	0.1	10

Compliant to JEDEC Standard MS12F

Controlling dimensions are in inches; millimeter dimensions are for reference only

This product is RoHS compliant and Halide free.

Soldering Temperature Resistance:

[a] Package is IPC/JEDEC Std 020D Moisture Sensitivity Level 1

[b] Package exceeds JEDEC Std No. 22-A111 for Solder Immersion Resistance; package can withstand 10 s immersion < 260°C

Dimension D does not include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs shall not exceed 0.15 mm per end. Dimension E does not include interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.25 mm per side.

The package top may be smaller than the package bottom. Dimensions D and E are determined at the outermost extremes of the plastic body exclusive of mold flash, tie bar burrs, gate burrs and interlead flash, but including any mismatch between the top and bottom of the plastic body.

Figure 6.1 : 14-Lead SOIC Package

7 Ordering Information

Part No.	Options	Package	Description
iW3690-00	120V _{AC} Input for up to 25W	SOIC-14	Tape & Reel ¹

Note 1: Tape & Reel packing quantity is 2,500/reel. Minimum ordering quantity is 2,500.

Prod	luct	Sum	mary



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Product Summary

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