

General Description

The MAX4475-MAX4478/MAX4488/MAX4489 wideband, low-noise, low-distortion operational amplifiers offer rail-to-rail outputs and single-supply operation down to 2.7V. They draw 2.2mA of guiescent supply current per amplifier while featuring ultra-low distortion (0.0002% THD+N), as well as low input voltage-noise density (4.5nV/ \sqrt{Hz}) and low input current-noise density (0.5fA/√Hz). These features make the devices an ideal choice for applications that require low distortion and/or low noise.

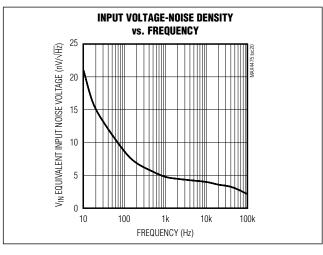
For power conservation, the MAX4475/MAX4488 offer a low-power shutdown mode that reduces supply current to 0.01µA and places the amplifiers' outputs into a highimpedance state. These amplifiers have outputs which swing rail-to-rail and their input common-mode voltage range includes ground. The MAX4475-MAX4478 are unity-gain stable with a gain-bandwidth product of 10MHz. The MAX4488/4489 are internally compensated for gains of +5V/V or greater with a gain-bandwidth product of 42MHz. The single MAX4475/MAX4476/ MAX4488 are available in space-saving, 6-pin SOT23 and TDFN packages.

Applications

ADC Buffers DAC Output Amplifiers Low-Noise Microphone/Preamplifiers Digital Scales Strain Gauges/Sensor Amplifiers Medical Instrumentation

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Typical Operating Characteristic



Features

- ♦ Low Input Voltage-Noise Density: 4.5nV/√Hz
- **♦** Low Input Current-Noise Density: 0.5fA/√Hz
- ♦ Low Distortion: 0.0002% THD+N (1kΩ load)
- ♦ Single-Supply Operation from +2.7V to +5.5V
- ♦ Input Common-Mode Voltage Range Includes Ground
- ♦ Rail-to-Rail Output Swings with a 1kΩ Load
- ♦ 10MHz GBW Product, Unity-Gain Stable (MAX4475-MAX4478)
- ♦ 42MHz GBW Product, Stable with A_V ≥ +5V/V (MAX4488/MAX4489)
- **♦ Excellent DC Characteristics**

 $V_{OS} = 70 \mu V$

IBIAS = 1pA

Large-Signal Voltage Gain = 120dB

♦ Low-Power Shutdown Mode:

Reduces Supply Current to 0.01µA Places Output in High-Impedance State

◆ Available in Space-Saving SOT23, TDFN, µMAX®, and TSSOP Packages

Ordering Information

PART	TEMP RANGE	PIN- PACKAGE	TOP MARK
MAX4475AUT+T	-40°C to +125°C	6 SOT23	AAZV
MAX4475AUA+	-40°C to +125°C	8 µMAX	_
MAX4475ASA+	-40°C to +125°C	8 SO	_
MAX4475ATT+T	-40°C to +125°C	6 TDFN-EP*	+ADD
MAX4475AUT/V+T	-40°C to +125°C	6 SOT23	+ACQQ

- +Denotes a lead(Pb)-free/RoHS-compliant package.
- *EP = Exposed pad (connect to Vss).

N denotes an automotive qualified part.

T = Tape and reel.

Ordering Information continued at end of data sheet. Pin Configurations and Typical Operating Circuit appear at end of data sheet.

Selector Guide

PART	GAIN BW (MHz)	STABLE GAIN (V/V)	NO. OF AMPS	SHDN
MAX4475	10	1	1	Yes
MAX4476	10	1	1	_
MAX4477	10	1	2	_
MAX4478	10	1	4	_
MAX4488	42	5	1	Yes
MAX4489	42	5	2	_

Maxim Integrated Products 1

ABSOLUTE MAXIMUM RATINGS

Power-Supply Voltage (VDD to VSS)0.3V to +6.0V
Analog Input Voltage (IN_+, IN)(VSS - 0.3V) to (VDD + 0.3V)
SHDN Input Voltage(VSS - 0.3V) to +6.0V
Output Short-Circuit Duration to Either SupplyContinuous
Continuous Input Current (IN+, IN-)±10mA
Continuous Power Dissipation ($T_A = +70^{\circ}C$)
6-Pin SOT23 (derate 9.1mW/°C above +70°C)727mW
6-Pin TDFN (derate 18.2mW/°C above 70°C)1454mW
8-Pin μMAX (derate 4.5mW/°C above +70°C)362mW
8-Pin SO (derate 5.88mW/°C above +70°C)471mW

14-Pin SO (derate 8.33mW/°C above +70°C	C)667mW
14-Pin TSSOP (derate 9.1mW/°C above +70	0°C)727mW
Operating Temperature Range	40°C to +125°C
Junction Temperature	+150°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10s)	+300°C
Soldering Temperature (reflow)	
SOT23, µMAX, TSSOP, TDFN, and 8-Pin SC)+260°C
14-Pin SO	+240°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

DC ELECTRICAL CHARACTERISTICS

 $(V_{DD} = +5V, V_{SS} = 0V, V_{CM} = 0V, V_{OUT} = V_{DD}/2, R_L \text{ tied to } V_{DD}/2, \overline{SHDN} = V_{DD}, T_A = -40^{\circ}C \text{ to } +125^{\circ}C, \text{ unless otherwise noted.}$ Typical values are at $T_A = +25^{\circ}C$.) (Notes 1, 2)

PARAMETER	SYMBOL	co	NDITION	IS	MIN	TYP	MAX	UNITS
Supply Voltage Range	V_{DD}	(Note 3)			2.7		5.5	V
Quiescent Supply Current Per		Normal mode		$V_{DD} = 3V$		2.2		mA
Amplifier	ID	Normal mode		$V_{DD} = 5V$		2.5	4.4	1117 (
7 1172 1110		Shutdown mode	(SHDN =	Vss) (Note 2)		0.01	1.0	μΑ
Input Offset Voltage	Vos	$T_A = +25^{\circ}C$				±70	±350	μV
input Onset Voltage	VUS	$T_A = -40^{\circ}C \text{ to } +12^{\circ}$	25°C				±750	μν
Input Offset Voltage Tempco	TC _{VOS}					±0.3	±6	μV/°C
Input Bias Current	lΒ	(Note 4)				±1	±150	рΑ
Input Offset Current	los	(Note 4)				±1	±150	рΑ
Differential Input Resistance	RIN					1000		GΩ
Input Common-Mode Voltage	Voi	Guaranteed by	T _A = +	25°C	-0.2		V _{DD} - 1.6	V
Range	V _{CM}	CMRR Test	T _A = -4	10°C to +125°C	-0.1		V _{DD} - 1.7	v
Occurred Made Deignetics Detic	OMPD	$(V_{SS} - 0.2V) \le V_{CM} \le (V_{DD} - 1.6V)$	T _A = +	25°C	90	115		- C
Common-Mode Rejection Ratio	mmon-Mode Rejection Ratio CMRR $(V_{SS} - 0.1)$ $V_{CM} \le (V_{D} 1.7)$		T _A = -4	10°C to +125°C	90			dB
Power-Supply Rejection Ratio	PSRR	$V_{DD} = 2.7 \text{ to } 5.5 \text{V}$	/		90	120		dB
		$R_L = 10k\Omega$ to $V_{DD}/2$; $V_{OUT} = 100mV$ to $(V_{DD} - 125mV)$		90	120			
Large-Signal Voltage Gain	Avol	$R_L = 1k\Omega$ to $V_{DD}/2$; $V_{OUT} = 200mV$ to $(V_{DD} - 250mV)$		85	110		dB	
		$R_L = 500\Omega$ to $V_{DD}/2$; $V_{OUT} = 350$ mV to $(V_{DD} - 500$ mV)		85	110			

DC ELECTRICAL CHARACTERISTICS (continued)

 $(V_{DD} = +5V, V_{SS} = 0V, V_{CM} = 0V, V_{OUT} = V_{DD}/2, R_L \text{ tied to } V_{DD}/2, \overline{SHDN} = V_{DD}, T_A = -40^{\circ}C \text{ to } +125^{\circ}C, \text{ unless otherwise noted.}$ Typical values are at $T_A = +25^{\circ}C$.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
		$ V_{IN+} - V_{IN-} \ge 10 \text{mV},$	V _{DD} - V _{OH}		10	45	
		$R_L = 10k\Omega$ to $V_{DD}/2$	Vol - Vss		10	40	
Output Valtage Swing	\/ o =	$ V_{IN+} - V_{IN-} \ge 10 \text{mV},$	V _{DD} - V _{OH}		80	200	mV
Output Voltage Swing	Vout	$R_L = 1k\Omega$ to $V_{DD}/2$	V _{OL} - V _{SS}		50	150	IIIV
		$ V_{IN+} - V_{IN-} \ge 10 \text{mV},$	V _{DD} - V _{OH}		100	300	
		$R_L = 500\Omega$ to $V_{DD}/2$	V _{OL} - V _{SS}		80	250	
Output Short-Circuit Current	Isc				48		mA
Output Leakage Current	ILEAK	Shutdown mode (SHDN VOUT = VSS to VDD	$\bar{I} = V_{SS}),$		±0.001	±1.0	μΑ
SHDN Logic Low	V _I L					$0.3 \times V_{DD}$	V
SHDN Logic High	VIH			0.7 x V _{DD}			V
SHDN Input Current		$\overline{SHDN} = V_{SS}$ to V_{DD}			0.01	1	μΑ
Input Capacitance	CIN				10		рF

AC ELECTRICAL CHARACTERISTICS

 $(V_{DD} = +5V, V_{SS} = 0V, V_{CM} = 0V, V_{OUT} = V_{DD}/2, R_L \text{ tied to } V_{DD}/2, \overline{SHDN} = V_{DD}, T_A = +25^{\circ}C.)$

PARAMETER	SYMBOL	CONDITI	ons	MIN	TYP	MAX	UNITS	
Gain-Bandwidth Product	GBWP	MAX4475-MAX4478	$A_V = +1V/V$		10		MHz	
Gain-Bandwidtin Product	GBWP	MAX4488/MAX4489	$A_V = +5V/V$		42		IVIIIZ	
Slew Rate	SR	MAX4475-MAX4478	$A_V = +1V/V$		3		V/µs	
Siew nate	Sh	MAX4488/MAX4489	$A_V = +5V/V$		10		ν/μδ	
Full-Power Bandwidth (Note 5)		MAX4475-MAX4478	$A_V = +1V/V$		0.4		MHz	
Full-Power Bandwidth (Note 5)		MAX4488/MAX4489	$A_V = +5V/V$		1.25		IVITZ	
Peak-to-Peak Input Noise Voltage	e _{n(P-P)}	f = 0.1Hz to 10Hz			260		nV _{P-P}	
		f = 10Hz			21			
Input Voltage-Noise Density	en	f = 1kHz			4.5		nV/√ Hz	
		f = 30kHz			3.5			
Input Current-Noise Density	in	f = 1kHz			0.5		fA/√Hz	
		$V_{OUT} = 2V_{P-P},$ $A_V = +1V/V$	f = 1kHz		0.0002			
		(MAX4475–MAX4478), $R_L = 10k\Omega$ to GND	f = 20kHz		0.0007			
Total Harmonic Distortion Plus		$V_{OUT} = 2V_{P-P},$ $A_V = +1V/V$	f = 1kHz		0.0002			
Noise (Note 6)	THD + N	(MAX4475–MAX4478), $R_L = 1k\Omega$ to GND	f = 20kHz		0.001		%	
		$V_{OUT} = 2V_{P-P},$ $A_V = +5V/V$	f = 1kHz		0.0004			
		(MAX4488/MAX4489), $R_L = 10k\Omega$ to GND	f = 20kHz		0.0006			

AC ELECTRICAL CHARACTERISTICS (continued)

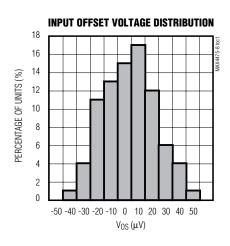
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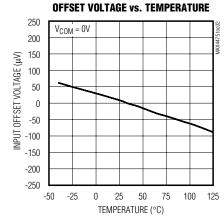
PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Total Harmonic Distortion Plus	TUD. N	$V_{OUT} = 2V_{P-P},$ $A_V = +5V/V$	f = 1kHz		0.0005		0/
Noise (Note 6)	THD + N	I (MAX4488/MAX4489)	f = 20kHz		0.008		%
Capacitive-Load Stability		No sustained oscillation	S		200		pF
Gain Margin	GM				12		dB
Phone Margin	ΦМ	MAX4475-MAX4478, Av	MAX4475–MAX4478, $A_V = +1V/V$		70		dograda
Phase Margin	ΨΙνΙ	MAX4488/MAX4489, A _V	y' = +5V/V		80		degrees
Settling Time		To 0.01%, V _{OUT} = 2V st	ер		2		μs
Delay Time to Shutdown	t _{SH}				1.5		μs
Enable Delay Time from Shutdown	t _{EN}	V _{OUT} = 2.5V, V _{OUT} settles to 0.1%			10		μs
Power-Up Delay Time		V _{DD} = 0 to 5V step, V _{OUT} stable to 0.1%			13		μs

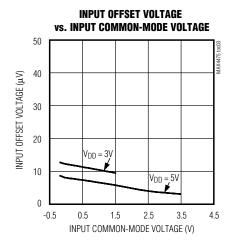
- **Note 1:** All devices are 100% tested at $T_A = +25^{\circ}C$. Limits over temperature are guaranteed by design.
- Note 2: SHDN is available on the MAX4475/MAX4488 only.
- Note 3: Guaranteed by the PSRR test.
- Note 4: Guaranteed by design.
- **Note 5:** Full-power bandwidth for unity-gain stable devices (MAX4475–MAX4478) is measured in a closed-loop gain of +2V/V to accommodate the input voltage range, $V_{OUT} = 4V_{P-P}$.
- Note 6: Lowpass-filter bandwidth is 22kHz for f = 1kHz and 80kHz for f = 20kHz. Noise floor of test equipment = 10nV/√Hz.

Typical Operating Characteristics

 $(V_{DD} = +5V, V_{SS} = 0V, V_{CM} = 0V, V_{OUT} = V_{DD}/2, R_L \text{ tied to } V_{DD}/2, \text{ input noise floor of test equipment } = 10nV/\sqrt{Hz} \text{ for all distortion measurements, } T_A = +25^{\circ}C, \text{ unless otherwise noted.})$







Typical Operating Characteristics (continued)

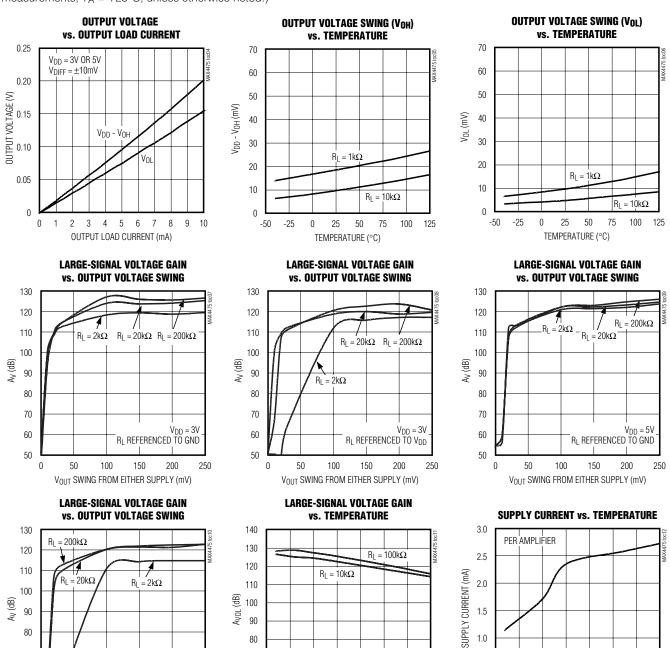
1.0

0.5

0

-50 -25

 $(V_{DD} = +5V, V_{SS} = 0V, V_{CM} = 0V, V_{OUT} = V_{DD}/2, R_L \text{ tied to } V_{DD}/2, \text{ input noise floor of test equipment } = 10 \text{nV}/\sqrt{\text{Hz}} \text{ for all distortion}$ measurements, $T_A = +25$ °C, unless otherwise noted.)



80

70

60

-50

 $V_{OUT} = 150 \text{mV} \overline{10 \ 4.75 \text{V}}$

25 50 75

TEMPERATURE (°C)

 $V_{DD} = 5V$

R_L REFERENCED TO V_{DD}

V_{OUT} SWING FROM EITHER SUPPLY (mV)

80

70

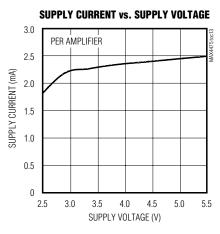
60

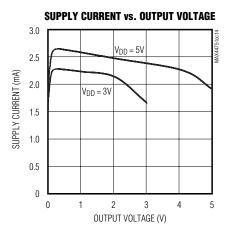
100 125

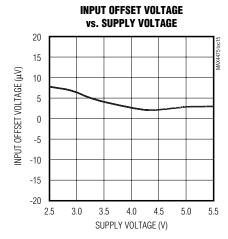
TEMPERATURE (°C)

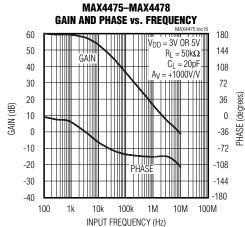
Typical Operating Characteristics (continued)

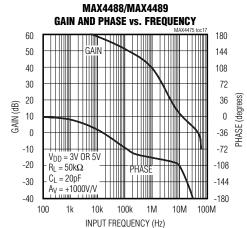
 $(V_{DD} = +5V, V_{SS} = 0V, V_{CM} = 0V, V_{OUT} = V_{DD}/2, R_L \text{ tied to } V_{DD}/2, \text{ input noise floor of test equipment } = 10nV/\sqrt{Hz} \text{ for all distortion measurements, } T_A = +25^{\circ}C, \text{ unless otherwise noted.})$

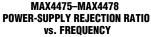


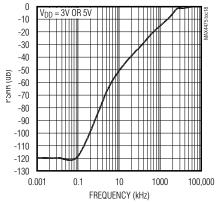


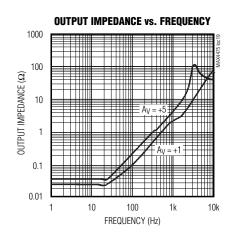






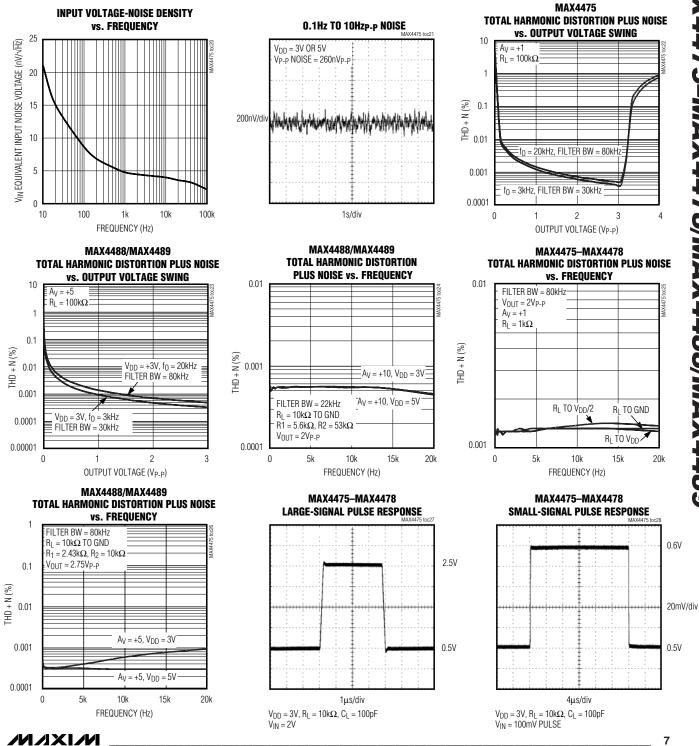






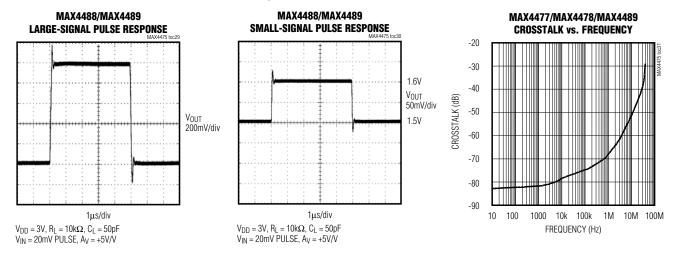
Typical Operating Characteristics (continued)

 $(V_{DD} = +5V, V_{SS} = 0V, V_{CM} = 0V, V_{OUT} = V_{DD}/2, R_L \text{ tied to } V_{DD}/2, \text{ input noise floor of test equipment } = 10nV/\sqrt{Hz} \text{ for all distortion}$ measurements, $T_A = +25^{\circ}C$, unless otherwise noted.)



_Typical Operating Characteristics (continued)

 $(V_{DD} = +5V, V_{SS} = 0V, V_{CM} = 0V, V_{OUT} = V_{DD}/2, R_L \text{ tied to } V_{DD}/2, \text{ input noise floor of test equipment } = 10nV/\sqrt{Hz} \text{ for all distortion measurements, } T_A = +25^{\circ}C, \text{ unless otherwise noted.})$



Pin Description

		PIN				
MAX4475/ MAX4488	MAX4475/ MAX4488	MAX4476	MAX4477/ MAX4489	MAX4478	NAME	FUNCTION
SOT23/TDFN	SO/μMAX	SOT23/TDFN	SO/μMAX	SO/TSSOP		
1	6	1	1, 7	1, 7, 8, 14	OUT, OUTA, OUTB, OUTC, OUTD	Amplifier Output
2	4	2	4	11	V _{SS}	Negative Supply. Connect to ground for single- supply operation
3	3	3	3, 5	3, 5, 10, 12	IN+, INA+, INB+, INC+, IND+	Noninverting Amplifier Input
4	2	4	2, 6	2, 6, 9, 13	IN-, INA-, INB-, INC-, IND-	Inverting Amplifier Input
6	7	6	8	4	V_{DD}	Positive Supply
5	8	_	_	_	SHDN	Shutdown Input. Connect to V _{DD} for normal operation (amplifier(s) enabled).
_	1, 5	5	_	_	N.C.	No Connection. Not internally connected.
EP (TDFN only)	_	EP (TDFN only)	_	_	EP	Exposed Paddle. Connect to Vss.

Detailed Description

The MAX4475–MAX4478/MAX4488/MAX4489 single-supply operational amplifiers feature ultra-low noise and distortion. Their low distortion and low noise make them ideal for use as preamplifiers in wide dynamic-range applications, such as 16-bit analog-to-digital converters (see *Typical Operating Circuit*). Their high-input impedance and low noise are also useful for signal conditioning of high-impedance sources, such as piezoelectric transducers.

These devices have true rail-to-rail ouput operation, drive loads as low as $1k\Omega$ while maintining DC accuracy, and can drive capactive loads up to 200pF without oscillation. The input common-mode voltage range extends from (VDD - 1.6V) to 200mV below the negative rail. The push-pull output stage maintains excellent DC characteristics, while delivering up to $\pm5mA$ of current.

The MAX4475–MAX4478 are unity-gain stable, while the MAX4488/MAX4489 have a higher slew rate and are stable for gains \geq 5V/V. The MAX4475/MAX4488 feature a low-power shutdown mode, which reduces the supply current to 0.01µA and disables the outputs.

Low Distortion

Many factors can affect the noise and distortion that the device contributes to the input signal. The following guidelines offer valuable information on the impact of design choices on Total Harmonic Distortion (THD).

Choosing proper feedback and gain resistor values for a particular application can be a very important factor in reducing THD. In general, the smaller the closed-loop gain, the smaller the THD generated, especially when driving heavy resistive loads. The THD of the part normally increases at approximately 20dB per decade, as a function of frequency. Operating the device near or above the full-power bandwidth significantly degrades distortion.

Referencing the load to either supply also improves the part's distortion performance, because only one of the MOSFETs of the push-pull output stage drives the output. Referencing the load to midsupply increases the part's distortion for a given load and feedback setting. (See the Total Harmonic Distortion vs. Frequency graph in the *Typical Operating Characteristics*.)

For gains ≥ 5V/V, the decompensated devices MAX4488/MAX4489 deliver the best distortion performance, since they have a higher slew rate and provide a higher amount of loop gain for a given closed-loop gain setting. Capacitive loads below 100pF do not significantly affect distortion results. Distortion performance is relatively constant over supply voltages.

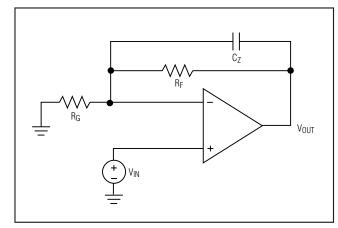


Figure 1. Adding Feed-Forward Compensation

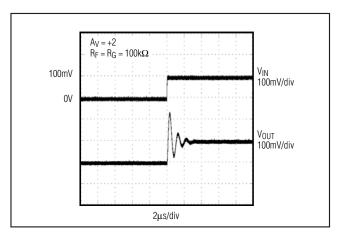


Figure 2a. Pulse Response with No Feed-Forward Compensation

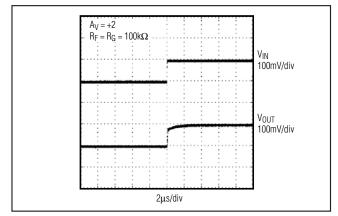


Figure 2b. Pulse Response with 10pF Feed-Forward Compensation

Low Noise

The amplifier's input-referred noise-voltage density is dominated by flicker noise at lower frequencies, and by thermal noise at higher frequencies. Because the thermal noise contribution is affected by the parallel combination of the feedback resistive network (RF II RG, Figure 1), these resistors should be reduced in cases where the system bandwidth is large and thermal noise is dominant. This noise contribution factor decreases, however, with increasing gain settings.

For example, the input noise-voltage density of the circuit with RF = 100k Ω , RG = 11k Ω (AV = +5V/V) is en = 14nV/ $\sqrt{\text{Hz}}$, en can be reduced to 6nV/ $\sqrt{\text{Hz}}$ by choosing RF = 10k Ω , RG = 1.1k Ω (AV = +5V/V), at the expense of greater current consumption and potentially higher distortion. For a gain of 100V/V with RF = 100k Ω , RG = 1.1k Ω , the en is still a low 6nV/ $\sqrt{\text{Hz}}$.

Using a Feed-Forward Compensation Capacitor, Cz

The amplifier's input capacitance is 10pF. If the resistance seen by the inverting input is large (feedback network), this can introduce a pole within the amplifier's bandwidth resulting in reduced phase margin. Compensate the reduced phase margin by introducing a feed-forward capacitor (Cz) between the inverting input and the output (Figure 1). This effectively cancels the pole from the inverting input of the amplifier. Choose the value of Cz as follows:

$$Cz = 10 \times (R_F / R_G) [pF]$$

In the unity-gain stable MAX4475–MAX4478, the use of a proper Cz is most important for Av = +2V/V, and Av = -1V/V. In the decompensated MAX4488/MAX4489, Cz is most important for Av = +10V/V. Figures 2a and 2b show transient response both with and without Cz.

Using a slightly smaller Cz than suggested by the formula above achieves a higher bandwidth at the expense of reduced phase and gain margin. As a general guideline, consider using Cz for cases where Rg II RF is greater than $20 k\Omega$ (MAX4475–MAX4478) or greater than $5 k\Omega$ (MAX4488/MAX4489).

Applications Information

The MAX4475–MAX4478/MAX4488/MAX4489 combine good driving capability with ground-sensing input and rail-to-rail output operation. With their low distortion and low noise, they are ideal for use in ADC buffers, medical instrumentation systems and other noise-sensitive applications.

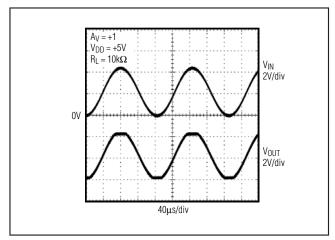


Figure 3. Overdriven Input Showing No Phase Reversal

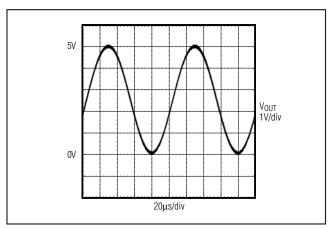


Figure 4. Rail-to-Rail Output Operation

Ground-Sensing and Rail-to-Rail Outputs

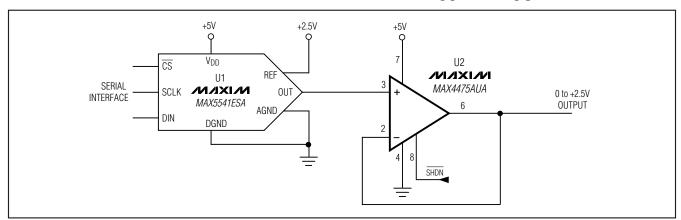
The common-mode input range of these devices extends below ground, and offers excellent common-mode rejection. These devices are guaranteed not to undergo phase reversal when the input is overdriven (Figure 3).

Figure 4 showcases the true rail-to-rail output operation of the amplifier, configured with Av = 5V/V. The output swings to within 8mV of the supplies with a $10k\Omega$ load, making the devices ideal in low-supply voltage applications.

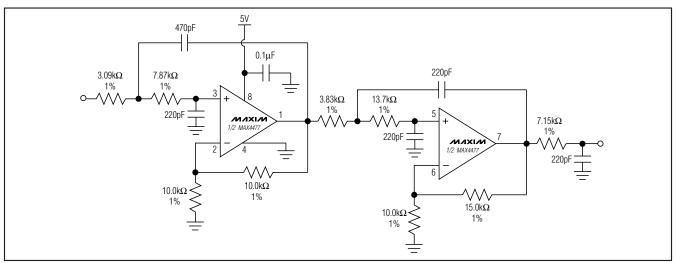
Power Supplies and Layout

The MAX4475–MAX4478/MAX4488/MAX4489 operate from a single +2.7V to +5.5V power supply or from dual supplies of ± 1.35 V to ± 2.75 V. For single-supply operation, bypass the power supply with a 0.1μ F ceramic

Typical Application Circuit



Typical Operating Circuit



capacitor placed close to the V_{DD} pin. If operating from dual supplies, bypass each supply to ground.

Good layout improves performance by decreasing the amount of stray capacitance and noise at the op amp's inputs and output. To decrease stray capacitance, minimize PC board trace lengths and resistor leads, and place external components close to the op amp's pins.

Typical Application Circuit

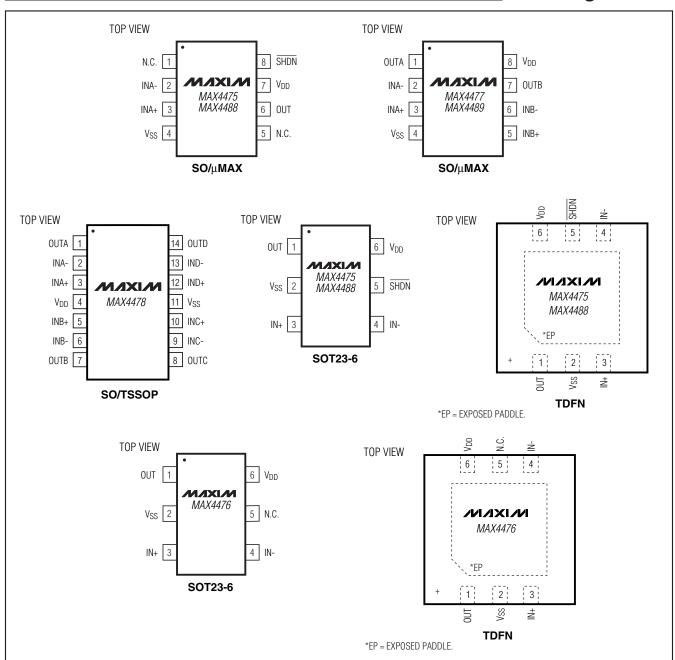
The Typical Application Circuit shows the single MAX4475 configured as an output buffer for the MAX5541 16-bit DAC. Because the MAX5541 has an unbuffered voltage output, the input bias current of the op amp used must be less than 6nA to maintain 16-bit accuracy. The MAX4475 has an input bias current of only 150pA (max), virtually eliminating this as a source

of error. In addition, the MAX4475 has excellent open-loop gain and common-mode rejection, making this an excellent ouput buffer amplifier.

DC-Accurate Lowpass Filter

The MAX4475–MAX4478/MAX4488/MAX4489 offer a unique combination of low noise, wide bandwidth, and high gain, making them an excellent choice for active filters up to 1MHz. The *Typical Operating Circuit* shows the dual MAX4477 configured as a 5th order Chebyschev filter with a cutoff frequency of 100kHz. The circuit is implemented in the Sallen-Key topology, making this a DC-accurate filter.

Pin Configurations



13

SOT23, Low-Noise, Low-Distortion, Wide-Band, Rail-to-Rail Op Amps

_Ordering Information (continued)

	Chip	Information
PROCESS: BICMOS	_	

PART	TEMP RANGE	PIN- PACKAGE	TOP MARK
MAX4476AUT+T	-40°C to +125°C	6 SOT23	AAZX
MAX4476ATT+T	-40°C to +125°C	6 TDFN-EP*	+ADF
MAX4477AUA+	-40°C to +125°C	8 µMAX	_
MAX4477AUA+	-40°C to +125°C	8 µMAX	_
MAX4477ASA+	-40°C to +125°C	8 SO	_
MAX4478AUD+	-40°C to +125°C	14 TSSOP	_
MAX4478AUD/V+	-40°C to +125°C	14 TSSOP	_
MAX4478ASD+	-40°C to +125°C	14 SO	_
MAX4488AUT+T	-40°C to +125°C	6 SOT23	AAZW
MAX4488AUA+	-40°C to +125°C	8 µMAX	_
MAX4488ASA+	-40°C to +125°C	8 SO	_
MAX4488ATT+T	-40°C to +125°C	6 TDFN-EP*	+ADE
MAX4489AUA+	-40°C to +125°C	8 µMAX	_

⁺Denotes a lead(Pb)-free/RoHS-compliant package.

N denotes an automotive qualified part.

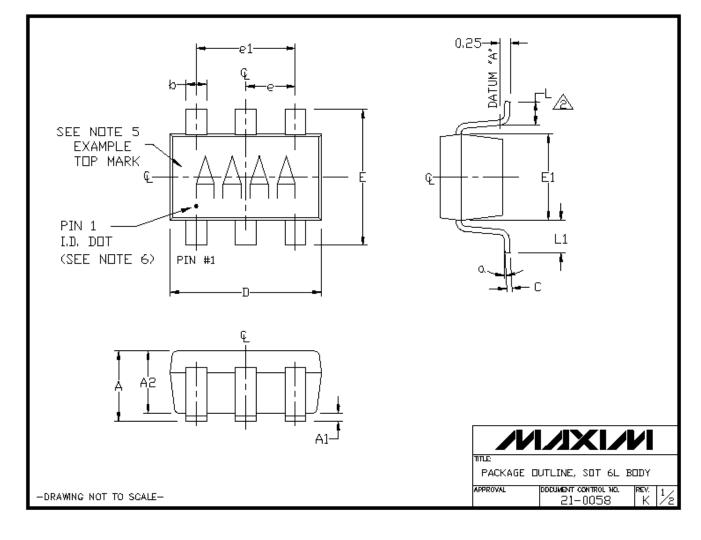
^{*}EP = Exposed pad (connect to Vss).

T = Tape and reel.

Package Information

For the latest package outline information and land patterns, go to www.maxim-ic.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
6 SOT23	U6F-6	<u>21-0058</u>	<u>90-0175</u>
8 μMAX	U8-1	<u>21-0036</u>	90-0092
14 TSSOP	U14-2	<u>21-0066</u>	<u>90-0117</u>
8 SO	S8-4	21-0041	_
14 SO	S14-4	<u>21-0041</u>	_
6 TDFN-EP	T633-2	<u>21-0137</u>	<u>90-0058</u>



Package Information (continued)

For the latest package outline information and land patterns, go to www.maxim-ic.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

NOTES:

ALL DIMENSIONS ARE IN MILLIMETERS.

🛕 FOOT LENGTH MEASURED AT INTERCEPT POINT BETWEEN DATUM A & LEAD SURFACE,

- PACKAGE DUTLINE EXCLUSIVE OF MOLD FLASH & METAL BURR, MOLD FLASH, PROTRUSION OR METAL BURR SHOULD NOT EXCEED 0.25mm.
- PACKAGE DUTLINE INCLUSIVE OF SOLDER PLATING.
- PIN 1 IS LOWER LEFT PIN WHEN READING TOP MARK FROM LEFT TO RIGHT. (SEE EXAMPLE TOP MARK)
- PIN 1 I.D. DOT IS 0.3mm Ø MIN. LOCATED ABOVE PIN 1.
- MEETS JEDEC MO178, VARIATION AB.
- SOLDER THICKNESS MEASURED AT FLAT SECTION OF LEAD BETWEEN 0.08mm AND 0.15mm FROM LEADTIP.
- LEAD TO BE COPLANAR WITHIN 0.1mm.
- 10. NUMBER OF LEADS SHOWN ARE FOR REFERENCE ONLY.
- 11. MARKING IS FOR PACKAGE ORIENTATION REFERENCE ONLY.
- 12. ALL DIMENSIONS APPLY TO BOTH LEADED (-) AND PEFREE (+) PKG.

SYMBOL	MIN	MAX		
Α	0.90	1.25	1,45	
A1	0.00	0.05	0.15	
A2	0.90	1.10	1.30	
ь	0.35	0.40	0.50	
С	0.08	0.15	0.20	
D	2,80	2.90	3,00	
Ε	2.60	2.80	3.00	
E1	1.50	1.625	1.75	
L	0.35	0.45	0.60	
L1	0.60 REF.			
e1	1.90 BSC.			
6	D.95 BSC.			
0.	0* 2.5° 10°			

PKG CODES(U6-1, U6-2, U6-4, U6CN-2, U6SN-1, U6F-6, U6FH-6

PACKAGE DUTLINE, SOT 6L BODY

APPROVAL

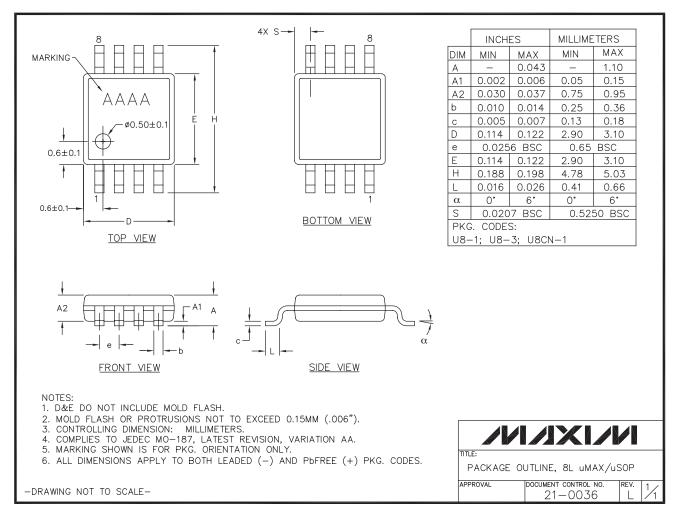
DDCUMENT CONTROL NO. 21-0058

2/2

-DRAWING NOT TO SCALE-

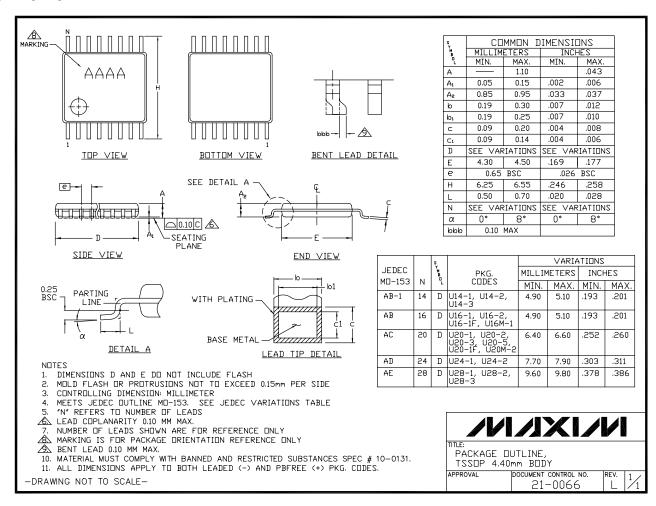
Package Information (continued)

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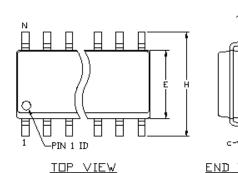
Package Information (continued)

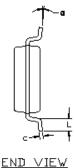
For the latest package outline information and land patterns, go to www.maxim-ic.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.



Package Information (continued)

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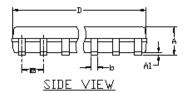


COMMON DIMENSIONS						
SYMBOL	INC	HES	мм			
STRIBUL	MIN.	MAX.	MIN.	MAX.		
Α	.053	.069	1.35	1.75		
A1	.004	.010	0.10	0.25		
b	.014	.019	0.35	0.49		
c	.007	.D1G	D.15	0.25		
Ε	.150	.157	3.60	4.DD		
e	.050	BSC	1.27	BSC		
Н	.228	.244	5.60	6.20		
L	.016	.050	0.40	1.27		
Œ	0*	₽•	0*	8⁺		

VARIATION A					
SYMBOL	INC	HES	мм		
STRIBUL	MIN.	MAX.	MIN.	MAX.	
D	.189	.197	4.80	5.00	
N	8				
MS012	AA				
PKG. CODE	\$8-2, \$8-4, \$8-5, \$8-6F, \$8-7F, \$8-8F, \$8-10F, \$8-11F, \$8-16F				

VARIATION B						
SYMBOL	INC	HES	ММ			
OIMDUL	MIN.	MAX.	MIN.	MAX.		
D	.337 .344		B.55	9.75		
N	14					
MS012	AB					
PKG. CODE	\$14-1, \$14-4, \$14-5, \$14-6, \$14M-4, \$14M-5, \$14M-6, \$14M-7					

VARIATI⊡N C					
SYMBOL	INC	HES	ММ		
SIMBUL	MIN.	MAX.	MIN.	MAX.	
D	.386	.394	9.80	10.00	
N	16				
MS012	AC				
PKG. 516-1. 516-3. 516-5. 516-6. 516-6. 516-8, 516-7F, 516-9F, 516-10F; 516M-3, 516M-6					



NOTES:

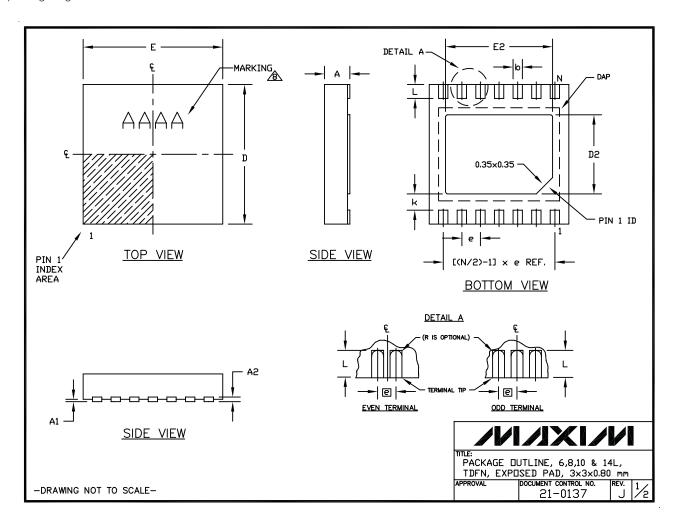
- 1. ALL DIMENSIONS ARE IN MILLIMETERS UNLESS OTHERWISE SPECIFIED.
- 2. MATERIAL MUST COMPLY WITH BANNED AND RESTRICTED SUBSTANCES SPEC # 10-0131.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSION, ALLOWABLE MOLD PROTRUSION IS 0.15 MM (.006*) PER SIDE.
- LEADS TO BE COPLANAR WITHIN 0.10mm (.004°).
- 5. MEETS JEDEC MS012
- 6. ALL DIMENSIONS APPLY TO BOTH LEADED (-) AND PHEREE (+) PKG. CODES.

-DRAWING NOT TO SCALE-



Package Information (continued)

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Package Information (continued)

For the latest package outline information and land patterns, go to www.maxim-ic.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

COMMON DIMENSIONS					
SYMBOL	MIN.	MAX.			
Α	0.70	0.80			
D	2.90	3.10			
Е	2.90	3.10			
A1	0.00	0.05			
L	0.20	0.40			
k	0.25 MIN.				
A2	0.20 REF.				

PACKAGE VARIATIONS							
PKG. CODE	N	D2	E2	е	JEDEC SPEC	b	[(N/2)-1] x e
T633-2	6	1.50±0.10	2.30±0.10	0.95 BSC	MO229 / WEEA	0.40±0.05	1.90 REF
T833-2	8	1.50±0.10	2.30±0.10	0.65 BSC	MO229 / WEEC	0.30±0.05	1.95 REF
T833-3	8	1.50±0.10	2.30±0.10	0.65 BSC	MO229 / WEEC	0.30±0.05	1.95 REF
T1033-1	10	1.50±0.10	2.30±0.10	0.50 BSC	MO229 / WEED-3	0.25±0.05	2.00 REF
T1033MK-1	10	1.50±0.10	2.30±0.10	0.50 BSC	MO229 / WEED-3	0.25±0.05	2.00 REF
T1033-2	10	1.50±0.10	2.30±0.10	0.50 BSC	MO229 / WEED-3	0.25±0.05	2.00 REF
T1433-1	14	1.70±0.10	2.30±0.10	0.40 BSC		0.20±0.05	2.40 REF
T1433-2	14	1.70±0.10	2.30±0.10	0.40 BSC		0.20±0.05	2.40 REF
T1433-3F	14	1.70±0.10	2.30±0.10	0.40 BSC		0.20±0.05	2.40 REF

-DRAWING NOT TO SCALE-

- 1. ALL DIMENSIONS ARE IN mm. ANGLES IN DEGREES.
- 2. COPLANARITY SHALL NOT EXCEED 0.08 mm.
- 3. WARPAGE SHALL NOT EXCEED 0.10 mm.
- 4. PACKAGE LENGTH/PACKAGE WIDTH ARE CONSIDERED AS SPECIAL CHARACTERISTIC(S).
- 5. DRAWING CONFORMS TO JEDEC MO229, EXCEPT DIMENSIONS "D2" AND "E2", AND T1433-1 & T1433-2.
- 6. "N" IS THE TOTAL NUMBER OF LEADS.
- 7. NUMBER OF LEADS SHOWN ARE FOR REFERENCE ONLY.
- ⚠ MARKING IS FOR PACKAGE ORIENTATION REFERENCE ONLY.
- 9. ALL DIMENSIONS APPLY TO BOTH LEADED (-) AND PhFREE (+) PKG. CODES.

PACKAGE DUTLINE, 6,8,10 & 14L, TDFN, EXPOSED PAD, 3x3x0.80 mm

DOCUMENT CONTROL NO.

21-0137

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
4	12/09	Added lead-free designations and an automotive part to the <i>Ordering Information</i> and added input current spec in <i>Absolute Maximum Ratings</i> section	1, 2, 13
5	7/10	Added /V designation to the MAX4475 product and soldering temperature	1, 2

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