

## ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO (P6-P8-P3) : (J6-J3)  $: 1CT : 1 \pm 3\%$ 

3.0 LEAKAGE INDUCTANCE P6-P3 (WITH J6 AND J3 SHORT)

(P2-P7-P1) : (J2-J1): 1CT : 2 <u>+</u> 3%

: 0.3uH MAX. @ 1MHz

: 0,08uH MAX, @ 1MHz

2.0 INDUCTANCE (P6-P3)=(J6-J3) : 98uH MIN. @ 0.01V , 10KHz

(P2-P1)

: 18uH MIN. @ 0.01V, 10KHz

P2-P1 (WITH J2 AND J1 SHORT)

4.0 INTERWINDING CAPACITANCE (P6,P3) TD (J6,J3) : 8pf MAX @ 1MHz

(P2,P1) TO (J2,J1) : 6pf MAX, @ 1MHz

5.0 DC RESISTANCE (J6-J3)=(J2-J1) 1 0.7 ohms Max.

> (P6-P8)=(P8-P3): 0.3 ohms Max.

> (P2-P7)=(P7-P1) 1 0.3 ohms Max.

6.0 DIELECTRIC WITHSTAND (P6,P3) TO (J6,J3) : 1500VAC

> (P2,P1) TO (J2,J1) : 1500VAC

## NOTES

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

2.0 Ref. MG06

InNet Technologies Inc.

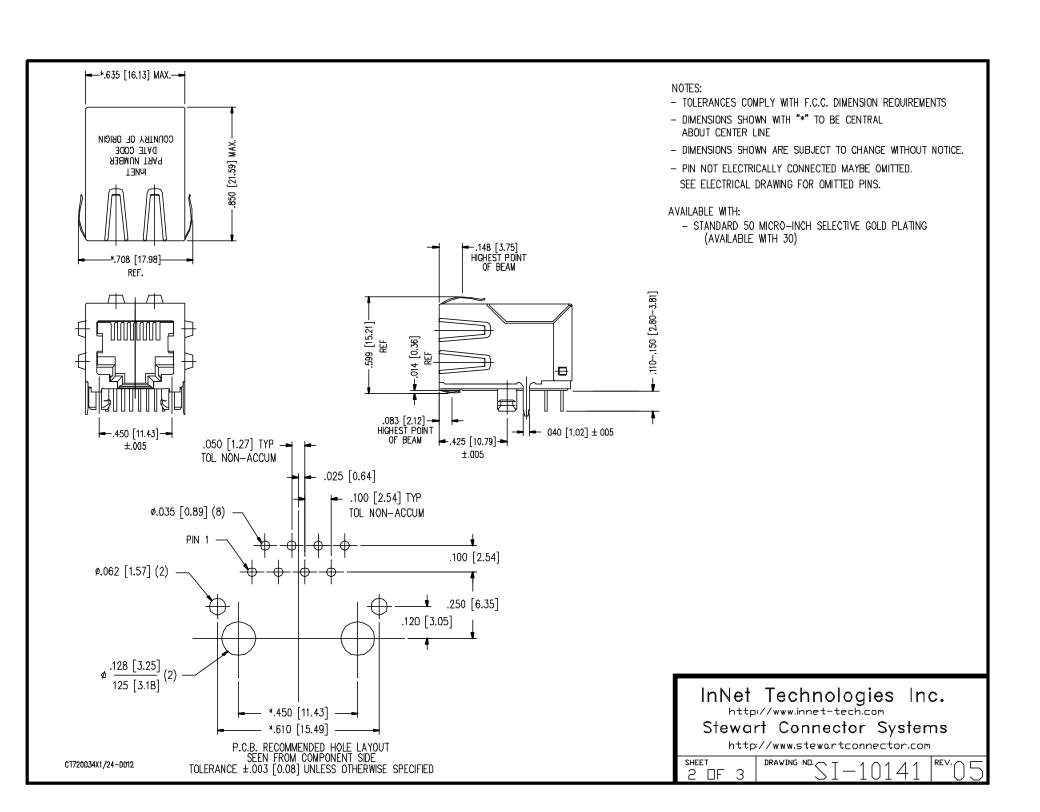
http://www.lnnet-tech.com

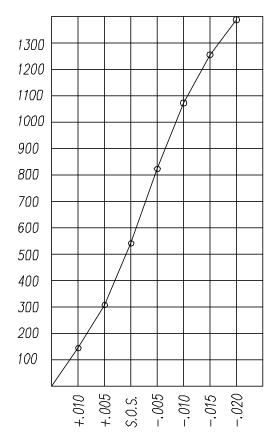
Stewart Connector Systems

http://www.stewartconnector.com

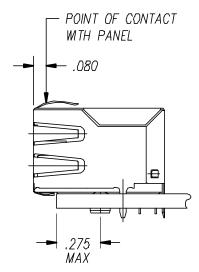
SHEET 1 DF 3

DRAWING NO.

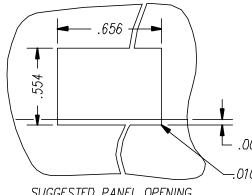




PANEL GROUNDING BEAM DEFLECTION S.O.S. = SUGGESTED OPENING SIZE



THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YFT MAINTAIN RFI IABI F GROUNDING CAPABILITY. THESE VARIABLES CAN BE ADJUSTED IN EITHER DIRECTION BUT MAY CARRY SOME CONSEQUENCES IN THE FORM OF LOWER MATING FORCES OR TIGHTER ASSEMBLY TOLERANCES. FORCE VALUES ON THE GRAPH ARE GENERAL AVERAGES TAKEN AT THE POINT OF CONTACT SHOWN ABOVE. THE SUGGESTED PANEL OPENING INCLUDES APPROXIMATELY .020 CLEARANCE ON THE SIDES AND TOP AND .013 ON THE BOTTOM, AT PANEL OPENING.



.000 (TOP OF PCB TO BOTTOM OF OPENING) -.010 MAX. RADIUS(4)

SUGGESTED PANEL OPENING

CT720034X1/24-001302

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