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April 1st, 2010 Renesas Electronics Corporation

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DATA SHEET



BIPOLAR ANALOG INTEGRATED CIRCUIT μ PC79Mxx Series

THREE TERMINAL NEGATIVE VOLTAGE REGULATOR

<R> DESCRIPTION

The μ PC79Mxx Series are three-terminal negative output voltage stabilization power supply circuit of fixed output voltage. It regulates non-stabilized DC input voltage to output stabilized fixed voltage.

The six types of voltage value are -5 V, -8 V, -12 V, -15 V, -18 V and -24 V, and they can be respectively used as power supply circuit with maximum current capacity 500 mA.

FEATURES

- Output current : 500 mA
- On-chip some protection circuits (over current protection, SOA protection and thermal shut down)
- Low noise

<R> ORDERING INFORMATION

Part Number	Package	Output Voltage	Marking	Package Type
μ PC79M05HF	3-PIN PLASTIC SIP (MP-45G) (Isolated TO-220)	–5 V	79M05	Packed in envelope
μ PC79M05HF-AZ Note	3-PIN PLASTIC SIP (MP-45G) (Isolated TO-220)	–5 V	79M05	Packed in envelope
μ PC79M08HF	3-PIN PLASTIC SIP (MP-45G) (Isolated TO-220)	–8 V	79M08	Packed in envelope
μ PC79M08HF-AZ Note	3-PIN PLASTIC SIP (MP-45G) (Isolated TO-220)	-8 V	79M08	Packed in envelope
μ PC79M12HF	3-PIN PLASTIC SIP (MP-45G) (Isolated TO-220)	-12 V	79M12	Packed in envelope
μ PC79M12HF-AZ Note	3-PIN PLASTIC SIP (MP-45G) (Isolated TO-220)	–12 V	79M12	Packed in envelope
μ PC79M15HF	3-PIN PLASTIC SIP (MP-45G) (Isolated TO-220)	−15 V	79M15	Packed in envelope
μ PC79M15HF-AZ Note	3-PIN PLASTIC SIP (MP-45G) (Isolated TO-220)	–15 V	79M15	Packed in envelope
_μ PC79M18HF	3-PIN PLASTIC SIP (MP-45G) (Isolated TO-220)	–18 V	79M18	Packed in envelope
μ PC79M18HF-AZ Note	3-PIN PLASTIC SIP (MP-45G) (Isolated TO-220)	–18 V	79M18	Packed in envelope
_μ PC79M24HF	3-PIN PLASTIC SIP (MP-45G) (Isolated TO-220)	–24 V	79M24	Packed in envelope
μ PC79M24HF-AZ Note	3-PIN PLASTIC SIP (MP-45G) (Isolated TO-220)	–24 V	79M24	Packed in envelope

Note Pb-free (This product does not contain Pb in external electrode).

Remark Output voltage -5 V product is written in the text as μ PC79M05. It applies to other output voltage products as same.

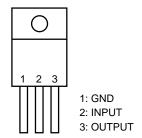
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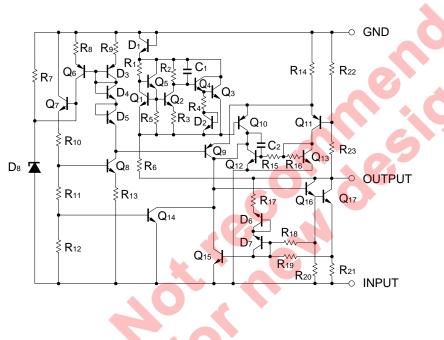
Document No. G11629EJ7V0DS00 (7th edition)
Date Published January 2007 NS CP(N)
Printed in Janan

PIN CONFIGURATION (Marking Side)

3-PIN PLASTIC SIP (MP-45G) (Isolated TO-220)



EQUIVALENT CIRCUIT



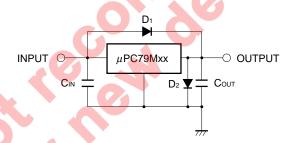
ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Input Voltage	Vin	-35/-40 Note1	V
Internal Power Dissipation	Рт	15 Note2	W
Operating Ambient Temperature	Та	-20 to +85	°C
Operating Junction Temperature	TJ	−20 to +150	°C
Storage Temperature	Tstg	−55 to +150	°C
Thermal Resistance (junction to case)	Rth(J-C)	7	°C/W
Thermal Resistance (junction to ambient)	Rth(J-A)	65	°C/W

Notes 1. μ PC79M05, 08, 12, 15, 18: –35 V, μ PC79M24: –40 V

- 2. Internally limited. When operating junction temperature rise above 150°C, the internal protection circuit shutdown output voltage.
- <R> Caution Product quality may suffer if the absolute maximum rating is exceeded even momentarily for any parameter. That is, the absolute maximum ratings are rated values at which the product is on the verge of suffering physical damage, and therefore the product must be used under conditions that ensure that the absolute maximum ratings are not exceeded.

<R> TYPICAL CONNECTION



 C_{IN} : Required if regulator is located an appreciable distance from power supply filter. (More than 2 μ F)

Cout: Connect it within 2 cm from OUTPUT pin and GND pin (More than 1 μ F).

D1 : Needed for V_{IN} > V_O.D2 : Needed for V_O > GND.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Type Number	MIN.	TYP.	MAX.	Unit
Input Voltage	Vin	μPC79M05	-7	-10	-25	V
		μPC79M08	-10.5	-14	-25	V
		μPC79M12	-14.5	-19	-30	V
		μPC79M15	-17.5	-23	-30	V
		μPC79M18	-21	-27	-33	V
		μPC79M24	-27	-33	-38	V
Output Current	lo	All	5		350	mA
Operating Ambient Temperature	TA	All	-20		+85	°C
Operating Junction Temperature	TJ	All	-20		+125	°C

<R>

ELECTRICAL CHARACTERISTICS

 μ PC79M05 (Vin = -10 V, Io = 350 mA, 0°C \leq TJ \leq +125°C, Cin = 2.2 μ F, Cout = 1 μ F, unless otherwise specified)

		, , ,				
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo	T _J = 25°C	-4.8	-5.0	-5.2	V
		$-7 \text{ V} \le V_{IN} \le -25 \text{ V}, 5 \text{ mA} \le I_0 \le 350 \text{ mA}$	-4.75		-5.25	V
Line Regulation	REGIN	T _J = 25°C, -7 V ≤ V _{IN} ≤ -25 V		18	50	mV
		T _J = 25°C, −8 V ≤ V _{IN} ≤ −18 V		10	30	mV
Load Regulation	REG∟	T _J = 25°C, 5 mA ≤ I _O ≤ 500 mA		15	100	mV
		T _J = 25°C, 5 mA ≤ I _O ≤ 350 mA		10		mV
Quiescent Current	IBIAS	T _J = 25°C		4.3	6.0	mA
Quiescent Current Change	⊿IBIAS	-8 V ≤ V _{IN} ≤ -25 V			0.5	mA
		5 mA ≤ lo ≤ 350 mA			0.4	mA
Output Noise Voltage	Vn	T _J = 25°C, 10 Hz ≤ f ≤ 100 kHz		45	200	$\mu V_{r.m.s.}$
Ripple Rejection	R•R	$T_J = 25^{\circ}C, -8 \text{ V} \le \text{Vin} \le -18 \text{ V},$	50	72		dB
		f = 120 Hz, lo = 100 mA)			
Dropout Voltage	V _{DIF}	T _J = 25°C		1.1		V
Short Circuit Current	Oshort	T _J = 25°C, V _{IN} = -25 V		500		mA
Peak Output Current	lOpeak	T _J = 25°C	620	880	1020	mA
Temperature Coefficient of Output Voltage	ΔVο/ΔΤ	lo = 5 mA		0.2		mV/°C

 μ PC79M08 (V_{IN} = -14 V, lo = 350 mA, 0°C \leq TJ \leq +125°C, C_{IN} = 2.2 μ F, Cout = 1 μ F, unless otherwise specified)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo	T _J = 25°C	-7.7	-8.0	-8.3	V
		$-10.5 \text{ V} \le \text{V}_{\text{IN}} \le -25 \text{ V}, 5 \text{ mA} \le \text{lo} \le 350 \text{ mA}$	-7.6		-8.4	V
Line Regulation	REGIN	$T_J = 25^{\circ}C$, $-10.5 \text{ V} \le V_{IN} \le -25 \text{ V}$		20	80	mV
		$T_J = 25^{\circ}C$, $-11 \text{ V} \le V_{IN} \le -21 \text{ V}$		15	50	mV
Load Regulation	REG∟	T _J = 25°C, 5 mA ≤ I _O ≤ 500 mA		20	160	mV
		T _J = 25°C, 5 mA ≤ lo ≤ 350 mA		15		mV
Quiescent Current	IBIAS	T _J = 25°C		4.3	6.0	mA
Quiescent Current Change	⊿IBIAS	-10.5 V ≤ V _{IN} ≤ -25 V			0.5	mA
		5 mA ≤ lo ≤ 350 mA			0.4	mA
Output Noise Voltage	Vn	T _J = 25°C, 10 Hz ≤ f ≤ 100 kHz		65	220	μVr.m.s.
Ripple Rejection	R•R	$T_J = 25^{\circ}C$, $-11.5 \text{ V} \le V_{IN} \le -21.5 \text{ V}$, $f = 120 \text{ Hz}$, $I_O = 100 \text{ mA}$	50	66		dB
Dropout Voltage	V _{DIF}	T _J = 25°C		1.1		V
Short Circuit Current	Oshort	T _J = 25°C, V _{IN} = -25 V		500		mA
Peak Output Current	lOpeak	T _J = 25°C	620	880	1020	mA
Temperature Coefficient of Output Voltage	⊿Vo/⊿T	lo = 5 mA		0.3		mV/°C

 μ PC79M12 (V_{IN} = -19 V, Io = 350 mA, 0°C \leq T_J \leq +125°C, C_{IN} = 2.2 μ F, C_{OUT} = 1 μ F, unless otherwise specified)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo	T _J = 25°C	-11.5	-12.0	-12.5	V
		$-14.5 \text{ V} \le V_{IN} \le -30 \text{ V}, 5 \text{ mA} \le I_0 \le 350 \text{ mA}$	-11.4		-12.6	V
Line Regulation	REGIN	$T_J = 25^{\circ}C, -14.5 \text{ V} \le V_{IN} \le -30 \text{ V}$		25	80	mV
		T _J = 25°C, −15 V ≤ V _{IN} ≤ −25 V		20	50	mV
Load Regulation	REG∟	T _J = 25°C, 5 mA ≤ I _O ≤ 500 mA		35	240	mV
		T _J = 25°C, 5 mA ≤ I _O ≤ 350 mA		25		mV
Quiescent Current	IBIAS	T _J = 25°C		4.4	6.0	mA
Quiescent Current Change	⊿IBIAS	$-14.5 \text{ V} \le \text{V}_{IN} \le -30 \text{ V}$			0.5	mA
		5 mA ≤ lo ≤ 350 mA			0.4	mA
Output Noise Voltage	Vn	T _J = 25°C, 10 Hz ≤ f ≤ 100 kHz		125	280	μVr.m.s.
Ripple Rejection	R•R	$T_J = 25^{\circ}C$, $-15 \text{ V} \le V_{IN} \le -25 \text{ V}$, $f = 120 \text{ Hz}$, $I_0 = 100 \text{ mA}$	50	64		dB
Dropout Voltage	VDIF	T _J = 25°C		1.1		V
Short Circuit Current	Oshort	T _J = 25°C, V _{IN} = -30 V		400		mA
Peak Output Current	lOpeak	T _J = 25°C	620	880	1020	mA
Temperature Coefficient of Output Voltage	ΔVo/ΔΤ	lo = 5 mA		0.4		mV/°C

 μ PC79M15 (Vin = -23 V, lo = 350 mA, 0°C \leq TJ \leq +125°C, Cin = 2.2 μ F, Cout = 1 μ F, unless otherwise specified)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo	T _J = 25°C	-14.4	-15.0	-15.6	V
		$-17.5 \text{ V} \le \text{V}_{\text{IN}} \le -30 \text{ V}, 5 \text{ mA} \le \text{Io} \le 350 \text{ mA}$	-14.25		-15.75	V
Line Regulation	REGIN	$T_J = 25^{\circ}C, -17.5 \text{ V} \le V_{IN} \le -30 \text{ V}$		30	80	mV
		T _J = 25°C, -18 V ≤ V _{IN} ≤ -28 V		25	50	mV
Load Regulation	REGL	T _J = 25°C, 5 mA ≤ I _O ≤ 500 mA		50	240	mV
		T _J = 25°C, 5 mA ≤ lo ≤ 350 mA		35		mV
Quiescent Current	IBIAS	T _J = 25°C		4.4	6.0	mA
Quiescent Current Change	⊿IBIAS	-17.5 V ≤ V _{IN} ≤ -30 V			0.5	mA
	•	5 mA ≤ lo ≤ 350 mA			0.4	mA
Output Noise Voltage	Vn	T _J = 25°C, 10 Hz ≤ f ≤ 100 kHz		150	360	μVr.m.s.
Ripple Rejection	R•R	$T_J = 25^{\circ}C$, $-18.5 \text{ V} \le V_{IN} \le -28.5 \text{ V}$, $f = 120 \text{ Hz}$, $I_0 = 100 \text{ mA}$	50	62		dB
Dropout Voltage	V _{DIF}	T _J = 25°C		1.1		V
Short Circuit Current	Oshort	$T_J = 25^{\circ}C$, $V_{IN} = -30 \text{ V}$		400		mA
Peak Output Current	lOpeak	T _J = 25°C	620	880	1020	mA
Temperature Coefficient of Output Voltage	ΔVο/ΔT	lo = 5 mA		0.6		mV/°C

Data Sheet G11629EJ7V0DS

 μ PC79M18 (Vin = -27 V, lo = 350 mA, 0°C \leq TJ \leq +125°C, Cin = 2.2 μ F, Cout = 1 μ F, unless otherwise specified)

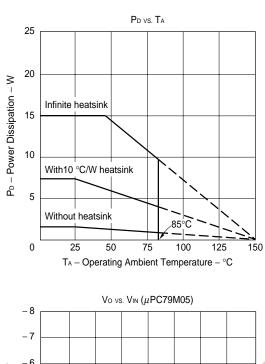
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Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo	T _J = 25°C	-17.3	-18.0	-18.7	V
		$-21 \text{ V} \le V_{IN} \le -33 \text{ V}, 5 \text{ mA} \le I_0 \le 350 \text{ mA}$	-17.1		-18.9	V
Line Regulation	REGIN	T _J = 25°C, −21 V ≤ V _{IN} ≤ −33 V		30	80	mV
		T _J = 25°C, −24 V ≤ V _{IN} ≤ −30 V		25	50	mV
Load Regulation	REG∟	T _J = 25°C, 5 mA ≤ I _O ≤ 500 mA		60	300	mV
		T _J = 25°C, 5 mA ≤ I _O ≤ 350 mA		45		mV
Quiescent Current	IBIAS	T _J = 25°C		4.4	6.0	mA
Quiescent Current Change	⊿IBIAS	$-21 \text{ V} \leq \text{V}_{\text{IN}} \leq -33 \text{ V}$			0.5	mA
		5 mA ≤ lo ≤ 350 mA			0.4	mA
Output Noise Voltage	Vn	T _J = 25°C, 10 Hz ≤ f ≤ 100 kHz		200	440	μVr.m.s.
Ripple Rejection	R•R	$T_J = 25^{\circ}C$, $-22 \text{ V} \le V_{IN} \le -32 \text{ V}$, $f = 120 \text{ Hz}$, $I_0 = 100 \text{ mA}$	50	60		dB
Dropout Voltage	V _{DIF}	T _J = 25°C		1.1		V
Short Circuit Current	Oshort	T _J = 25°C, V _{IN} = -33 V		350		mA
Peak Output Current	lOpeak	T _J = 25°C	620	880	1020	mA
Temperature Coefficient of Output Voltage	ΔVo/ΔT	lo = 5 mA		0.8		mV/°C

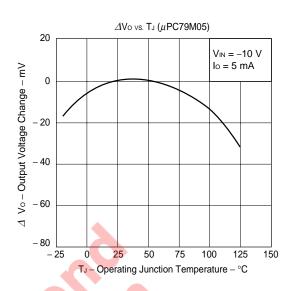
 μ PC79M24 (Vin = -33 V, lo = 350 mA, 0°C \leq TJ \leq +125°C, Cin = 2.2 μ F, Cout = 1 μ F, unless otherwise specified)

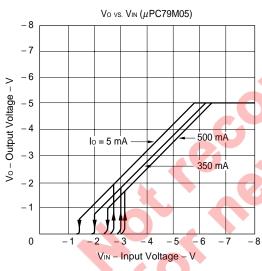
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo	T _J = 25°C	-23.0	-24.0	-25.0	V
	•	$-27 \text{ V} \le \text{V}_{\text{IN}} \le -38 \text{ V}, 5 \text{ mA} \le \text{Io} \le 350 \text{ mA}$	-22.8		-25.2	V
Line Regulation	REGIN	$T_J = 25^{\circ}C, -27 \text{ V} \le V_{IN} \le -38 \text{ V}$		30	80	mV
		T _J = 25°C, -30 V ≤ V _{IN} ≤ -36 V		25	50	mV
Load Regulation	REGL	T _J = 25°C, 5 mA ≤ I _O ≤ 500 mA		80	360	mV
		T _J = 25°C, 5 mA ≤ I _O ≤ 350 mA		50		mV
Quiescent Current	IBIAS	T _J = 25°C		4.5	6.0	mA
Quiescent Current Change	⊿IBIAS	-27 V ≤ V _{IN} ≤ -38 V			0.5	mA
		5 mA ≤ lo ≤ 350 mA			0.4	mA
Output Noise Voltage	Vn	T _J = 25°C, 10 Hz ≤ f ≤ 100 kHz		250	600	μVr.m.s.
Ripple Rejection	R•R	$T_J = 25^{\circ}C$, $-28 \text{ V} \le \text{V}_{IN} \le -38 \text{ V}$, $f = 120 \text{ Hz}$, $I_0 = 100 \text{ mA}$	50	57		dB
Dropout Voltage	V _{DIF}	T _J = 25°C		1.1		V
Short Circuit Current	Oshort	T _J = 25°C, V _{IN} = -38 V		200		mA
Peak Output Current	Opeak	T _J = 25°C	620	880	1020	mA
Temperature Coefficient of Output Voltage	⊿Vo/⊿T	lo = 5 mA		1.0		mV/°C

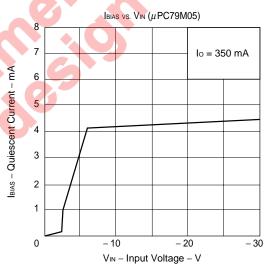
NEC

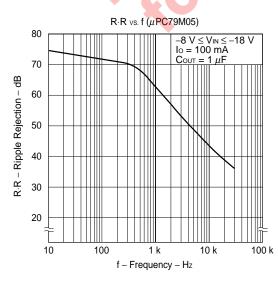
TYPICAL CHARACTERISTICS (T_J = 25°C, unless otherwise specified)

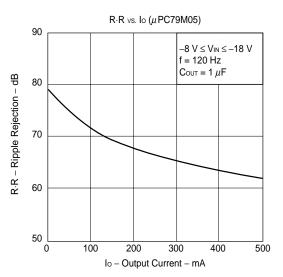


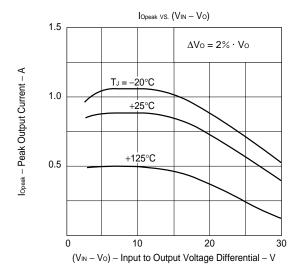


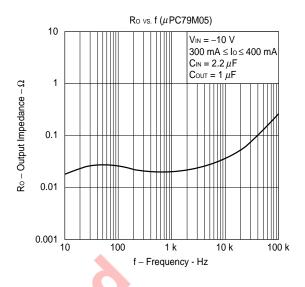


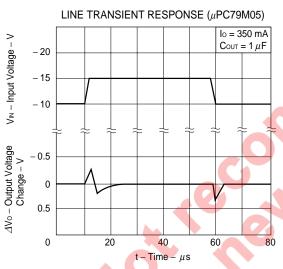


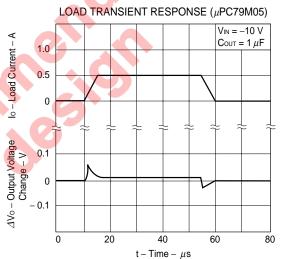






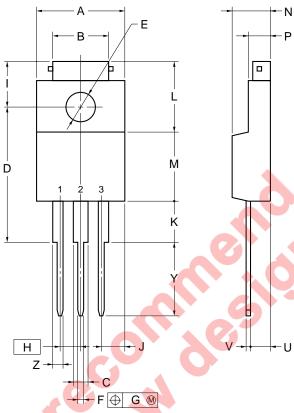






<R> PACKAGE DRAWING (Unit: mm)

3PIN PLASTIC SIP (MP-45G)



NOTE

Each lead centerline is located within 0.25 mm of its true position (T.P.) at maximum material condition.

MILLIMETERS
IIII LING
10.0±0.2
7.0±0.2
1.50±0.2
17.0±0.3
ϕ 3.3±0.2
0.75±0.10
0.25
2.54 (T.P.)
5.0±0.3
2.46±0.2
5.0±0.2
8.5±0.2
8.5±0.2
4.5±0.2
2.8±0.2
2.4±0.5
0.65±0.10
8.9±0.7
1.30±0.2

P3HF-254B-4

<R> RECOMMENDED SOLDERING CONDITIONS

The μ PC79Mxx Series should be soldered and mounted under the following recommended conditions.

For soldering methods and conditions other than those recommended below, contact an NEC Electronics sales representative.

For technical information, see the following website.

Semiconductor Device Mount Manual (http://www.necel.com/pkg/en/mount/index.html)

Through-hole devices

 μ PC79M05HF, 79M08HF, 79M12HF, 79M15HF, 79M18HF, 79M24HF, μ PC79M05HF-AZ, 79M08HF-AZ, 79M12HF-AZ, 79M15HF-AZ, 79M18HF-AZ, 79M24HF-AZ : 3-PIN PLASTIC SIP (MP-45G) (Isolated TO-220)

Process	Conditions	Symbol
Wave soldering	Solder temperature: 260°C or below, Flow time: 10 seconds or less,	WS60-00-1
(only to leads)	Maximum number of flow processes: 1 time.	
Partial heating method	Pin temperature: 350°C or below,	P350
	Heat time: 3 seconds or less (Per each pin).	

Caution For through-hole device, the wave soldering process must be applied only to leads, and make sure that the package body does not get jet soldered.

<R> REFERENCE DOCUMENTS

Document Name	Document No.
Usage of Three-Terminal Regulators User's Manual	G12702E
Semiconductor Device Mount Manual	http://www.necel.com/pkg/en/mount/index.html
Review of Quality and Reliability Handbook Information	C12769E

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