



74AUP2G3404

BUFFER AND INVERTER

Description

The Advanced Ultra Low Power (AUP) CMOS logic family is designed for low power and extended battery life in portable applications.

The 74AUP2G3404 has one buffer and one inverter. Both gates have push-pull outputs designed for operation over a power supply range of 0.8V to 3.6V. The device is fully specified for partial power down applications using I_{OFF} . The I_{OFF} circuitry disables the output preventing damaging current backflow when the device is powered down.

Features

- Advanced Ultra Low Power (AUP) CMOS
- Supply Voltage Range from 0.8V to 3.6V
- ± 4mA Output Drive at 3.0V
- Low Static power consumption
 - I_{CC} < 0.9µA
- Low Dynamic Power Consumption
 - C_{PD} = 6pF Typical at 3.6V
- Schmitt Trigger Action at All Inputs Make the Circuit Tolerant for Slower Input Rise and Fall Time. The hysteresis is typically 250mV at $V_{CC} = 3.0V$
- IOFF Supports Partial-Power-Down Mode Operation
 - ESD Protection per JESD 22
 - Exceeds 200-V Machine Model (A115)
 - Exceeds 2000-V Human Body Model (A114-)
 - Exceeds 1000-V Charged Device Model (C101)
- Latch-Up Exceeds 100mA per JESD 78, Class I
- Leadless packages per JESD30E
 - DFN1410 denoted as X2-DFN1410-6
 - DFN1010 denoted as X2-DFN1010-6
 - DFN0910 denoted as X2-DFN0910-6
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Click for Ordering Information

Pin Assignments



Applications

- Suited for battery and low power needs
- Wide array of products such as:
 - PCs, networking, Notebooks, Netbooks, PDAs
 - Tablet Computers, E-Readers
 - Computer Peripherals, Hard Drives, CD/DVD ROM
 - TV, DVD, DVR, Set Top Box
 - Cell Phones, Personal Navigation / GPS
 - MP3 players ,Cameras, Video Recorders



Pin Descriptions

Pin Name	Pin No.	Function
1A	1	Data Input
GND	2	Ground
2A	3	Data Input
2Y	4	Data Output
V _{CC}	5	Supply Voltage
1Y	6	Data Output

Logic Diagram



Function Tables

Input	Output
1A	1Y
Н	Н
L	L

Input	Output
2A	2Y
Н	L
L	Н



Symbol	Description	Rating	Unit	
ESD HBM	Human Body Model ESD Protection	2	KV	
ESD CDM	Charged Device Model ESD Protection	1	KV	
ESD MM	Machine Model ESD Protection	200	V	
V _{CC}	Supply Voltage Range	-0.5 to 4.6	V	
VI	Input Voltage Range	-0.5 to 4.6	V	
Vo	Voltage applied to output in high or low state	-0.5 to V _{CC} +0.5	V	
I _{IK}	Input Clamp Current V _I < 0	50	mA	
loк	Output Clamp Current ($V_O < 0$)	-50	mA	
Ι _Ο	Continuous Output Current (V _O = 0 to V _{CC})	±20	mA	
I _{CC}	Continuous Current Through V _{CC}	50	mA	
I _{GND}	Continuous Current Through GND	-50	mA	
TJ	Operating Junction Temperature	-40 to +150	°C	
T _{STG}	Storage Temperature	-65 to +150	°C	

Absolute Maximum Ratings (Note 4) (@T_A = +25°C, unless otherwise specified.)

Note: 4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

Recommended Operating Conditions (Note 5) (@T_A = +25°C, unless otherwise specified.)

Symbol		Parameter	Min	Max	Unit
V _{CC}	Operating Voltage	—	0.8	3.6	V
VI	Input Voltage		0	3.6	V
Vo	Output Voltage		0	Vcc	V
		$V_{CC} = 0.8V$	—	-20	μA
	High-Level Output Current	V _{CC} = 1.1V	—	-1.1	
I _{ОН}		$V_{CC} = 1.4V$	—	-1.7	
		V _{CC} = 1.65V	—	-1.9	mA
		$V_{CC} = 2.3V$	—	-3.1	
		$V_{CC} = 3.0V$	—	-4	
		$V_{CC} = 0.8V$	—	20	μA
		V _{CC} = 1.1V	—	1.1	
		$V_{CC} = 1.4V$	—	1.7	
I _{OL}	Low-Level Output Current	V _{CC} = 1.65V	—	1.9	mA
		$V_{CC} = 2.3V$	—	3.1	
		$V_{CC} = 3.0V$	—	4	
Δt/ΔV	Input transition rise or fall rate	$V_{CC} = 0.8V$ to 3.6V	_	200	ns/V
TA	Operating free-air temperature	—	-40	+125	°C

Note: 5. Unused inputs should be held at $V_{\mbox{\scriptsize CC}}$ or Ground.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Test Conditions	М	T _A = -	+25°C	T _A =-40	to +85°C	Unit
Symbol	Parameter	Test Conditions	Vcc	Min	Max	Min	Max	Unit
		_	0.8V to 1.65V	0.80 X V _{CC}	—	0.80 X V _{CC}	_	
Max	High-Level Input		1.65V to 1.95V	0.65 X V _{CC}	—	0.65 X V_{CC}	_	v
Vih	Voltage		2.3V to 2.7V	1.6	—	1.6	_	v
		<u> </u>	3.0V to 3.6V	2.0	—	2.0	_	
		_	0.8V to 1.65V	—	0.30 X V _{CC}	_	0.30 X V _{CC}	
V	Low-Level Input	<u> </u>	1.65V to 1.95V	_	0.35 X V _{CC}	_	0.35 X V _{CC}	V
VIL	Voltage		2.3V to 2.7V	—	0.7	_	0.7	v
		—	3.0V to 3.6V	_	0.9	_	0.9	
		I _{OH} = -20μA	0.8V to 3.6V	$V_{CC} - 0.1$	—	V _{CC} – 0.1	_	
		I _{OH} = -1.1mA	1.1V	0.75 X V _{CC}	_	0.7 X V _{CC}	_	
		I _{OH} = -1.7mA	1.4V	1.11	—	1.03	_	
V _{OH} High-Level Output Voltage	I _{OH} = -1.9mA	1.65V	1.32	—	1.3	_	v	
	I _{OH} = -2.3mA	2.3V	2.05	_	1.97	_	v	
		I _{OH} = -3.1mA	2.3V	1.9	—	1.85	_	
		I _{OH} = -2.7mA	3V	2.72	—	2.67	_	
		I _{OH} = -4mA	37	2.6	—	2.55	_	
		I _{OL} = 20μΑ	0.8V to 3.6V	—	0.1	_	0.1	
		I _{OL} = 1.1mA	1.1V	—	0.3 X V _{CC}	—	0.3 X V _{CC}	
		I _{OL} = 1.7mA	1.4V	—	0.31	_	0.37	
Vol	Low-Level Input	I _{OL} = 1.9mA	1.65V	—	0.31	_	0.35	v
VOL	Voltage	I _{OL} = 2.3mA	2.3V	—	0.31	—	0.33	v
		I _{OL} = 3.1mA	2.50	—	0.44	_	0.45	
		I _{OL} = 2.7mA	3V	_	0.31	_	0.33	
		I _{OL} = 4mA	30	_	0.44	_	0.45	
lı	Input Current	A or B Input	0V to 3.6V	—	± 0.1	_	± 0.5	μA
IOFF	Power Down	V_1 or V_0 = 0V to 3.6V	0V	—	± 0.2	_	± 0.6	μA
Δl _{OFF}	Delta Power	V_1 or $V_0 = 0V$ to 3.6V	0V to 0.2V	—	± 0.2	_	± 0.6	μA
lcc	Supply Current	$V_{I} = GND \text{ or } V_{CC}, I_{O} = 0$	0.8V to 3.6V	—	0.5	—	0.9	μA
ΔI _{CC}	Additional Supply	One input at V _{CC} –0.6V Other	3.3V	_	40	_	50	μA



Electrical Characteristics (cont.) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Test Conditions	N	T _A = -40	to 125°C	Unit	
Symbol	Parameter	Test Conditions	Vcc	Min	Max	Unit	
		—	0.8V to 1.65V	0.80 X V _{CC}	—		
VIH	High-Level Input Voltage	—	1.65V to 1.95V	0.70 X V _{CC}	—	v	
VIH		—	2.3V to 2.7V	1.6	—	v	
		—	3.0V to 3.6V	2.0			
		—	0.8V to 1.65V	—	0.25 X V_{CC}		
VIL	Low-Level Input Voltage	—	1.65V to 1.95V	—	0.30 X V _{CC}	V	
۷IL	Low-Level input voltage	—	2.3V to 2.7V	—	0.7	v	
		—	3.0V to 3.6V	_	0.9		
		I _{OH} = -20μA	0.8V to 3.6V	V _{CC} – 0.11	_		
		I _{OH} = -1.1mA	1.1V	0.6 X V _{CC}	—		
		I _{OH} = -1.7mA	1.4V	0.93	—		
		I _{OH} = -1.9mA	1.65V	1.17	—	N/	
V _{OH}	High-Level Output Voltage	I _{OH} = -2.3mA	0.0)/	1.77	—	V	
		I _{OH} = -3.1mA	2.3V	1.67	—		
		I _{OH} = -2.7mA	3V	2.40	_		
		I _{OH} = -4mA	30	2.30	—		
		I _{OL} = 20μA	0.8V to 3.6V	—	0.11		
		I _{OL} = 1.1mA	1.1V	—	0.33 X V _{CC}		
		I _{OL} = 1.7mA	1.4V	—	0.41		
.,		I _{OL} = 1.9mA	1.65V	_	0.39	v	
V _{OL}	Low-Level Input Voltage	I _{OL} = 2.3mA	0.0)/	—	0.36	v	
		I _{OL} = 3.1mA	2.3V	_	0.50		
		I _{OL} = 2.7mA	0) (_	0.36		
		I _{OL} = 4mA	3V		0.50		
l _l	Input Current	A or B Input, V _I = GND to 3.6V	0V to 3.6V	—	± 0.75	μA	
I _{OFF}	Power Down Leakage Current	V_1 or $V_0 = 0V$ to 3.6V	0V	—	± 1.0	μA	
Δl _{OFF}	Delta Power Down Leakage Current	V_1 or V_0 = 0V to 3.6V	0V to 0.2V	—	± 2.5	μA	
Icc	Supply Current	$V_I = GND \text{ or } V_{CC}, I_O = 0$	0.8V to 3.6V	—	1.4	μA	
ΔI _{CC}	Additional Supply Current	Input at V _{CC} –0.6V Other input at V _{CC} or GND	3.3V	_	75	μA	

Operating and Package Characteristics

Γ _A = +25°C					
	Parameter	Test Conditions	Vcc	Тур	Unit
			0.8V	5.1	
			1.2V ± 0.1V	5.2	
0	Dower dissinction consultance	f = 1MHz	1.5V ± 0.1V	5.2	~ F
C _{pd}	Power dissipation capacitance	No Load	1.8V ± 0.15V	5.5	pF
			2.5V ± 0.2V	5.7	
			3.3V ± 0.3V	6.0	
CI	Input Capacitance	$V_i = V_{CC} \text{ or } GND$	0V or 3.3V	2.0	pF
Co	Output Capacitance	$V_{O} = V_{CC} \text{ or } GND$	0V	3.5	pF



Switching Characteristics

$C_L = 5pF se$	From	TO OUTPUT		٦	T _A = +25°C			T _A = -40 to +85°C		T _A = -40 to +125°C	
Parameter	Input		Vcc	Min	Тур	Max	Min	Мах	Min	Max	Unit
		v	0.8V		16.0						
			1.2V ± 0.1V	2.4	5.0	10.3	2.0	11.4	2.0	12.6	-
	^		1.5V ± 0.1V	1.8	3.6	6.4	1.6	7.4	1.6	8.2	
t _{pd} A	Ŷ	1.8V ± 0.15V	1.5	2.9	5.0	1.4	5.9	1.4	6.5	ns	
			2.5V ± 0.2V	1.2	2.4	3.9	1.1	4.5	1.1	5.0	
			3.3V ± 0.3V	1.1	2.1	3.2	1.0	3.9	1.0	4.3	1

C_L = 10pF see Figure 1

Parameter	From	TO OUTPUT	N	T _A = +25°C			T _A = -40 to +85°C		T _A = -40 to	Unit	
Falameter	Input		V _{cc}	Min	Тур	Мах	Min	Max	Min	Max	Unit
			0.8V		19.8						
		X	1.2V ± 0.1V	2.8	5.9	12.2	2.3	13.7	2.3	15.1	
	^		1.5V ± 0.1V	2.3	4.2	7.5	1.9	8.7	1.9	9.6	
t _{pd}	A	ř	1.8V ± 0.15V	2.0	3.5	5.9	1.7	7.0	1.7	7.7	ns
			2.5V ± 0.2V	1.7	2.9	4.6	1.5	5.4	1.5	6.0	
			3.3V ± 0.3V	1.6	2.7	3.8	1.4	4.5	1.4	5.1	

C_L = 15pF see Figure 1

Parameter	From	TO OUTPUT	Vaa	$T_A = +25^{\circ}C$			T _A = -40 to +85°C		T _A = -40 to	Unit	
Farameter	Input		Vcc	Min	Тур	Max	Min	Мах	Min	Max	Unit
			0.8V		23.3						
			1.2V ± 0.1V	3.2	6.7	13.0	2.6	15.8	2.6	17.4	ns
	^	v	1.5V ± 0.1V	2.6	4.7	8.6	2.2	10.0	2.2	11.0	
t _{pd}	A	ř	1.8V ± 0.15V	2.3	4.0	6.7	2.0	8.0	2.0	8.8	
			2.5V ± 0.2V	2.1	3.3	5.1	1.8	6.1	1.8	6.8	
			3.3V ± 0.3V	2.0	3.1	4.2	1.6	5.0	1.6	5.5	

C_L = 30pF see Figure 1

Parameter	From	TO OUTPUT	N	T _A = +25°C			T _A = -40	to +85°C	T _A = -40 to	Unit	
i arameter	Input		V _{cc}	Min	Тур	Max	Min	Мах	Min	Мах	Unit
			0.8V		33.6						
		Y	1.2V ± 0.1V	4.4	8.9	16.3	3.6	19.0	3.6	20.9	
			1.5V ± 0.1V	3.6	6.3	10.8	3.2	12.9	3.2	14.2	
t _{pd}	A		1.8V ± 0.15V	3.2	5.3	9.0	2.9	10.5	2.9	11.6	ns
			2.5V ± 0.2V	2.4	4.5	6.5	2.6	7.6	2.6	8.5	
			3.3V ± 0.3V	2.2	4.2	5.6	2.2	6.2	2.2	7.2	



Parameter Measurement Information



N N	Inputs		N.	0
V _{cc}	VI	t _r /t _f	V _M	CL
0.8 V	V _{CC}	≤3ns	V _{CC} /2	5, 10, 15, 30pF
1.2V±0.1V	V _{CC}	≤3ns	V _{CC} /2	5, 10, 15, 30pF
1.5V±0.1V	V _{CC}	≤3ns	V _{CC} /2	5, 10, 15, 30pF
1.8V±0.15V	V _{CC}	≤3ns	V _{CC} /2	5, 10, 15, 30pF
2.5V±0.2V	Vcc	≤3ns	V _{CC} /2	5, 10, 15, 30pF
3.3V±0.3V	Vcc	≤3ns	V _{CC} /2	5, 10, 15, 30pF







Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs



Notes:

- A. Includes test lead and test apparatus capacitance.

 - B. All pulses are supplied at pulse repetition rate \leq 10 MHz. C. Inputs are measured separately one transition per measurement.
 - D. t_{PLH} and t_{PHL} are the same as $t_{PD.}$



Ordering Information



Device	Deekere Code	Packaging	7" Tape and Reel	
Device	Package Code	(Note 7)	Quantity	Part Number Suffix
74AUP2G3404FZ4-7	FZ4	X2-DFN1410-6	5000/Tape & Reel	-7
74AUP2G3404FW4-7	FW4	X2-DFN1010-6	5000/Tape & Reel	-7
74AUP2G3404FW3-7	FW3	X2-DFN0910-6	5000/Tape & Reel	-7

Note: 7. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf 8. For packaging details, go to our website at http://www.diodes.com/products/packages.html

Marking Information

(1) X2-DFN1410-6, X2-DFN1010-6, X2-DFN0910-6

(Top View)

	<u>XX</u> : Identification Code
XX	Y : Year : 0~9
YWX	₩: Week : A~Z : 1~26 week;
•	a~z: 27~52 week; z represents
	52 and 53 week
	\underline{X} : A~Z : Internal code

Part Number	Package	Identification Code
74AUP2G3404FZ4	X2-DFN1410-6	RU
74AUP2G3404FW4	X2-DFN1010-6	SU
74AUP2G3404FW3	X2-DFN0910-6	MU



Package Outline Dimensions (All dimensions in mm.)

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

(1) Package Type X2-DFN1410-6



X2-DFN1410-6			
Dim	Min	Max	Тур
Α		0.40	0.39
A1	0.00	0.05	0.02
A3			0.13
b	0.15	0.25	0.20
D	1.35	1.45	1.40
Е	0.95	1.05	1.00
е			0.50
L	0.25	0.35	0.30
Z			0.10
Z1	0.045	0.105	0.075
All	All Dimensions in mm		

(2) Package Type: X2-DFN1010-6





	X2-DFN1010-6			
Dim	Min	Max	Тур	
Α		0.40	0.39	
A1	0.00	0.05	0.02	
A3			0.13	
b	0.14	0.20	0.17	
b1	0.05	0.15	0.10	
D	0.95	1.05	1.00	
Е	0.95	1.05	1.00	
е		_	0.35	
L	0.35	0.45	0.40	
К	0.15			
Z			0.065	
All Dimensions in mm				

74AUP2G3404 Document number: DS36096 Rev. 4 - 2



Package Outline Dimensions (cont.) (All dimensions in mm.)

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

(3) Package Type: X2-DFN0910-6



	X2-DFN0910-6			
Dim	Min	Max	Тур	
Α	-	0.35	0.30	
A1	0	0.03	0.02	
b	0.10	0.20	0.15	
D	0.85	0.95	0.90	
E	0.95	1.05	1.00	
е	-	-	0.30	
κ	0.20	-	-	
K1	0.25	-	-	
L	0.25	0.35	0.30	
L1	0.30	0.40	0.35	
Z	-	-	0.075	
Z1	-	-	0.075	
All	All Dimensions in mm			



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

(1) Package Type X2-DFN1410-6



Dimensions	Value
Dimensions	(in mm)
С	0.500
G	0.250
X	0.250
X1	1.250
Y	0.525
Y1	1.250

(2) Package Type: X2-DFN1010-6



Dimensions	Value (in mm)
С	0.350
G	0.150
X	0.200
X1	0.900
Y	0.550
Y1	1.250

(3) Package Type: X2-DFN0910-6



Dimensions	Value (in mm)
G	0.100
G1	0.050
G2	0.150
Х	0.150
X1	0.750
Y	0.525
Y1	0.475
Y2	1.150



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