

Off-Line Digital Power Controller for LED Driver with High-Power Factor and Low-Ripple Current

1 Description

The iW3626 is a high performance, single-stage AC/DC power controller for LED luminaires with power factor (PF) correction. The device uses digital control technology to build unique hybrid mode control in PWM flyback power supplies to achieve high power factor meanwhile minimizing LED current ripple. This distinctive control approach enables the capability for users to make trade-offs between PF and LED current ripple in a single-stage design. It can achieve excellent LED current regulation over line and load variation, without the need for secondary feedback circuit. The built-in temperature sensor along with control logic can automatically adjust output current in real-time without visible flicker during the process. The iW3626 operates in quasi-resonant mode to provide high efficiency along with a number of key built-in protection features while minimizing the external component count, simplifying EMI design, and lowering the total bill of material cost. It also eliminates the need for loop compensation components while maintaining stability over all operating conditions. The built-in power limit function enables optimized transformer design in universal off-line applications with input voltage from 90V_{AC} to 277V_{AC}.

Dialog's innovative proprietary technology maximizes the iW3626 performance in a tiny SOT-23 package. The iW3626 offers two multi-function pins allowing users to configure PF and OTP as required with no cost or size impact, thereby providing design flexibility. The active start-up scheme enables the shortest possible start-up time without sacrificing active efficiency.

2 Features

- All-in-one non-dimmable low-cost off-line LED driver (isolated and non-isolated applications)
- Supports universal input voltage range (90V_{AC} to $277V_{AC}$) and output power up to 10W
- High power factor (PF) with low current-ripple control technology
- User-configurable power factor setting (>0.7 to >0.9)
- User-configurable over-temperature protection (OTP) with temperature-current derating
- Very tight LED current regulation (±5%) across line and load, and within primary inductance tolerance (±20%)
- Isolated design without opto-coupler

3 Applications

- Solid-state LED lighting
- LED lighting ballast

- Supports wide range of LED numbers with tight current regulation
- Stabilized LED current-ripple control without visible shimmer or flicker
- Active start-up scheme enables fastest possible startup
- 72kHz maximum PWM switching frequency with quasiresonant operation
- Dynamic base current control to drive low-cost BJT
- EZ-EMI® design enhances manufacturability
- Built-in single-point fault protection features: LED open-/short-circuit protection and over-current protection
- No audible noise over entire operating range

Produc	t Summary
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Figure 3.1 : iW3626 Typical Application Circuit (Non-isolated Application)



Figure 3.2 : iW3626 Typical Application Circuit (Isolated Application)



iW3626

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Figure 3.3 : iW3626 Typical Application Circuit (Isolated Application without Using Active Start-up Device)

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5 Pinout Description



Figure 5.1 : 6-Pin SOT23 Package

Pin No.	Pin Name	Туре	Pin Description
1	V _{CC}	Power Input	Power supply for control logic and BJT drive.
2	FB/OTP	Analog Input	Multi-function pin. Used for OTP current derating configuration at the beginning of start-up and to provide auxiliary voltage sense for primary regulation during normal operation.
3	ASU	Output	Control signal for active start-up device (BJT or depletion mode NFET).
4	CS/PF	Analog Input	Multi-function pin. Used for PF configuration at the beginning of start-up and to provide primary current sense for cycle-by-cycle peak current control and limit during normal operation.
5	GND	Ground	Ground.
6	OUTPUT	Output	Base drive for BJT.

4 Absolute Maximum Ratings

Absolute maximum ratings are the parameter values or ranges which can cause permanent damage if exceeded.

Parameter	Symbol	Value	Units
DC supply voltage range (pin 1, I _{CC} = 20mA max)	V _{cc}	-0.3 to 18.0	V
Continuous DC supply current at V_{CC} pin (V_{CC} = 15V)	I _{cc}	20	mA
ASU output (pin 3)		-0.3 to 18.0	V
OUTPUT (pin 6)		-0.3 to 4.0	V
FB/OTP input (pin 2, I _{FB/OTP} ≤ 10mA)		-0.7 to 4.0	V
CS/PF input (pin 4)		-0.3 to 4.0	V
Maximum junction temperature	T _{JMAX}	150	°C
Operating junction temperature	T _{JOPT}	-40 to 150	°C
Storage temperature	T _{STG}	-65 to 150	°C
Thermal resistance junction-to-ambient	θ _{JA}	190	°C/W
ESD rating per JEDEC JESD22-A114		±2,000	V
Latch-up test per JESD78A		±100	mA

iW3626

Off-Line Digital Power Controller for LED Driver with High-Power Factor and Low-Ripple Current 6 Physical Dimensions



Symbol	Millim	neters		
Syr	MIN	MAX		
А	0.90	1.45		
A1	0.00	0.15		
A2	0.90	1.30		
В	0.30	0.50		
С	0.09	0.20		
D	2.80	3.00		
Е	2.60	3.00		
E1	1.50	1.75		
е	0.95	0.95 BSC		
e1	1.90	1.90 BSC		
L	0.30	0.55		
α	0°	8°		

Compliant to JEDEC Standard MO-178AB

Controlling dimensions are in millimeters

This package is RoHS compliant and Halide free.

Soldering Temperature Resistance:

- [a] Package is IPC/JEDEC Std 020D Moisture Sensitivity Level 1
- [b] Package exceeds JEDEC Std No. 22-A111 for Solder Immersion Resistance; packages can withstand 10 s immersion < 260°C

Dimension D does not include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs shall not exceed 0.25 mm per side.

The package top may be smaller than the package bottom. Dimensions D and E1 are are determined at the outermost extremes of the plastic body exclusive of mold flash, tie bar burrs and interlead flash, but including any mismatch between top and bottom of the plastic body.

7 Ordering Information

Part No.	Options	Package	Description
iW3626-00	Maximum driver current = 40mA, wide V_{OUT} range	SOT-23	Tape & Reel ¹
iW3626-01	Maximum driver current = 40mA, narrow V_{OUT} range	SOT-23	Tape & Reel ¹
iW3626-02	Maximum driver current = 50mA, wide V _{OUT} range	SOT-23	Tape & Reel ¹

Note 1: Tape and reel packing quantity is 3,000/reel. Minimum ordering quantity is 3,000.



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Product Summary

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