### **CHANGE NOTIFICATION**



June 28, 2013

Dear Sir/Madam:

PCN# 062813

#### Subject: Notification of Change to LTC3612, LTC3614, LTC3615, LTC3616

Please be advised that Linear Technology Corporation has made minor changes to the LTC3612, LTC3614, LTC3615/-1 and LTC3616 family of monolithic synchronous buck converters. A minor mask revision was made to improve application robustness and efficiency at low switching frequencies. The original production silicon turns off the low side switch too early in some applications, which leads to current flow in the body diode. The revised silicon avoids this limitation and improves efficiency in low frequency (<600 KHz) Burst Mode operation. Further, the external Soft Start function was improved to keep the output voltage in regulation during fast drops in input voltage.

In addition, in order to improve manufacturability, the specification for the minimum top switch current limit at 100% duty cycle will be reduced slightly for the LTC3612, LTC3614 and LTC3616 as shown in the attached redlined specifications. The LTC3615/LTC3615-1 specifications and datasheet are unchanged.

These changes were qualified by performing characterization over the full operating junction temperature range and through rigorous engineering bench evaluations. In addition, each product successfully completed 1000 hours of HTOL stress. Product built using the new die and tested to the updated specification will be shipped with a datecode of approximately 1348.

Should you have any further questions, please feel free to contact me at 408-432-1900 ext. 2077, or by email at <u>JASON.HU@LINEAR.COM</u>. If I do not hear from you by August 29<sup>th</sup>, 2013, we will consider this change to be approved by your company.

Sincerely,

Jason Hu Quality Assurance Engineer

# **ELECTRICAL CHARACTERISTICS** The $\bullet$ denotes the specifications which apply over the full operating junction temperature range, otherwise specifications are at T<sub>A</sub> = 25°C. V<sub>IN</sub> = 3.3V, RT/SYNC = SV<sub>IN</sub>, unless otherwise specified (Note 2).

| SYMBOL                        | PARAMETER                                       | CONDITIONS                                                                                                                                                        |   | MIN                    | TYP  | MAX                     | UNITS    |
|-------------------------------|-------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|------------------------|------|-------------------------|----------|
| VIN                           | Operating Voltage Range                         |                                                                                                                                                                   | ٠ | 2.25                   |      | 5.5                     | ١        |
| V <sub>UVLO</sub>             | Undervoltage Lockout Threshold                  | SV <sub>IN</sub> Ramping Down<br>SV <sub>IN</sub> Ramping Up                                                                                                      | : | 1.7                    |      | 2.25                    | 1        |
| V <sub>FB</sub>               | Feedback Voltage Internal Reference             | $\begin{array}{l} (Notes 3, 4) \; V_{TRACK/SS} = SV_{IN}, \; V_{DDR} = 0V \\ 0^{\circ}C < T_{J} < 85^{\circ}C \\ -40^{\circ}C < T_{J} < 125^{\circ}C \end{array}$ | • | 0.594<br>0.591         | 0.6  | 0.606<br>0.609          |          |
|                               | Feedback Voltage External Reference<br>(Note 7) | (Notes 3, 4) V <sub>TRACK/SS</sub> = 0.3V, V <sub>DDR</sub> = SV <sub>IN</sub>                                                                                    |   | 0.289                  | 0.3  | 0.311                   | ١        |
|                               |                                                 | (Notes 3, 4) V <sub>TRACK/SS</sub> = 0.5V, V <sub>DDR</sub> = SV <sub>IN</sub>                                                                                    |   | 0.489                  | 0.5  | 0.511                   | ١        |
| I <sub>FB</sub>               | Feedback Input Current                          | V <sub>FB</sub> = 0.6V                                                                                                                                            | ٠ |                        |      | ±30                     | n/       |
| $\Delta V_{LINEREG}$          | Line Regulation                                 | $SV_{IN} = PV_{IN} = 2.25V \text{ to } 5.5V$<br>(Notes 3, 4) TRACK/SS = $SV_{IN}$                                                                                 | • |                        |      | 0.2                     | %/\      |
| ΔV <sub>LOADREG</sub>         | Load Regulation                                 | ITH from 0.5V to 0.9V (Notes 3, 4) $V_{ITH} = SV_{IN}$ (Note 5)                                                                                                   |   |                        |      | 0.25<br>2.6             | 9/<br>9/ |
| I <sub>S</sub>                | Active Mode                                     | V <sub>FB</sub> = 0.5V, V <sub>MODE</sub> = SV <sub>IN</sub> (Note 6)                                                                                             |   |                        | 1100 |                         | μ/       |
|                               | Sleep Mode                                      | $V_{FB} = 0.7$ V, $V_{MODE} = 0$ V, ITH = SV <sub>IN</sub><br>(Note 5)                                                                                            |   |                        | 70   | 100                     | µ/       |
|                               |                                                 | V <sub>FB</sub> = 0.7V, V <sub>MODE</sub> = 0V (Note 4)                                                                                                           |   |                        | 120  | 160                     | μ/       |
|                               | Shutdown                                        | $SV_{IN} = PV_{IN} = 5.5V$ , $V_{RUN} = 0V$                                                                                                                       |   |                        | 0.1  | 1                       | μ/       |
| R <sub>DS(ON)</sub>           | Top Switch On-Resistance                        | PVIN = 3.3V (Note 10)                                                                                                                                             |   |                        | 70   |                         | mΩ       |
|                               | Bottom Switch On-Resistance                     | PVIN = 3.3V (Note 10)                                                                                                                                             |   |                        | 45   |                         | mΩ       |
| ILIM                          | Top Switch Current Limit                        | Sourcing (Note 8), V <sub>FB</sub> = 0.5V<br>Duty Cycle <35%<br>Duty Cycle = 100%                                                                                 |   | 5.2<br>4 3.7           | 6    | 6.8                     | ļ        |
|                               | Bottom Switch Current Limit                     | Sinking (Note 8), V <sub>FB</sub> = 0.7V,<br>Forced Continuous Mode                                                                                               |   | -3                     | -4   | -5                      | ŀ        |
| gm(EA)                        | Error Amplifier Transconductance                | -5μA < I <sub>ITH</sub> < 5μA (Note 4)                                                                                                                            |   |                        | 200  |                         | μ        |
| I <sub>EAO</sub>              | Error Amplifier Max Output Current              | (Note 4)                                                                                                                                                          |   |                        | ±30  |                         | μ/       |
| t <sub>ss</sub>               | Internal Soft-Start Time                        | V <sub>FB</sub> from 0.06V to 0.54V,<br>TRACK/SS = SV <sub>IN</sub>                                                                                               |   | 0.65                   | 1    | 1.5                     | m        |
| VTRACK/SS                     | Enable Internal Soft-Start                      | (Note 7)                                                                                                                                                          |   | 0.62                   |      |                         | ١        |
| t <sub>TRACK/SS_DIS</sub>     | Soft-Start Discharge Time at<br>Start-Up        |                                                                                                                                                                   |   | 70                     |      |                         | μs       |
| Ron(track/ss_dis)             | TRACK/SS Pull-Down Resistor at<br>Start-Up      |                                                                                                                                                                   |   |                        |      | 200                     | Ω        |
| f <sub>osc</sub>              | Oscillator Frequency                            | RT/SYNC = 370k                                                                                                                                                    | ٠ | 0.8                    | 1    | 1.2                     | MH       |
|                               | Internal Oscillator Frequency                   | V <sub>RT/SYNC</sub> = SV <sub>IN</sub>                                                                                                                           | • | 1.8                    | 2.25 | 2.7                     | MH       |
| fsync                         | Synchronization Frequency                       |                                                                                                                                                                   |   | 0.3                    |      | 4                       | MH       |
| V <sub>RT/SYNC</sub>          | SYNC Level High                                 |                                                                                                                                                                   |   | 1.2                    |      |                         | ١        |
|                               | SYNC Level Low                                  |                                                                                                                                                                   |   |                        |      | 0.3                     | ١        |
| I <sub>SW(LKG)</sub>          | Switch Leakage Current                          | $SV_{IN} = PV_{IN} = 5.5V$ , $V_{RUN} = 0V$                                                                                                                       |   |                        | 0.1  | 1                       | μA       |
| V <sub>DDR</sub>              | DDR Option Enable Voltage                       |                                                                                                                                                                   |   | SV <sub>IN</sub> – 0.3 |      |                         | \        |
| V <sub>MODE</sub><br>(Note 9) | Internal Burst Mode Operation                   |                                                                                                                                                                   |   |                        |      | 0.3                     | \        |
|                               | Pulse-Skipping Mode                             |                                                                                                                                                                   |   | SV <sub>IN</sub> – 0.3 |      |                         | ١        |
|                               | Forced Continuous Mode                          |                                                                                                                                                                   |   | 1.1                    |      | SV <sub>IN</sub> • 0.58 | ١        |
|                               | External Burst Mode Operation                   |                                                                                                                                                                   |   | 0.45                   |      | 0.8                     | ١        |



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SYMBOL PARAMETER CONDITIONS MIN TYP MAX UNITS 2.25 5.5 ۷ VIN Operating Voltage Range • SVIN Ramping Down V<sub>UVLO</sub> Undervoltage Lockout Threshold 1.7 v ٠ SVIN Ramping Up • 2.25 ۷ (Note 3)  $V_{TRACK} = SV_{IN}$ ,  $V_{DDR} = 0V$   $0^{\circ}C < T_J < 85^{\circ}C$ VFB Feedback Voltage Internal Reference 0.594 0.6 0.606 ۷ -40°C < T<sub>J</sub> < 125°C 0.591 0.609 V . v Feedback Voltage External Reference (Note 3) V<sub>TRACK</sub> = 0.3V, V<sub>DDR</sub> = SV<sub>IN</sub> 0.288 0.300 0.312 (Note 7) ۷ (Note 3) V<sub>TRACK</sub> = 0.5V, V<sub>DDR</sub> = SV<sub>IN</sub> 0.488 0.500 0.512  $V_{FB} = 0.6V$ Feedback Input Current • ±30 nΑ I<sub>FB</sub> SV<sub>IN</sub> = PV<sub>IN</sub> = 2.25V to 5.5V (Notes 3, 4) TRACK/SS = SV<sub>IN</sub> Line Regulation 0.2 %/V ΔV<sub>LINEREG</sub> . ITH from 0.5V to 0.8V (Notes 3, 4) %  $\Delta V_{LOADREG}$ Load Regulation 0.25 VITH = SVIN (Note 5) 2.6 % V<sub>FB</sub> = 0.5V, V<sub>MODE</sub> = SV<sub>IN</sub> (Note 6) Active Mode Supply Current 1100 ls μA V<sub>FB</sub> = 0.7V, V<sub>MODE</sub> = 0V, ITH = SV<sub>IN</sub> Sleep Mode Supply Current 75 100 μA (Note 5) VFB = 0.7V, VMODE = 0V (Note 4) 175 130 μA Shutdown Current SVIN = PVIN = 5.5V, VRUN = 0V 0.1 1 uА Top Switch On-Resistance PV<sub>IN</sub> = 3.3V (Note 10) R<sub>DS(ON)</sub> 35 mΩ Bottom Switch On-Resistance PV<sub>IN</sub> = 3.3V (Note 10) 25 mΩ Sourcing (Note 8), VFB = 0.5V Top Switch Current Limit LIM Duty Cycle <35% 9 10.5 7.5 A Duty Cycle = 100% 5.8 5 A Sinking (Note 8), V<sub>FB</sub> = 0.7V, Bottom Switch Current Limit -6 -8 A -11 Forced Continuous Mode Error Amplifier Transconductance -5µA < I<sub>ITH</sub> < 5µA (Note 4) 200 μS g<sub>m(EA)</sub> Error Amplifier Maximum Output I<sub>EA0</sub> (Note 4) ±30 μA Current V<sub>FR</sub> from 0.06V to 0.54V. t<sub>ss</sub> Internal Soft-Start Time 0.65 1.2 1.9 ms TRACK/SS = SVIN Enable Internal Soft-Start (Note 7) 0.62 ۷ VTRACK/SS Soft-Start Discharge Time at Start-Up 60 μs t<sub>TRACK/SS\_DIS</sub> TRACK/SS Pull-Down Resistor at 200 RON(TRACK/SS\_DIS) Ω Start-Up RT/SYNC = 370k MHz Oscillator Frequency . 0.8 1 1.2 fosc Internal Oscillator Frequency 1.8 2.25 2.7 MHz  $V_{RT/SYNC} = SV_{IN}$ . Synchronization Frequency Range 0.3 4 MHz fsync V SYNC Input Threshold High 1.2 VRT/SYNC ۷ SYNC Input Threshold Low 0.3 Switch Leakage Current SVIN = PVIN = 5.5V, VRUN = 0V 0.1 1 μA SW(LKG) ۷ V<sub>DDR</sub> **DDR Option Enable Voltage** SV<sub>IN</sub> - 0.3 V<sub>MODE</sub> (Note 9) ۷ Internal Burst Mode Operation 0.3 Pulse-Skipping Mode ٧ SV<sub>IN</sub> - 0.3 ۷ Forced Continuous Mode 1.1 SVIN • 0.58

**ELECTRICAL CHARACTERISTICS** The  $\bullet$  denotes the specifications which apply over the full operating junction temperature range, otherwise specifications are at T<sub>A</sub> = 25°C. V<sub>IN</sub> = 3.3V, RT/SYNC = SV<sub>IN</sub> unless otherwise specified (Notes 1, 2, 11).



External Burst Mode Operation

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0.45

3614fa

٧

0.8

3616fa

3

| SYMBOL                        | PARAMETER                                       | CONDITIONS                                                                                                                                                                                                                                                               |   | MIN                    | TYP   | MAX                     | UNITS  |
|-------------------------------|-------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|------------------------|-------|-------------------------|--------|
| VIN                           | Operating Voltage Range                         |                                                                                                                                                                                                                                                                          | • | 2.25                   |       | 5.5                     | ١      |
| V <sub>UVLO</sub>             | Undervoltage Lockout Threshold                  | SV <sub>IN</sub> Ramping Down<br>SV <sub>IN</sub> Ramping Up                                                                                                                                                                                                             | : | 1.7                    |       | 2.25                    | 1      |
| V <sub>FB</sub>               | Feedback Voltage Internal Reference             | $\begin{array}{l} (\text{Note 3)} \ \text{V}_{\text{TRACK}} = \text{SV}_{\text{IN}}, \ \text{V}_{\text{DDR}} = 0\text{V} \\ 0^{\circ}\text{C} < \text{T}_{\text{J}} < 85^{\circ}\text{C} \\ -40^{\circ}\text{C} < \text{T}_{\text{J}} < 125^{\circ}\text{C} \end{array}$ | • | 0.594<br>0.591         | 0.6   | 0.606<br>0.609          | 1      |
|                               | Feedback Voltage External Reference<br>(Note 7) | (Note 3) V <sub>TRACK</sub> = 0.3V, V <sub>DDR</sub> = SV <sub>IN</sub>                                                                                                                                                                                                  |   | 0.275                  | 0.300 | 0.325                   | \      |
|                               |                                                 | (Note 3) V <sub>TRACK</sub> = 0.5V, V <sub>DDR</sub> = SV <sub>IN</sub>                                                                                                                                                                                                  |   | 0.475                  | 0.500 | 0.525                   | \      |
| I <sub>FB</sub>               | Feedback Input Current                          | V <sub>FB</sub> = 0.6V                                                                                                                                                                                                                                                   | ٠ |                        |       | ±30                     | n/     |
| ΔV <sub>LINEREG</sub>         | Line Regulation                                 | $SV_{IN} = PV_{IN} = 2.25V \text{ to } 5.5V$<br>(Notes 3, 4) TRACK/SS = $SV_{IN}$                                                                                                                                                                                        | • |                        |       | 0.2                     | %/\    |
| ΔV <sub>LOADREG</sub>         | Load Regulation                                 | ITH from 0.5V to 0.9V (Notes 3, 4)<br>V <sub>ITH</sub> = SV <sub>IN</sub> (Note 5)                                                                                                                                                                                       |   |                        |       | 0.25<br>2.6             | %<br>% |
| IS                            | Active Mode                                     | $V_{FB} = 0.5V$ , $V_{MODE} = SV_{IN}$ (Note 6)                                                                                                                                                                                                                          |   |                        | 1100  |                         | μA     |
|                               | Sleep Mode                                      | $V_{FB}$ = 0.7V, $V_{MODE}$ = 0V, ITH = $SV_{IN}$ (Note 5)                                                                                                                                                                                                               |   |                        | 75    | 100                     | μA     |
|                               |                                                 | V <sub>FB</sub> = 0.7V, V <sub>MODE</sub> = 0V (Note 4)                                                                                                                                                                                                                  |   |                        | 130   | 175                     | μA     |
|                               | Shutdown                                        | $SV_{IN} = PV_{IN} = 5.5V$ , $V_{RUN} = 0V$                                                                                                                                                                                                                              |   |                        | 0.1   | 1                       | μA     |
| R <sub>DS(ON)</sub>           | Top Switch On-Resistance                        | PV <sub>IN</sub> = 3.3V (Note 10)                                                                                                                                                                                                                                        |   |                        | 35    |                         | mΩ     |
|                               | Bottom Switch On-Resistance                     | PV <sub>IN</sub> = 3.3V (Note 10)                                                                                                                                                                                                                                        |   |                        | 25    |                         | mΩ     |
| ILIM                          | Top Switch Current Limit                        | Sourcing (Note 8), V <sub>FB</sub> = 0.5V<br>Duty Cycle <35%<br>Duty Cycle = 100%                                                                                                                                                                                        |   | 10.5<br>,8′ <b>7.6</b> | 12    | 13.5                    | A      |
|                               | Bottom Switch Current Limit                     | Sinking (Note 8), V <sub>FB</sub> = 0.7V,<br>Forced Continuous Mode                                                                                                                                                                                                      |   | -6                     | -8    | -11                     | A      |
| g <sub>m(EA)</sub>            | Error Amplifier Transconductance                | -5μA < I <sub>ITH</sub> < 5μA (Note 4)                                                                                                                                                                                                                                   |   |                        | 200   |                         | μS     |
| IEAO                          | Error Amplifier Maximum Output<br>Current       | (Note 4)                                                                                                                                                                                                                                                                 |   |                        | ±30   |                         | μA     |
| t <sub>ss</sub>               | Internal Soft-Start Time                        | V <sub>FB</sub> from 0.06V to 0.54V,<br>TRACK/SS = SV <sub>IN</sub>                                                                                                                                                                                                      |   | 0.65                   | 1.2   | 1.9                     | ms     |
| VTRACK/SS                     | Enable Internal Soft-Start                      | (Note 7)                                                                                                                                                                                                                                                                 |   | 0.62                   |       |                         | 1      |
| t <sub>track/ss_dis</sub>     | Soft-Start Discharge Time at Start-Up           |                                                                                                                                                                                                                                                                          |   | 60                     |       |                         | με     |
| R <sub>ON(TRACK/SS_DIS)</sub> | TRACK/SS Pull-Down Resistor at<br>Start-Up      |                                                                                                                                                                                                                                                                          |   |                        |       | 200                     | Ω      |
| f <sub>osc</sub>              | Oscillator Frequency                            | RT/SYNC = 370k                                                                                                                                                                                                                                                           | • | 0.8                    | 1     | 1.2                     | MHz    |
|                               | Internal Oscillator Frequency                   | V <sub>RT/SYNC</sub> = SV <sub>IN</sub>                                                                                                                                                                                                                                  | • | 1.8                    | 2.25  | 2.7                     | MHz    |
| fsync                         | Synchronization Frequency Range                 |                                                                                                                                                                                                                                                                          |   | 0.3                    |       | 4                       | MHz    |
| Vrt/sync                      | SYNC Level High                                 |                                                                                                                                                                                                                                                                          |   | 1.2                    |       |                         | \      |
|                               | SYNC Level Low                                  |                                                                                                                                                                                                                                                                          |   |                        |       | 0.3                     | 1      |
| I <sub>SW(LKG)</sub>          | Switch Leakage Current                          | $SV_{IN} = PV_{IN} = 5.5V$ , $V_{RUN} = 0V$                                                                                                                                                                                                                              |   |                        | 0.1   | 1                       | μA     |
| V <sub>DDR</sub>              | DDR Option Enable Voltage                       |                                                                                                                                                                                                                                                                          |   | SV <sub>IN</sub> – 0.3 |       |                         | \      |
| V <sub>MODE</sub><br>(Note 9) | Internal Burst Mode Operation                   |                                                                                                                                                                                                                                                                          |   |                        |       | 0.3                     | \      |
|                               | Pulse-Skipping Mode                             |                                                                                                                                                                                                                                                                          |   | SV <sub>IN</sub> – 0.3 |       |                         | \      |
|                               | Forced Continuous Mode                          |                                                                                                                                                                                                                                                                          |   | 1.1                    |       | SV <sub>IN</sub> • 0.58 | \      |
|                               | External Burst Mode Operation                   |                                                                                                                                                                                                                                                                          |   | 0.45                   |       | 0.8                     | ١      |

## **ELECTRICAL CHARACTERISTICS** The $\bullet$ denotes the specifications which apply over the full operating junction temperature range, otherwise specifications are at T<sub>1</sub> = 25°C. V<sub>IN</sub> = 3.3V. RT/SYNC = SV<sub>IN</sub> unless otherwise specified (Notes 1, 2, 11).



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