**Product data sheet** 

# 1. General description

NPN high-voltage transistor in a SOT223 (SC73) Surface-Mounted Device plastic package.

### 2. Features and benefits

- Low current (max. 300 mA)
- High voltage (max. 400 V)
- Qualified according to AEC-Q101 and recommended for use in automotive applications

# 3. Applications

Telecommunication

## 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	500	V
I <sub>C</sub>	collector current		-	-	300	mA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 1 mA; T <sub>amb</sub> = 25 °C	40	-	-	

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	Е	emitter	4	С
2	С	collector		
3	В	base		B — ,
4	С	collector	□1 □2 □3	Ė
			SC-73 (SOT223)	sym123



#### NPN high-voltage transistor

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package						
	Name	Description	Version				
PZTA44-Q		plastic, surface-mounted package with increased heatsink; 4 leads; 2.3 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	SOT223				

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PZTA44-Q	PZTA44

# 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	500	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	500	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	6	V
I <sub>C</sub>	collector current			-	300	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	300	mA
I <sub>BM</sub>	peak base current			-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1] [2]	-	1.35	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

## 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	91	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	10	K/W

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

<sup>[2]</sup> For other mounting conditions, see "Thermal considerations for SOT223 in the General Part of associated Handbook".

<sup>[2]</sup> For other mounting conditions, see "Thermal considerations for SOT223 in the General Part of associated Handbook".

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# 10. Characteristics

### **Table 7. Characteristics**

Symbol	Parameter	Conditions	Mi	n Typ	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 400 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
	current	V <sub>CB</sub> = 400 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	10	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 4 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 1 mA; T <sub>amb</sub> = 25 °C	40	-	-	
		$V_{CE}$ = 10 V; $I_{C}$ = 10 mA; pulsed; $t_{p}$ ≤ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	50	-	200	
		$V_{CE}$ = 10 V; $I_{C}$ = 50 mA; pulsed; $t_{p}$ ≤ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	45	-	-	
		$V_{CE}$ = 10 V; $I_{C}$ = 100 mA; pulsed; $t_{p} \le$ 300 µs; $T_{amb}$ = 25 °C	40	-	-	
V <sub>CEsat</sub>	collector-emitter	$I_C = 1 \text{ mA}; I_B = 0.1 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$	-	-	400	mV
	saturation voltage	$I_C$ = 10 mA; $I_B$ = 1 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	-	500	mV
		$I_C$ = 50 mA; $I_B$ = 5 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	-	750	V
$V_{BEsat}$	base-emitter saturation voltage	$I_C$ = 10 mA; $I_B$ = 1 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	-	850	mV
f <sub>T</sub>	transition frequency	$V_{CE}$ = 10 V; $I_{C}$ = 10 mA; f = 100 MHz; $T_{amb}$ = 25 °C	20	-	-	MHz
C <sub>c</sub>	collector capacitance	$V_{CB} = 20 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 \text{ °C}$	-	-	7	pF
C <sub>e</sub>	emitter capacitance	$V_{EB}$ = 500 mV; $I_{C}$ = 0 A; $i_{c}$ = 0 A; $f$ = 1 MHz; $T_{amb}$ = 25 °C	-	-	180	pF

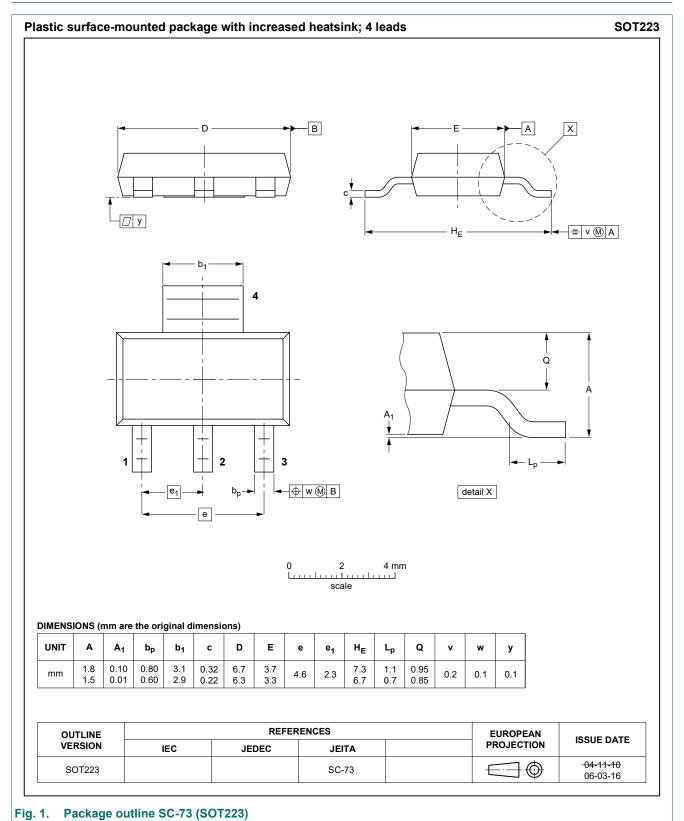
# 11. Test information

## **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

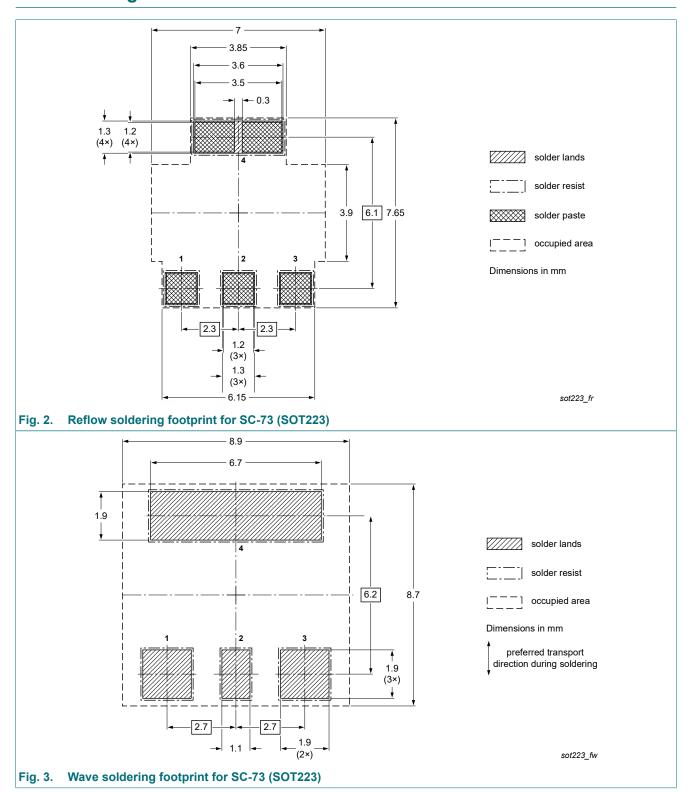
## NPN high-voltage transistor

# 12. Package outline



## NPN high-voltage transistor

# 13. Soldering



# NPN high-voltage transistor

# 14. Revision history

### **Table 8. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PZTA44-Q v.1	20211209	Product data sheet	-	-

#### NPN high-voltage transistor

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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