### **Product Specifications**

Customer	
Description	2.7" TFT EPD Panel
Model Name	EM027AS011
Date	2011/06/08
Doc. No.	1P008-00
Revision	01

Customer Approval						
Date						
•	The above signature represents that the product specifications, testing regulation, and warranty in the specifications are accepted					

Design Engineering			
Approval	Check	Design	
養+ 2011.06.09., 賞集+	★+ 2011.06.09., 富味+	蘇 2011.06.08 小雯	

No.18, Shengli 1st St., Rende Dist., Tainan City 71758, Taiwan (R.O.C.) Tel: +886-6-279-5399 Fax: +886-6-279-7630

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### - CONTENTS -

REVISION HISTORY	 3
1. GENERAL DESCRIPTION 1.1 OVERVIEW 1.2 FEATURES 1.3 APPLICATION 1.4 GENERAL SPECIFICATIONS 1.5 MECHANICAL SPECIFICATIONS	 4
2. ABSOLUTE MAXIMUM RATINGS 2.1 ABSOLUTE RATINGS OF ENVIRONMENT	 6
<ol> <li>ELECTRICAL CHARACTERISTICS</li> <li>3.1 ABSOLUTE MAXIMUM RATINGS OF PANEL</li> <li>3.2 RECOMMENDED OPERATION CONDITIONS OF PANEL</li> </ol>	 7
4. APPLICATION CIRCUIT BLOCK DIAGRAM	 8
5. INPUT TERMINAL PIN ASSIGNMENT 5.1 INPUT TERMINAL PIN ASSIGNMENT 5.2 REFERENCE CIRCUIT	 9
6. OPTICAL CHARACTERISTICS 6.1 TEST CONDITIONS 6.2 OPTICAL SPECIFICATIONS	 11
7. PACKING	 12
8. PRECAUTIONS	 14
9. DEFINITION OF LABELS	 15

Doc. No. 1P008-00

#### **REVISION HISTORY**

Version	Date	Page (New)	Section	Description
Ver 01	2011/6/8	All	All	Product specification first issued.

#### **1. GENERAL DESCRIPTION**

#### 1.1 OVERVIEW

This is a 2.7" a-Si, active matrix TFT, Electronic Paper Display (EPD) panel. The panel has such high resolution (117 dpi) that it is able to easily display fine patterns such as Japanese Kanji and barcodes. Due to its bi-stable nature, the EPD panel requires very little power to update and needs no power to maintain an image.

#### **1.2 FEATURES**

- a-Si TFT active matrix Electronic Paper Display
- High Resolution: 264 x 176
- Ultra low power consumption
- Super Wide Viewing Angle near 180°
- Extra thin & light
- SPI interface

#### **1.3 APPLICATION**

- Electronic shelf labels (ESLs)
- e-POP/e-POS systems
- Electronic bulletins

#### **1.4 GENERAL SPECIFICATIONS**

Specification	Unit	Note
70.42(H) x 45.8(V) x 1.0(T)	mm	(1)
57.288(H)x 38.192 (V)	mm	
a-Si TFT active matrix	-	
264 x 176	pixel	
0.217 x 0.217 (117dpi)	mm	
Vertical stripe	-	
Black/White	-	
Anti-Glare	-	
	70.42(H) x 45.8(V) x 1.0(T) 57.288(H) x 38.192 (V) a-Si TFT active matrix 264 x 176 0.217 x 0.217 (117dpi) Vertical stripe Black/White	70.42(H) x 45.8(V) x 1.0(T)       mm         57.288(H) x 38.192 (V)       mm         a-Si TFT active matrix       -         264 x 176       pixel         0.217 x 0.217 (117dpi)       mm         Vertical stripe       -         Black/White       -

Note (1) Not include FPC.

#### 1.5 MECHANICAL SPECIFICATIONS

Item		Min.	Тур.	Max.	Unit	Note
	Horizontal(H)		70.42		mm	
Glass Size	Vertical(V)		45.8		mm	
	Thickness(T)		1.0		mm	(1)
Weight			6.3		g	

Note (1) Not include Masking Film.



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#### 2. ABSOLUTE MAXIMUM RATINGS

#### 2.1 ABSOLUTE RATINGS OF ENVIRONMENT

ltem	Svmbol	Va	lue	Unit	Note	
item	Symbol	Min.	Max.	Onit	Note	
Storage Temperature	T <sub>ST</sub>	-20	+60	°C	(1)	
Operating Ambient Temperature	T <sub>OP</sub>	0	+50	°C	(1), (2)	

Note (1) (a) 90 %RH Max. (Ta  $\leq$  40 °C).

(b) Wet-bulb temperature should be 39 °C Max. (Ta > 40 °C).(c) No condensation.

Note (2) The temperature of panel display surface area should be 0 °C Min. and 50 °C Max. Refresh time depends on operation temperature.



#### **3. ELECTRICAL CHARACTERISTICS**

#### 3.1 ABSOLUTE MAXIMUM RATINGS OF PANEL

Parameter	Symbol	Va	alue	Unit	Note
Falameter	Symbol	Min.	Max.		NOLE
Digital Power	V <sub>DD</sub>	-0.3	5.0	V	
Analog Power	V <sub>CC</sub>	-0.3	5.0	V	

#### 3.2 RECOMMENDED OPERATION CONDITIONS OF PANEL

Parameter	Symbol	Value			Unit Note	Noto
Falameter	Symbol	Min.	Тур.	Max.	Onit	Note
Digital Power	V <sub>DD</sub>	2.7	3.0	3.3	V	
Analog Power	V <sub>cc</sub>	2.7	3.0	3.3	V	

Ta = 25 ± 2 °C

Ta = 25 ± 2 ℃

#### 4. APPLICATION CIRCUIT BLOCK DIAGRAM



#### 5. INPUT TERMINAL PIN ASSIGNMENT & REFERENCE CIRCUIT

#### 5.1 INPUT TERMINAL PIN ASSIGNMENT

No	Signal	I/O	Connected to	Function
1	/ČS	Ι	MCU	Chip Select. Low enable
2	BUSY	0	MCU	When BUSY = 1, EPD stays in busy state that EPD ignores any input data from SPI.
3	ID	1	Ground	Set ID to 0
4	SCLK	i	MCU	Clock for SPI
5	SI	i	MCU	Serial input from host MCU to EPD
6	SO	0	MCU	Serial output from EPD to host MCU
7	/RESET	-	MCU	Reset signal. Low enable
8	ADC IN	-	-	Not connected
9	VCL	С	Capacitor	
10	C42P	C	Charge-Pump	
11	C42M	C	Capacitor	
12	C41P	C	Charge-Pump	
13	C41M	C	Capacitor	
14	C31M	C	Charge-Pump	
15	C31P	Č	Capacitor	
16	C21M	Č	Charge-Pump	
17	C21P	C	Capacitor	
18	C16M	C	Charge-Pump	
19	C16P	Č	Capacitor	
20	C15M	С	Charge-Pump	
21	C15P	C	Capacitor	
22	C14M	С	Charge-Pump	
23	C14P	С	Capacitor	
24	C13M	С	Charge-Pump	
25	C13P	С	Capacitor	
26	C12M	С	Charge-Pump	
27	C12P	С	Capacitor	
28	C11M	С	Charge-Pump	
29	C11P	С	Capacitor	
30	VCOM_DRIVER	RC	Resistor & Capacitor	Vcom source from driver IC
31	VCC	Р	VCC	Power for analog circuit
32	VDD	Р	VDD	Power for digital circuit
33	GND	Р	Ground	Ŭ Ŭ
34	VGH	С	Capacitor	
35	VGL	С	Capacitor	
36	VDH	С	Capacitor	
37	VDL	С	Capacitor	
38	BORDER	Ρ	VDL	Connect to VDL for white frame border
39	VST	Р	VCOM_PANEL	
40	VCOM_PANEL	С	Capacitor	Vcom to panel

**5.2 REFERENCE CIRCUIT** 

Doc. No. 1P008-00



Note (1) Pin.1 location



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#### **6.OPTICAL CHARACTERISTICS**

#### 6.1 TEST CONDITIONS

Item	Symbol	Value	Unit
Ambient Temperature	Та	25±2	°C
Ambient Humidity	Ha	50±10	%RH
Supply Voltage	V <sub>CC</sub> & V <sub>DD</sub>	3.0	V

#### **6.2 OPTICAL SPECIFICATIONS**

ltem	symbol	Rating			Unit	Note
		Min.	Тур.	Max.	Onic	Note
Contrast Ratio	CR	5	10			θx=θy=0(1),(2)
Refresh time	Tr		3		sec	(2)
Reflectance	R%		32		%	(3)

Not include Masking Film. Driven and optical measured by PDI waveform and CM-700d. Note (1) Definition of Viewing Angle ( $\theta x, \theta y$ ):

Normal  $\theta x = \theta y = 0^{\circ}$ 



- (2) 1.5 second is the refresh time at 25°C. The refresh time and contrast ratio are easy to be changed due to different films, display performance requirement, and ambient temperature. The refresh time is times of that at room temperature.
- (3) Definition of Reflectance (R%):
  - The reflectance can be calculated by the following expression.
    - Reflectance(R%) = L(White) / L(Std. White)\*100%
      - L(White): Luminance of white
      - L(Std. White): Luminance of the standard white plate, which is made of BaCO<sub>3</sub>

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#### 7. PACKING TBD

#### 8. PRECAUTIONS

- (1) Do not apply rough force such as bending or twisting to the EPD panel during assembly.
- (2) It is recommended to assemble or install EPD panels in a clean working area. Dust and oil may cause electrical shorts or degrade the PS film.
- (3) Do not apply pressure to the EPD panel in order to prevent damaging it.
- (4) Always follow the correct power-on sequence in turning on the EPD panel. This will help prevent a short circuit "latchup" of the ICs.
- (5) Do not plug in or pull out the I/F connector while the EPD panel is in operation.
- (6) Do not disassemble or reassemble the EPD panel.
- (7) Use a soft dry cloth without chemicals for cleaning. The surface of the PS film is very soft and easily scratched.
- (8) Be mindful of moisture to avoid its penetration into the EPD panel, which may cause damage during operation.
- (9) High temperature, high humidity, sunlight or fluorescent light may degrade the EPD panel's performance. Please do not expose the unprotected EPD panel to high temperature, high humidity, sunlight, or fluorescent for long periods of time. It is highly recommended to store the EPD panel in a dark place without condensation, a temperature range of 20°C to 30°C, and humidity from 40%RH to 60%RH.
- (10) The ink used for marking the Panel ID number is erased easily by solvent. Please avoid using solvent to clean the EPD panel.

Doc. No. 1P008-00

#### 9. DEFINITION OF LABELS





Bar Code=Model Name.+Lot No.+Q'TY.(22 Digits)

### Pallet Label