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User Guide for  
**FEBFAN6863W\_CP452v1**  
Evaluation Board

Highly Integrated Green-Mode PWM  
Controller FAN6863W  
19 V / 2.1 A Notebook Adapter

Featured Fairchild Product:  
**FAN6863W**

*Direct questions or comments  
about this evaluation board to:  
“Worldwide Direct Support”*

[Fairchild Semiconductor.com](http://Fairchild Semiconductor.com)

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This user guide supports the evaluation kit for the FAN6863W. This FEBFAN6863W\_CP452v1 evaluation board can be identified by the silkscreen marking PLM0068 REV:1 on the bottom side of the PCB. It should be used in conjunction with the FAN6863W datasheet as well as Fairchild's application notes and technical support team. Please visit Fairchild's website at [www.fairchildsemi.com](http://www.fairchildsemi.com).

## 1. Introduction

This document describes a 40 W power supply using the FAN6863W.

### 1.1. Description

The operating current in FAN6863W is as small as 3 mA. The small operating current results in higher efficiency and reduces the  $V_{DD}$  hold-up capacitance requirement. Once FAN6863W enter “deep” Green Mode, the operating current is reduced to 0.6 mA, assisting the power supply in meeting power conservation requirements.

By using FAN6863W, an adapter can be implemented with few external components and minimized cost.

### 1.2. Features

- Low Standby Power: Under 0.1 W
- Low Startup Current: 8  $\mu$ A
- Low Operating Current in Green Mode: 600  $\mu$ A
- Peak-Current Mode Operation with Cycle-by-Cycle Current Limiting
- PWM Frequency Continuously Decreasing with Burst Mode at Light Loads
- $V_{DD}$  Over-Voltage Protection (OVP)
- Constant Output Power Limit (Full AC Input Range)
- Internal Latch Circuit (FAN6863WL) for OVP, OTP
- SENSE Pin Short-Circuit Protection (SSCP)
- Fixed PWM Frequency (65 KHz) with Frequency Hopping
- Feedback Open-Loop Protection: 60 ms Delay
- GATE Output Maximum Voltage Clamp: 13.5 V
- Soft-Start Time: 5 ms
- Soft Driving for EMI Improvement
- Full-Range Frequency Hopping
- Internal OTP Sensor with Hysteresis
- Gate Driving Capability: 400 mA

## 2. Evaluation Board Specifications

**Table 1. Summary of Features and Performance**

All result is tested with output DC cable, AWG 18, 1.8 M.

Specification	Min.	Max.	Unit
<b>Input</b>			
Voltage	90	264	V <sub>AC</sub>
Frequency	47	63	Hz
<b>Output</b>			
Output Voltage 1		19	V
Output Current 1	0	2.1	A
<b>Total Output Power</b>			
Full-load Output Power	0	40	W

### 3. Photographs



Figure 1. Top View

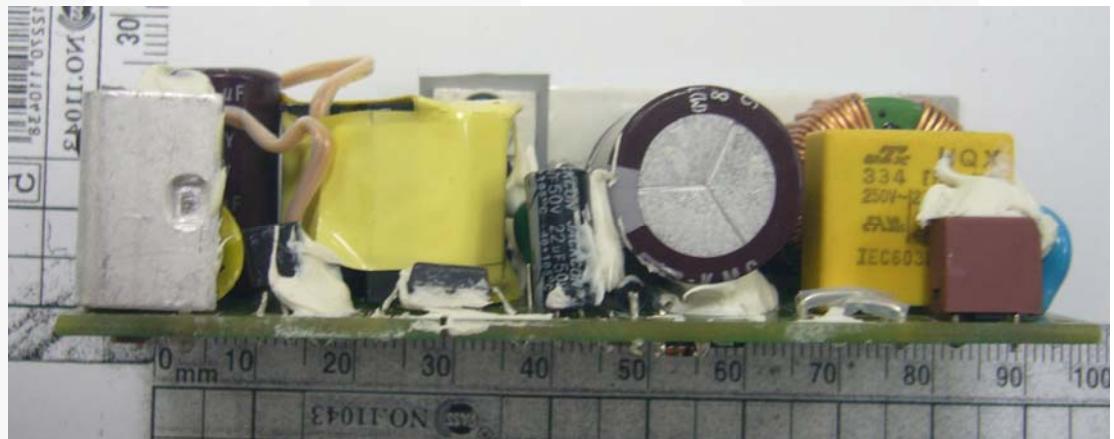
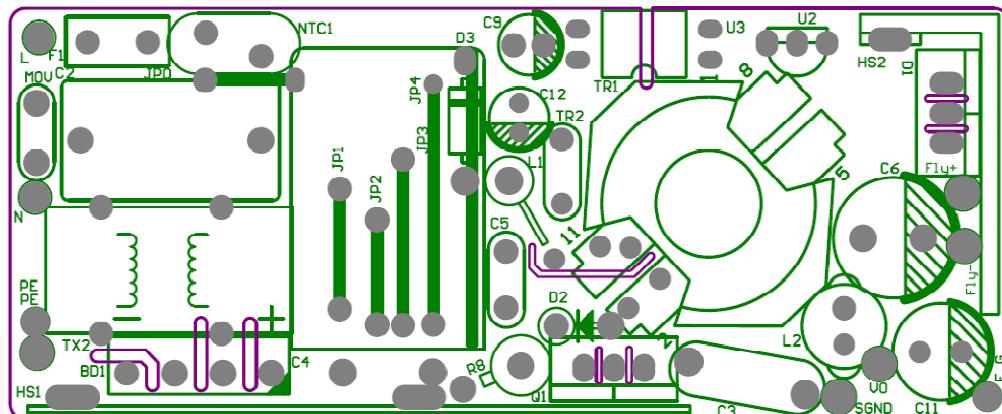
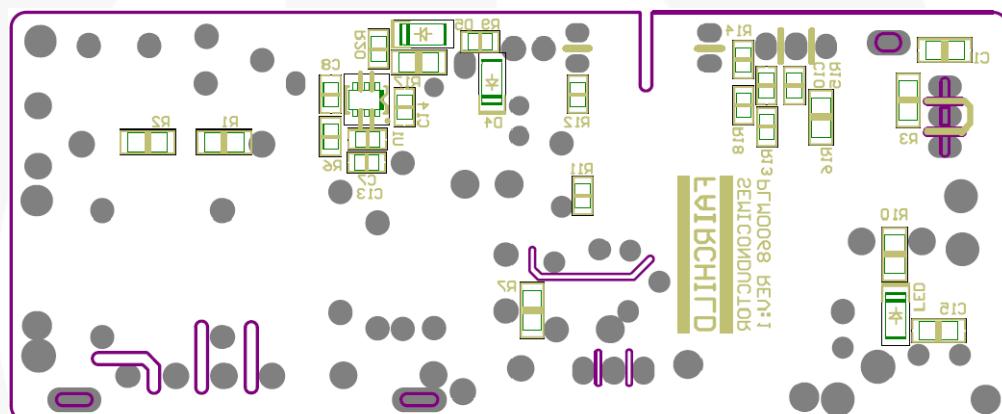


Figure 2. Bottom View

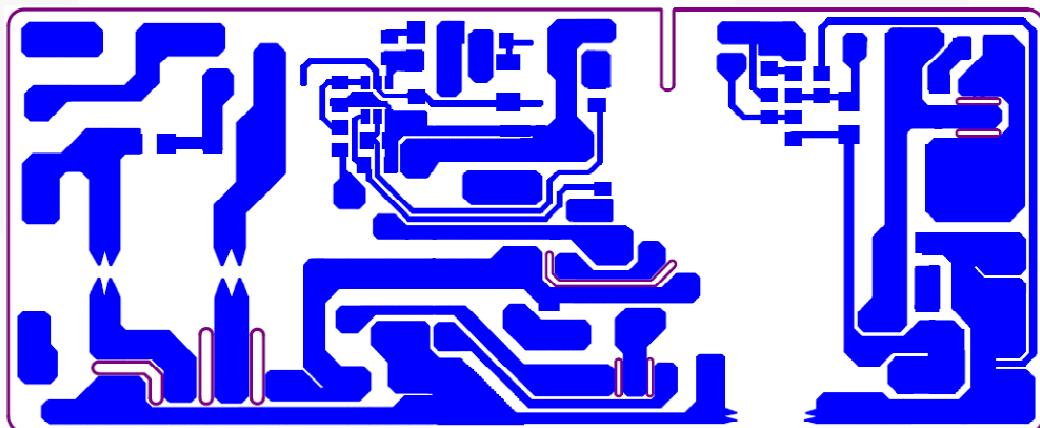
#### 4. Printed Circuit Board



**Figure 3.** Top Overlay



**Figure 4.** Bottom Overlay



**Figure 5.** Bottom Layer

## 5. Schematic

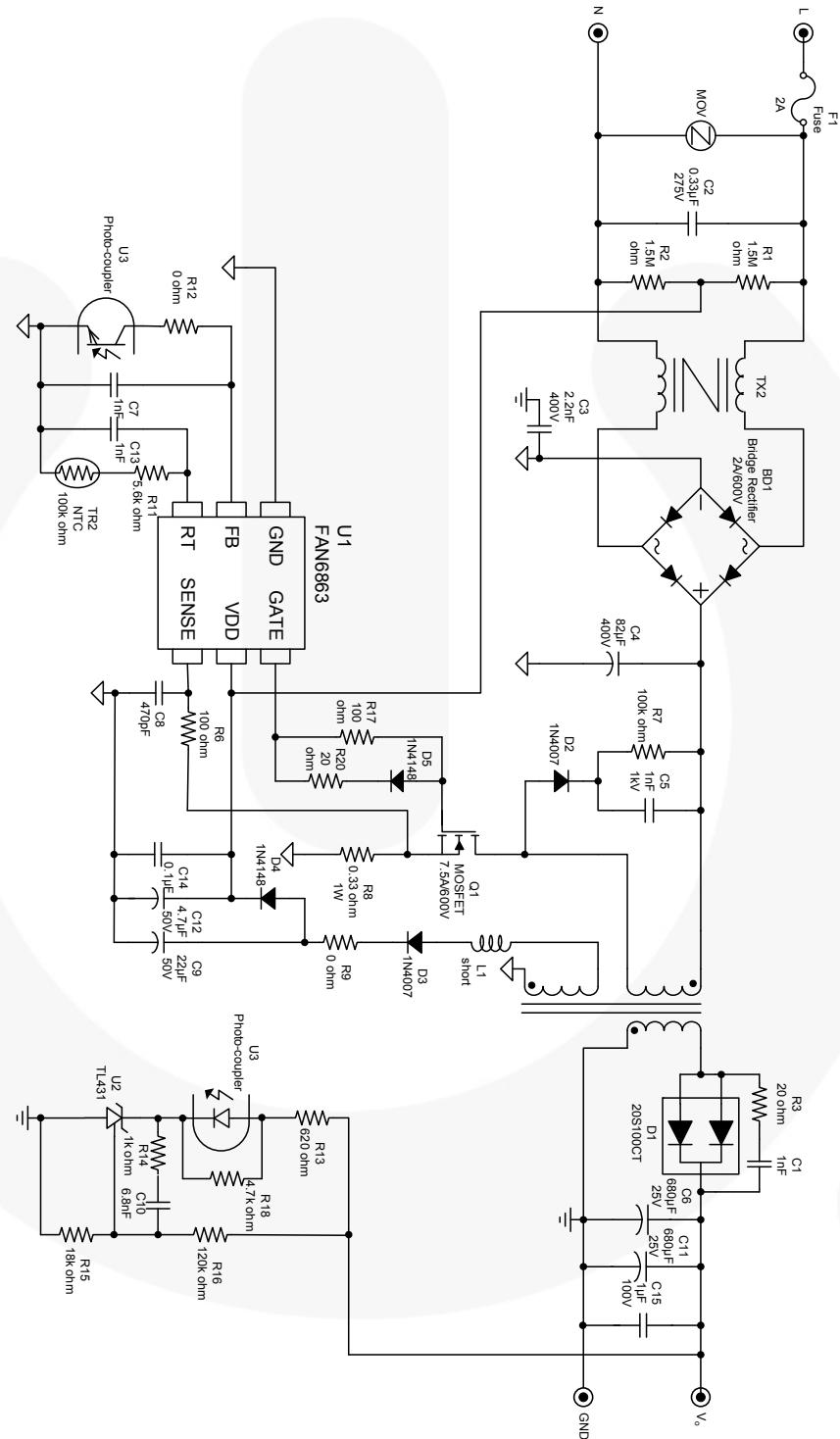


Figure 6. Evaluation Board Schematic

## 6. Bill of Materials

Part Type	Qty.	Part No.	Manufacturer	Designator
JUMPER WIRE 0.8ψ(mm)	8			JP0~JP4, NTC1, L1, L2
Metal-Oxide Resistor 1 W 0.33 Ω ±10%	1			R8
SMD Resistor 0805 0 Ω ±%	1			R9, R12
SMD Resistor 0805 20 Ω ±5%	1			R20
SMD Resistor 0805 100 Ω ±5%	2			R6
SMD Resistor 0805 620 Ω ±5%	1			R13
SMD Resistor 0805 1 kΩ ±5%	1			R14
SMD Resistor 0805 5.6 kΩ ±5%	1			R11
SMD Resistor 0805 18 kΩ ±5%	1			R15
SMD Resistor 0805 47 kΩ ±5%	1			R18
SMD Resistor 1206 20 Ω ±5%	1			R3
SMD Resistor 1206 100 Ω ±5%	1			R17
SMD Resistor 1206 100 kΩ ±5%	1			R7
SMD Resistor 1206 120 kΩ ±5%	1			R16
SMD Resistor 1206 1.5 MΩ ±5%	2			R1, R2
0805 X7R +/-10% 1 nF 50 V	2			C7, C13
0805 X7R +/-10% 470 pF 50 V	1			C8
0805 X7R +/-10% 6.8 nF 50 V	1			C10
0805 X7R +/-10% 0.1 μF 50 V	1			C14
1206 X7R +/-10% 1 nF 100 V	1			C1
1206 X7R +/-10% 1 μF 100 V	1			C15
Ceramic Capacitor 1 nF 1K V	1			C5
Electrolytic Capacitor 82 μF 400 V 105°C	1	KMG	NCC	C4
Electrolytic Capacitor 680 μF 25 V 105°C	1	KY	NCC	C6
Electrolytic Capacitor 22 μF 50 V 105°C	1	LHK	Jackcon	C9
Electrolytic Capacitor 680 μF 25 V 105°C	1	LHK	Jackcon	C11
Electrolytic Capacitor 4.7 μF 50 V 105°C	1	LHK	Jackcon	C12
X2 Capacitor 0.33 μF 275 V ±10%	1			C2
Y1 Capacitor 2.2 nF 250 V ±20%	1			C3
MOV Oxide Varistor 471	1			MOV
Common choke 30 mH ±10%	1	TRN0279	SEN HUEI	TX2
Transformer RM-8 L=920 μH	1	TRN0280	SEN HUEI	TR1
FUSE MICRO RST 250 V / 2 A Time-Lag	1		Walter	F1
NTC Resistor 100 kΩ	1	TTC104	Thinking	TR2
SMD Diode 1 A / 1000 V SOD-80	2	LL4148	Fairchild Semiconductor	D4, D5
Diode 1A/700V DO-41	2	1N4007	Fairchild Semiconductor	D2, D3
Bridge Rectifier 2 A / 600 V	1	2KBP06M	Fairchild Semiconductor	BD1
Schottky Diode 20 A / 100 V TO-220	1	YM20S100CT	Fairchild Semiconductor	D1
MOSFET 7.5 A/600 V TO-220	1	FQP8N60C	Fairchild Semiconductor	Q1
REGULATOR ±% TO-92	1	FAN431ACZ-AP	Fairchild Semiconductor	U2
Photo Coupler DIP	1	FOD817A	Fairchild Semiconductor	U3
PWM Controller SSOT-6	1	FAN6863W	Fairchild Semiconductor	U1
Heat Sink 55X20X1.5 mm	1	MCH0636		HS1
Heat Sink 11.5X24.9(L)X17(H)X1.5(W) mm	1	MCH0637		HS2
PCB PLM0068 REV0	1			

## 7. Test Conditions & Test Equipment

**Table 2. Test Conditions & Test Equipment**

Test Temperature	Ambient
<b>Test Equipment</b>	AC Source: 6810 AC POWER SOURCE (MG-0010) Electronic Load: Chroma 63102 (MG-0081) Power Meter: BM817 (MG-0011) Oscilloscope: LeCory 423 (MG-0008)
<b>Test Items</b>	1. Input Current 2. Input Wattage at No Load Condition 3. Turn On Time 4. Hold Up Time 5. DC Output Rising Time 6. Line & Load Regulation 7. Efficiency 8. Output Ripple & Noise 9. Step Response 10. Over-Current Protection 11. Short-Circuit Protection 12. Brownout Test 13. V <sub>DD</sub> Voltage Level 14. Voltage Stress on MOSFET & Rectifiers 15. EMI Test

## 8. Input Current

### 8.1. Test Condition

Measure the AC input current at maximum loading.

**Table 3. Test Result**

Input Voltage	Input Current (mA)
90 V / 60 Hz	824
264 V / 50 Hz	359

## 9. Input Wattage at No-load Condition

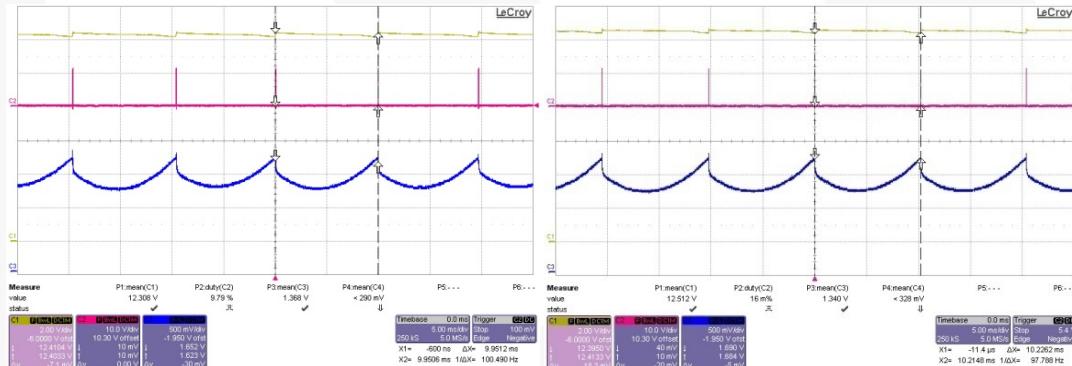
### 9.1. Test Condition

Measure the input wattage and output voltage at no load.

**Table 4. Test Result**

Input Voltage	Input Wattage (mW)	Output Voltage(V)	Specification
90 V / 60Hz	34	19.28	<0.3W
115 V / 60 Hz	37	19.28	
230 V / 50 Hz	68	19.28	
264 V / 50 Hz	81	19.28	
90 V / 60 Hz	34	19.28	

### 9.2. Measured Waveforms



**Figure 7. 90 V / 60 Hz at No Load, Ch1: V<sub>DD</sub>, Ch2: V<sub>GATE</sub>, Ch3: FB**

**Figure 8. 264 V / 50 Hz at No Load, Ch1: V<sub>DD</sub>, Ch2: V<sub>GATE</sub>, Ch3: FB**

## 10. Turn-On Time

### 10.1. Test Condition

Set the output at maximum loading. Measure the interval between the AC plug-in and stable output.

**Table 5. Test Result**

Input Voltage	Turn On Time (s)	Specification
90 V / 60 Hz	1.56	<3 s
264 V / 50 Hz	0.465	

## 11. Hold-up Time

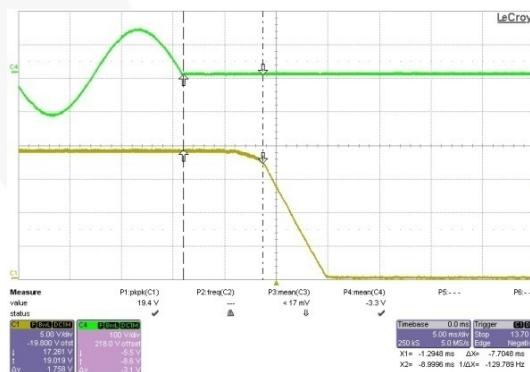
### 11.1. Test Condition

Set the output at maximum load. Measure the time interval between the AC power-off and output voltage falling to the lower limit of rated value. The AC waveform should be off at zero degrees.

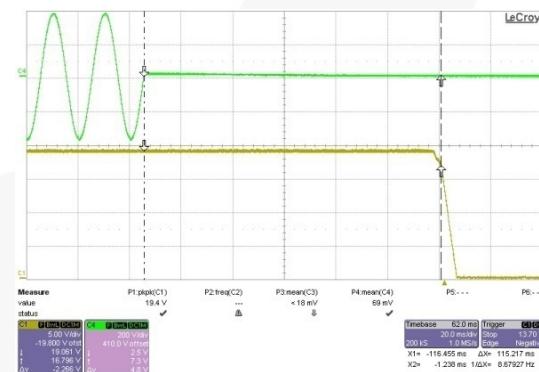
**Table 6. Test Result**

Input Voltage	Hold-up Time (ms)
90 V / 60 Hz	7.7
115 V / 60 Hz	15.1
230 V / 50 Hz	83.9
264 V / 50 Hz	115.2

### 11.2. Measured Waveform



**Figure 9. 90 V / 60 Hz at Maximum Load, Ch1: V<sub>AC</sub>, Ch4: V<sub>O</sub>**



**Figure 10. 264 V / 50 Hz at Maximum Load, Ch1: V<sub>AC</sub>, Ch4: V<sub>O</sub>**

## 12. DC Output Rising Time

### 12.1. Test Condition

Set the output at maximum loading and no loading. Measure the time interval between 10% to 90% of the output during startup.

**Table 7. Test Result**

Input Voltage	Maximum Load (ms)	No Load (ms)	Specification
90 V / 60 Hz	5.18	4.25	< 20 ms
264 V / 50 Hz	4.28	3.49	

### 12.2. Measured Waveform

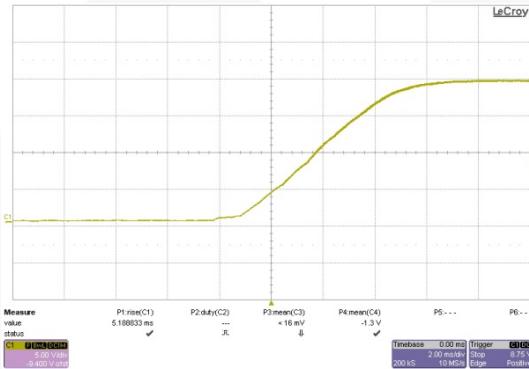


Figure 11. 90 V / 60 Hz at Maximum Load, Ch1: V<sub>o</sub>

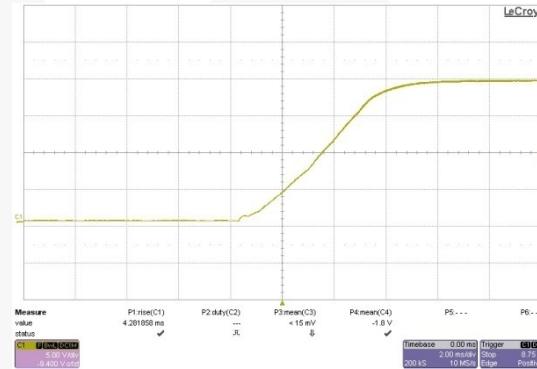


Figure 12. 264 V / 50 Hz at Maximum Load, Ch1: V<sub>o</sub>

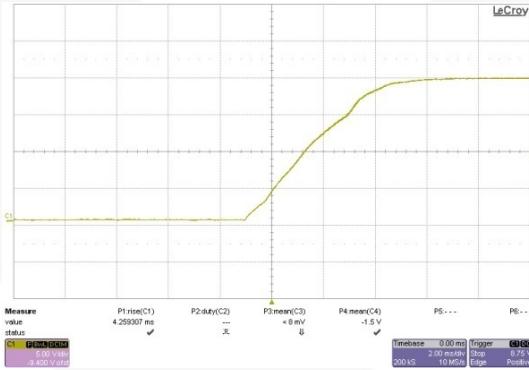


Figure 13. 90 V / 60 Hz at No Load, Ch1: V<sub>o</sub>

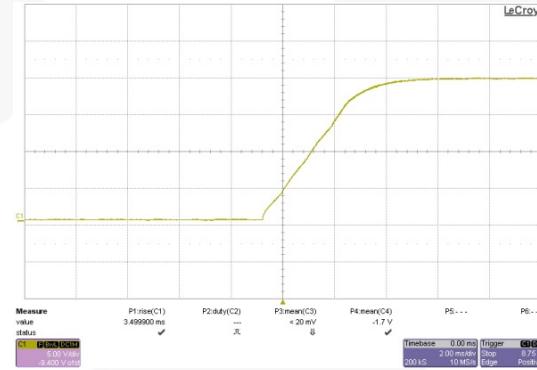


Figure 14. 264 V / 50 Hz at No Load Ch4: V<sub>o</sub>

## 13. Line & Load Regulation

### 13.1. Test Condition

Measure line and load regulation according to Table 8.

**Table 8. Test Result**

Input Voltage	Output Voltage at Max. Load (V)	Output Voltage at Min. Load(V)	Load Regulation (%)	Specification
90 V / 60 Hz	19.106	19.28	0.90	5%
115 V / 60 Hz	19.100	19.28	0.90	
132 V / 60 Hz	19.090	19.27	0.90	
180 V / 50 Hz	19.090	19.27	0.90	
230 V / 50 Hz	19.088	19.27	0.90	
264 V / 50 Hz	19.080	19.27	0.90	
Line Regulation	0.13%	0.05%		

## 14. Efficiency

### 14.1. Test Condition

Output at maximum load.

**Table 9. Test Result**

Input Voltage	Input Wattage (W)	Output Wattage (W)	Efficiency (%)	Specification
90 V / 60 Hz	45.32	39.98	88.20	
115 V / 60 Hz	44.69	39.96	89.40	
132 V / 60 Hz	44.50	39.91	89.60	
180 V / 50 Hz	44.31	39.91	90.06	
230 V / 50 Hz	44.26	39.91	90.10	
264 V / 50 Hz	44.56	39.92	89.58	

**Table 10. Test Result**

Input Voltage	Efficiency (%)					Specification
	25% Load	50% Load	75% Load	100% Load	Avg.	
115 V / 60 Hz	89.4	90.0	89.5	89.4	89.5	
230 V / 50 Hz	89.4	89.6	90.3	90.2	89.8	

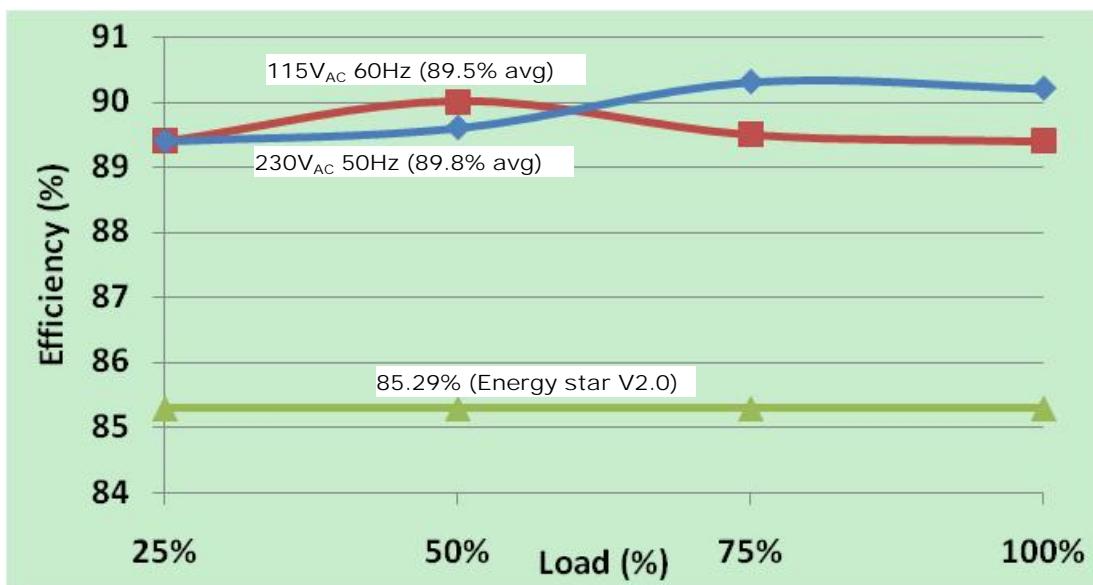


Figure 15. Efficiency at Various Loads

## 15. Output Ripple & Noise

### 15.1. Test Condition

Ripple and noise are measured by using a 20 MHz bandwidth-limited oscilloscope with a 10  $\mu\text{F}$  capacitor paralleled with a high-frequency 0.1  $\mu\text{F}$  capacitor across each output.

**Table 11. Test Result**

Input Voltage	Maximum Load (mV)	Minimum Load (mV)
90 V / 60 Hz	286	117
115 V / 60 Hz	264	125
230 V / 50 Hz	255	150
264 V / 50 Hz	248	160

### 15.2. Measured Waveform

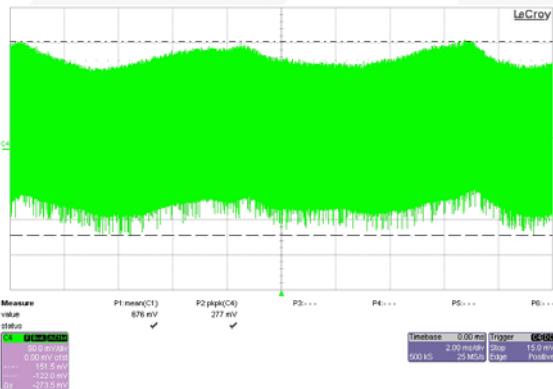


Figure 16. 90 V / 60 Hz at Maximum Load, Ch4:  $V_o$

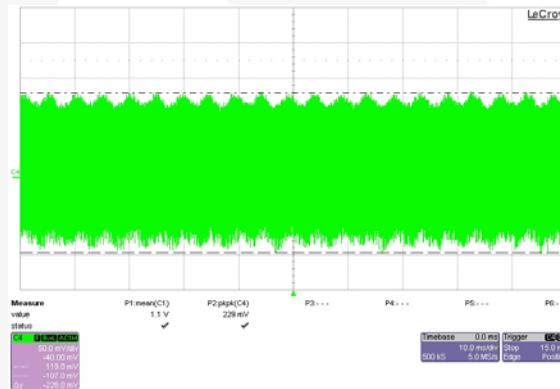


Figure 17. 264 V / 50 Hz at Maximum Load, Ch4:  $V_o$

## 16. Step Response

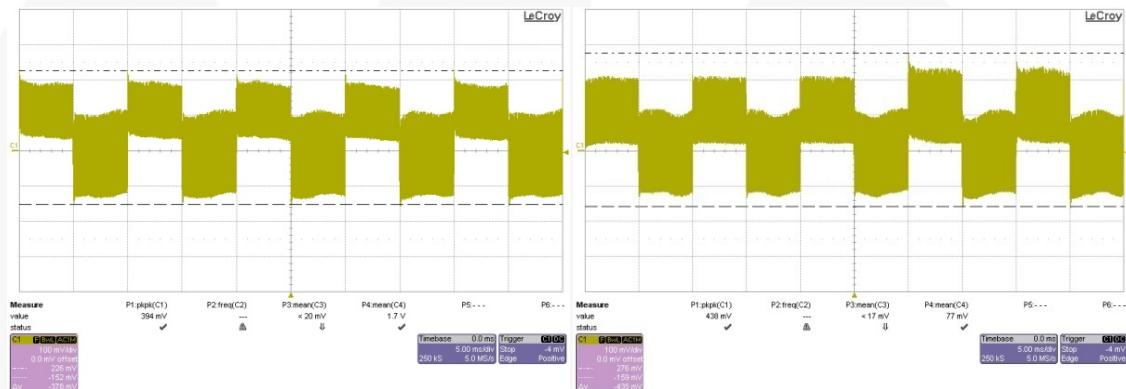
### 16.1. Test Condition

Dynamic loading (20%~80% of the full load, 5 ms duty cycle, 2.5 A/ $\mu$ s rise / fall time).

**Table 12. Test Result**

Input Voltage	Overshoot (mV)	Undershoot (mV)
115 V / 60 Hz	217	152
230 V / 50 Hz	276	159

### 16.2. Measured Waveform



**Figure 18. 115 V / 60 Hz at Maximum Load,  
Ch1: Vo**

**Figure 19. 264 V / 50 Hz at Maximum Load,  
Ch1: Vo**

## 17. Over-Current Protection (OCP)

### 17.1. Test Condition

Increase output loading gradually. Measure the output maximum current.

**Table 13. Test Result**

Input Voltage	Maximum Load (ms)
90 V / 60 Hz	2.94
115 V / 60 Hz	3.27
230 V / 50 Hz	3.39
264 V / 50 Hz	3.37

## 18. Short-Circuit Protection

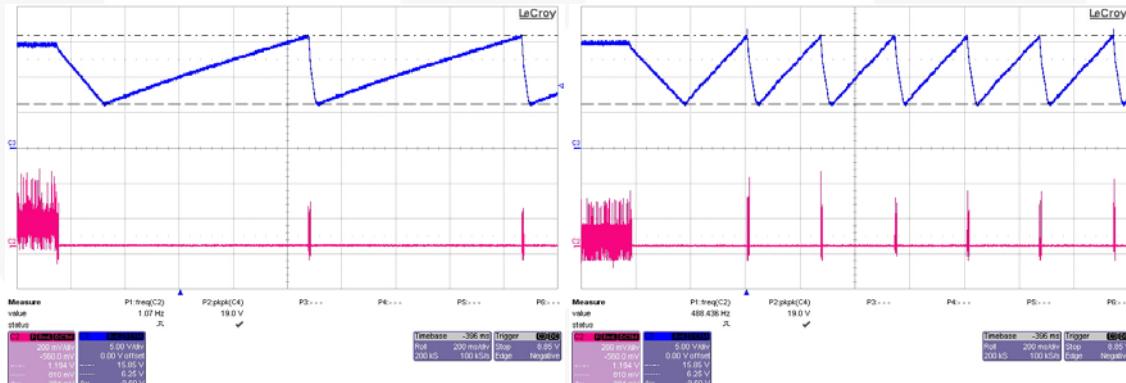
### 18.1. Test Condition

Short the output of the power supply. The power supply should enter “Hiccup” Mode protection with less than 2 W input voltage.

**Table 14. Test Result**

Input Voltage	Input Wattage at Maximum Loading (W)	Input Wattage at Minimum Loading (W)	Specification
90 V / 60 Hz	0.15	0.15	<2 W
264 V / 50 Hz	0.35	0.34	

### 18.2. Measured Waveform (Output-Short Condition)



**Figure 20. 90 V / 60 Hz at Maximum Load,  
Ch2: V<sub>CS</sub>, Ch3: V<sub>DD</sub>**

**Figure 21. 264 V / 50 Hz at Maximum Load,  
Ch2: V<sub>CS</sub>, Ch3: V<sub>DD</sub>**

## 19. Brownout Test

### 19.1. Test Condition

Set the output at maximum loading. Decrease the input voltage with 5 V<sub>AC</sub> step. Record the input wattage and output voltage. After the output is off, increase the AC voltage gradually and record the recovery voltage.

**Table 15. Test Result**

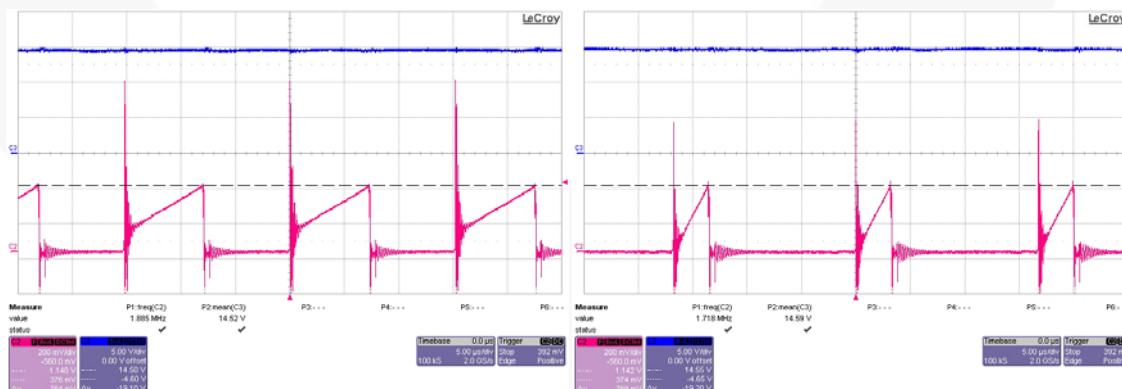
Input Voltage	Input Wattage (W)	Output Voltage (V)
90 V / 60 Hz	45.75	19.10
85 V / 60 Hz	45.87	19.10
80 V / 60 Hz	46.26	19.10
75 V / 60 Hz	46.62	19.10
70 V / 60 Hz	0	0
65 V / 60 Hz	0	0
60 V / 60 Hz	0	0
55 V / 60 Hz	0	0

## 20. V<sub>DD</sub> Voltage Level

**Table 16. Test Result**

	Minimum Load (V)	Maximum Load (V)	Near OPP (V)	Output S.C. (Maximum Value) (V)
90 V / 60 Hz	12.45	14.80	16.40	15.95
264 V / 50 Hz	12.60	14.00	15.00	15.95

### 20.1. Measured Waveform



**Figure 22. 90 V / 60 Hz at Maximum Load, Ch1: V<sub>DD</sub>, Ch2: V<sub>CS</sub>**

**Figure 23. 264 V / 50 Hz at Maximum Load, Ch1: V<sub>DD</sub>, Ch2: V<sub>CS</sub>**

## 21. Voltage Stress on MOSFET & Rectifiers

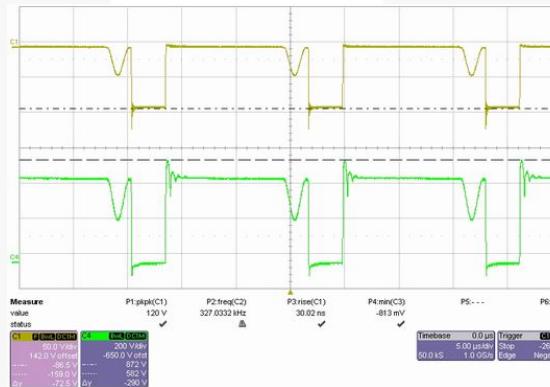
### 21.1. Test Condition

Measure the voltage stress on the MOSFET and secondary rectifiers under below specified conditions.

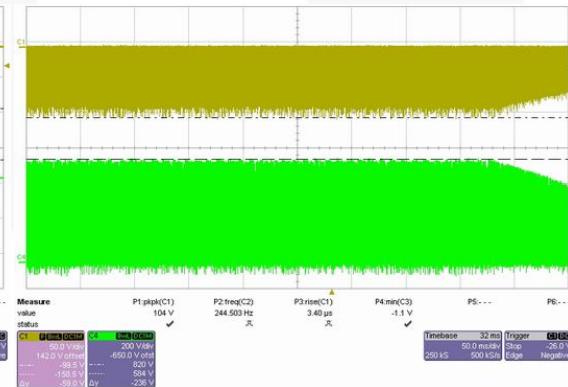
**Table 17. Test Result**

	Stress On MOSFET	Rating	Stress On Output Rectifier	Rating
90 V / 60 Hz, Maximum Load	322	600 V	55	100 V
90 V / 60 Hz, Maximum Load, Startup	361		49	
90 V / 60 Hz, Maximum Load, Output Short	294		28	
264 V / 50 Hz, Maximum Load	582		86	
264 V / 50 Hz, Maximum Load, Startup	534		99	
264 V / 50 Hz, Maximum Load, Output Short	502		93	
264 V / 50 Hz, Maximum Load Turns Off	322		55	

### 21.2. Measured Waveform



**Figure 24. 264 V / 50 Hz at Maximum Load, Ch1:  $V_{DS}$ (MOSFET), Ch4:  $V_{AK}$ (RECTIFIER)**



**Figure 25. 264 V / 50 Hz at Maximum Load (Turns Off), Ch1:  $V_{DS}$ (MOSFET), Ch4:  $V_{AK}$ (RECTIFIER)**

### 21.3. Electromagnetic Interference (EMI) Test

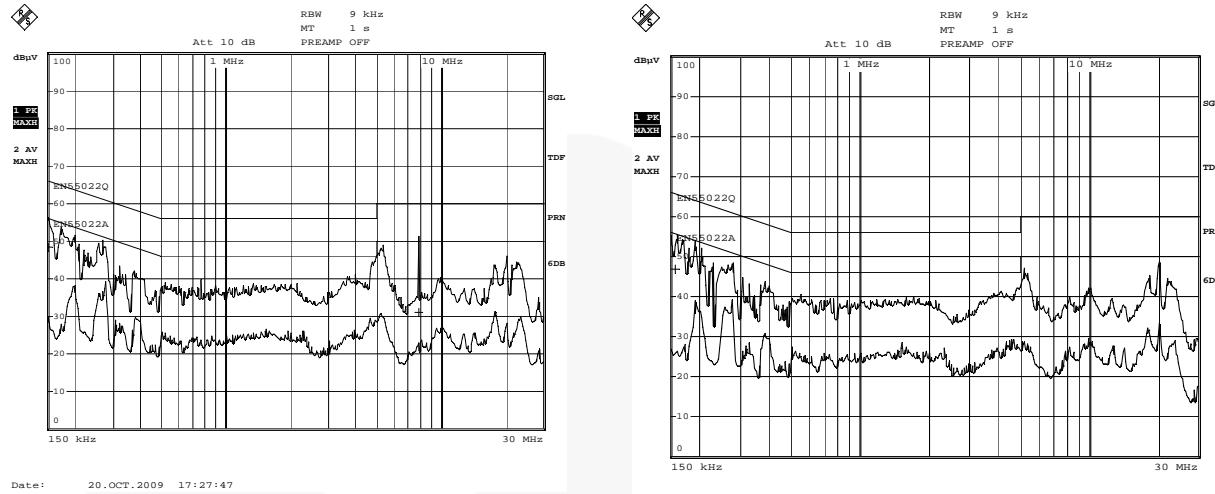


Figure 26. Conduction-Line & Neutral at 115V<sub>AC</sub>

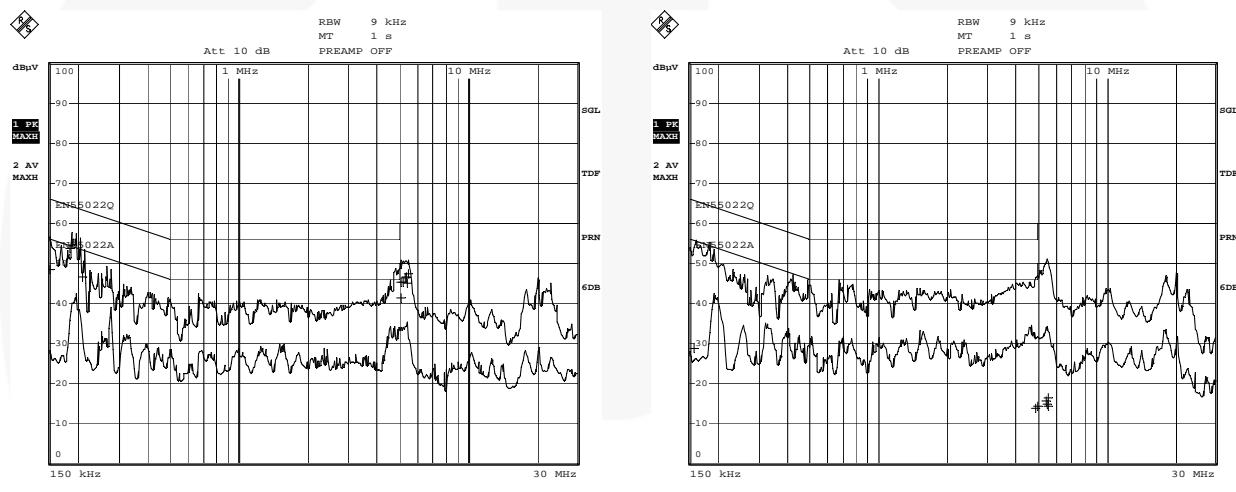


Figure 27. Conduction-Line & Neutral At 115V<sub>AC</sub>

## 22. Surge Test

Mode	Polarity	Phase	Voltage	Condition
L-PE	±	0°	4.4 kV	Pass
	±	90°		Pass
	±	180°		Pass
	±	270°		Pass
N-PE	±	0°	4.4 kV	Pass
	±	90°		Pass
	±	180°		Pass
	±	270°		Pass
L-N	±	0°	2.0 kV	Pass
	±	90°		Pass
	±	180°		Pass
	±	270°		Pass

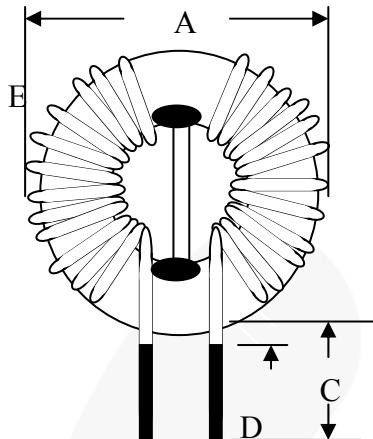
## 23. ESD Test

Air Discharge (16.5 kV)		Contact Discharge (8.8 kV)	
Pass	Pass	Pass	Pass

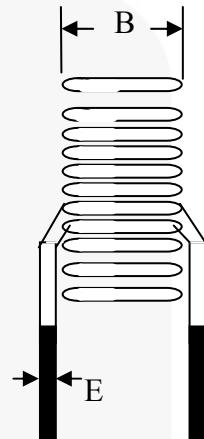
### SPECIFICATION APPROVAL

Customer	SYSTEM GENERAL CORP.		P/N:	TRN-0279
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#### 1. DIMENSION :



UNIT : mm



A	22 max
B	12 max
C	18 ± 1
D	15 max
E	φ0.6 ± 0.1

#### 2. ELECTRICL SPECIFICATION : at 1 kHz, 1 V

2.1 INDUCTANCE : L1=L2 : 22 mH min

2.2 DC RESISTANCE : L1=L2 : 0.78 Ohm max

2.3 TURN & WIRE : L1=L2 : φ0.6 x 40TS

#### MATERIALS LIST :

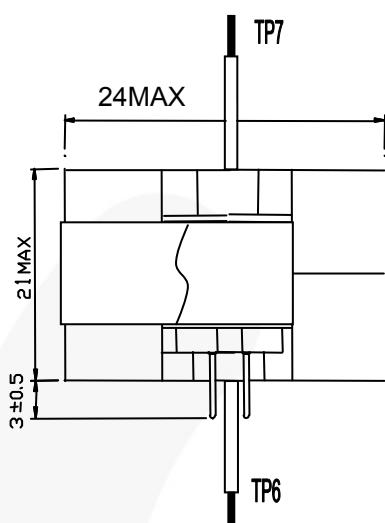
COMPONENT	MATERIAL	MANUFACTURE	UL FILE NO.
1.CORE	T18.5x9.8x7	core T18.5x9.8x8 JFE FERRITE Corp.	
2.WIRE	THFN-216	Ta Ya electric wire CO.,LTD	E197768
	UEWN/U	PACIFIC wire & cable CO.,LTD	E201757
	UEWE	Tai-l electric wire & cable CO.,LTD	E85640
	UWY	Jang Shing wire CO.,LTD	E174837
3.SOLDER	96.5% Sn,3% Ag,0.5% Cu,	Xin Yuan CO.,LTD	

UNIT	m/m	DRAWN	CHECK	TITLE	
TEL	(02)29450588	Ci wun Chen	Guo long Huang	IDENT N.O.	TRN-0279
FAX	(02)29447647			D W G N.O.	
No.26-1, Lane 128, Sec. 2, Singnan Rd., Jhonghe City, Taipei County 235, Taiwan (R.O.C.)		SEN HUEI INDUSTRIAL CO.,LTD.			

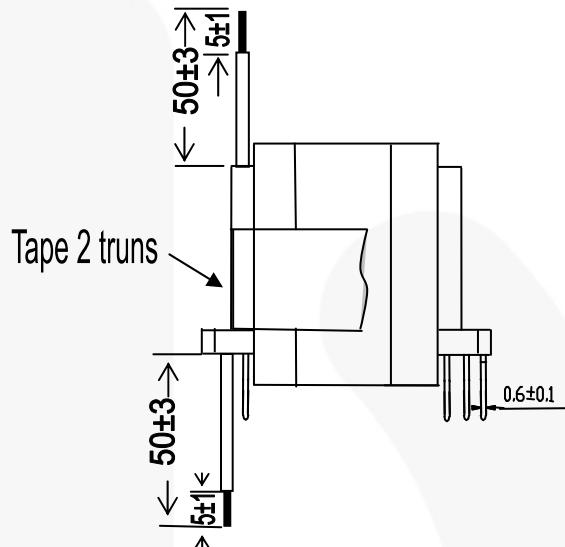
### SPECIFICATION APPROVAL

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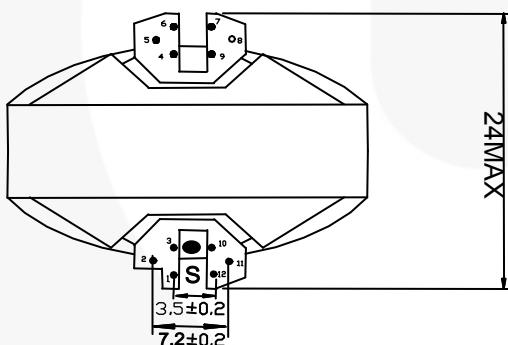
1.DIMENSION : Unit : mm



ELEVATION VIEW



SIDE VIEW



**TRN-0280**

Past label on the top of transformer,  
and the wording peak faces pin 1 & pin 12

NOTE: BOTTOM VIEW

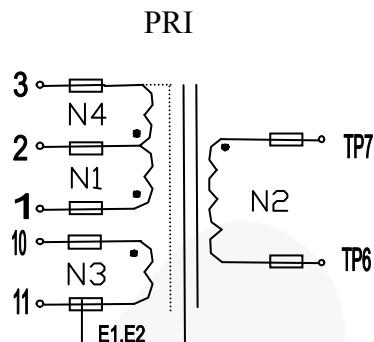
- 1) Cut off pin 2,4,5,6,7,8,9,12.
- 2) Core package of two turns tape.

UNIT	m/m	DRAWN	CHECK	TITLE	TRANS
TEL	(02)29450588	Ci wun Chen	Guo long Huang	IDENT N.O.	TRN-0280
FAX	(02)29447647			D W G N.O.	I0802
No.26-1, Lane 128, Sec. 2, Singnan Rd., Jhonghe City, Taipei County 235, Taiwan (R.O.C.)			SEN HUEI INDUSTRIAL CO.,LTD.		

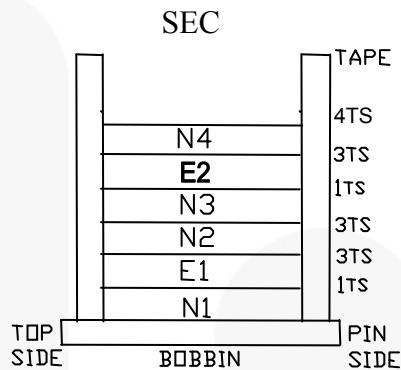
### SPECIFICATION APPROVAL

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#### 2. SCHEMATIC :



#### 2.1 SCHEMATIC :



Note: The shielding wields Copper Foil 7.5mm, and connects the pin11.

#### 2.3 WINDING :

SET	WINDING	MATERIAL	START-FINISH	TURNS	TAPE	REMARK
1	N1	2UEW- $\varphi$ 0.25×1P	1-2	33 <sup>TS</sup>	1 <sup>TS</sup>	
2	E1	2UEW- $\varphi$ 0.2×1P	11	1.2 <sup>TS</sup>	3 <sup>TS</sup>	Add 7.5 mm tape of copper foil
3	N2	TLW- $\varphi$ 0.5×2P	TP6-TP7	12 <sup>TS</sup>	3 <sup>TS</sup>	Add Tube of Teflon
4	N3	2UEW- $\varphi$ 0.25×1P	10-11	9 <sup>TS</sup>	1 <sup>TS</sup>	
5	E2	2UEW- $\varphi$ 0.2×1P	11	1.2 <sup>TS</sup>	3 <sup>TS</sup>	Add 7.5 mm tape of copper foil
6	N4	2UEW- $\varphi$ 0.25×1P	2-3	33 <sup>TS</sup>	4 <sup>TS</sup>	

UNIT	m/m	DRAWN	CHECK	TITLE	TRANS
TEL	(02)29450588	Ci wun Chen	Guo long Huang	IDENT N O.	TRN-0280
FAX	(02)29447647	No.26-1, Lane 128, Sec. 2, Singnan Rd., Jhonghe City, Taipei County 235, Taiwan (R.O.C.)	SEN HUEI INDUSTRIAL CO.,LTD.	D W G N O.	I0802

## SPECIFICATION APPROVAL

<b>Customer</b>	<b>SYSTEM GENERAL CORP.</b>			<b>P/N:</b>	<b>TRN-0280</b>
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### 3.ELECTRICAL SPECIFICATION :

3.1 Inductance test : at 1 KHz ,1 V

$$L3-1=920 \mu\text{H} \pm 5\%$$

3.2 Leakage inductance : at 1 KHz ,1 V

P(4-6) : 15  $\mu\text{H}$  Max (shorted A,B)

3.3 DC Resistance test at 25°C

P(4-6) : 23 m $\Omega$  Max

3.4 Hi-pot test :

AC 3.0 KV /60 Hz/0.5 mA hi-pot for one minute between primary to sec.

AC 1.5 KV /60 Hz/0.5 mA hi-pot for one minute between primary to core.

AC 1.5 KV /60 Hz/0.5 mA hi-pot for one minute between sec to core.

3.5 Insulation test :

The insulation resistance is between primary to sec and windings to core measured by DC 500 V, must Be over 100 M $\Omega$ .

3.6 Terminal strength :

1.0 Kg on terminals for 30 seconds, test the breakdown.

UNIT	m/m	DRAWN	CHECK	TITLE	TRANS
TEL	(02)29450588	Ci wun Chen	Guo long Huang	IDENT N O.	TRN-0280
FAX	(02)29447647			D W G N O.	I0802
	No.26-1, Lane 128, Sec. 2, Singnan Rd., Jhonghe City, Taipei County 235, Taiwan (R.O.C.)	SEN HUEI INDUSTRIAL CO.,LTD.			

### SPECIFICATION APPROVAL

Customer	SYSTEM GENERAL CORP.			P/N:	TRN-0280
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#### MATERIALS LIST

COMPONENT	MATERIAL	MANUFACTURE	FILE NO.
1.Bobbin	Phenolic 94v-0,T375J,150°C	RM-8 Chang Chun plastics co. ltd.	E59481(S)
2.Core	FERRITE 3C90 R2K (GAP)	Ferrite core RM-8 YANG GUANG DA CO.,LTD	
3.Wire	UEY 130°C	HOI LUEN ELECTRICAL MFR CO.,LTD	E164409
	TEX-E 130°C	SHEN ZHEN CHANGYUAN ELECTRONIC MATERIAL CO.,LTD	E249037
4.Varnish	48562/C 155°C	HANG CHEUNG PETROCHEMICAL LTD	E200154
5.Tape	MYLAR TAPE (PZ-YELLOW)	JINGJIANG YA HUA PRESSURE SENSITIVE GLUE CO.,LTD	E165111(N)
6.Tube	TEFOLN 200°C 150V	SHEN ZHEN WOER HEAT SHRINKABLE MATERIAL CO.,LTD	E203950
7.Terminals	Tin coated- Copper wire	Will fore special wire corp	
8.Shield	Copper foil	BO TONG CO.,LTD (copper foil : T0.025mm×7mm +TAPE)	
9.SOLDER	96.5% Sn 3% Ag 0.5% Cu	Xin Yuan CO.,LTD	

UNIT	m/m	DRAWN	CHECK	TITLE	TRANS
TEL	(02)29450588	Ci wun Chen	Guo long Huang	IDENT N O.	TRN-0280
FAX	(02)29447647			D W G N O.	I0802
	No.26-1, Lane 128, Sec. 2, Singnan Rd., Jhonghe City, Taipei County 235, Taiwan (R.O.C.)	SEN HUEI INDUSTRIAL CO.,LTD.			

## 24. Revision History

Rev.	Date	Description
1.0.0	September 2012	Initial Release

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