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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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MOS FIELD EFFECT TRANSISTOR μ PA1764

SWITCHING DUAL N-CHANNEL POWER MOS FET INDUSTRIAL USE

DESCRIPTION

The μ PA1764 is N-Channel MOS Field Effect Transistor designed for high current switching applications.

FEATURES

- · Dual chip type
- Low on-state resistance

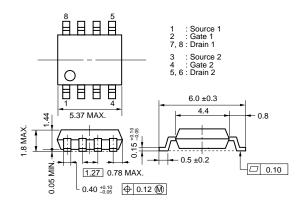
 $R_{DS(on)1} = 27 \text{ m}\Omega \text{ TYP.}$ (Vgs = 10 V, ID = 3.5 A)

RDS(on)2 = 32 m Ω TYP. (VGS = 4.5 V, ID = 3.5 A)

RDS(on)3 = 34 m Ω TYP. (VGS = 4.0 V, ID = 3.5 A)

- Low input capacitance
 C_{iss} = 1300 pF TYP.
- · Built-in G-S protection diode
- Small and surface mount package (Power SOP8)

PACKAGE DRAWING (Unit: mm)



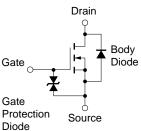
ORDERING INFORMATION

PART NUMBER	PACKAGE
μPA1764G	Power SOP8

ABSOLUTE MAXIMUM RATINGS (TA = 25°C, All terminals are connected.)

Drain to Source Voltage (Vgs = 0 V)	VDSS	60	V	
Gate to Source Voltage (Vps = 0 V)	Vgss	±20	V	F
Drain Current (DC) (Tc = 25°C)	$I_{D(DC)}$	±7	Α	_
Drain Current (pulse) Note1	D(pulse)	±28	Α	
Total Power Dissipation (1 unit) Note2	Рт	1.7	W	
Total Power Dissipation (2 unit) Note2	Рт	2.0	W	
Channel Temperature	Tch	150	°C	
Storage Temperature	T _{stg}	-55 to + 150	°C	(
Single Avalanche Current Note3	las	7	Α	,
Single Avalanche Energy Note3	Eas	98	mJ	F

EQUIVALENT CIRCUIT (1/2 Circuit)



- **Notes 1.** PW \leq 10 μ s, Duty cycle \leq 1%
 - **2.** T_A = 25°C, Mounted on ceramic substrate of 1200 mm² x 2.2 mm
 - 3. Starting T_{ch} = 25°C, V_{DD} = 30 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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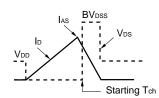


ELECTRICAL CHARACTERISTICS (TA = 25°C, All terminals are connected.)

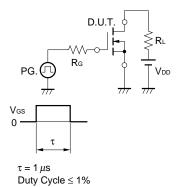
	•					
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 60 V, V _{GS} = 0 V			10	μΑ
Gate Leakage Current	lgss	Vgs = ±20 V, Vps = 0 V			±10	μΑ
Gate Cut-off Voltage	VGS(off)	V _{DS} = 10 V, I _D = 1 mA	1.5	2.0	2.5	V
Forward Transfer Admittance	yfs	V _{DS} = 10 V, I _D = 3.5 A	5.0	9.0		S
Drain to Source On-state Resistance	RDS(on)1	Vgs = 10 V, ID = 3.5 A		27	35	mΩ
	R _{DS(on)2}	V _G S = 4.5 V, I _D = 3.5 A		32	42	mΩ
	RDS(on)3	Vgs = 4.0 V, lp = 3.5 A		34	46	mΩ
Input Capacitance	Ciss	V _{DS} = 10 V		1300		pF
Output Capacitance	Coss	Vgs = 0 V		230		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		110		pF
Turn-on Delay Time	td(on)	V _{DD} = 30 V, I _D = 3.5 A		15		ns
Rise Time	tr	V _G S = 10 V		69		ns
Turn-off Delay Time	td(off)	$R_G = 10 \Omega$		65		ns
Fall Time	tr			27		ns
Total Gate Charge	Q _G	V _{DD} = 48 V		29		nC
Gate to Source Charge	Qgs	V _G S = 10 V		3.6		nC
Gate to Drain Charge	Q _{GD}	ID = 7.0 A		7.4		nC
Body Diode Forward Voltage	VF(S-D)	IF = 7.0 A, VGS = 0 V		0.84		V
Reverse Recovery Time	trr	IF = 7.0 A, Vgs = 0 V		40		ns
Reverse Recovery Charge	Qıı	di/dt = 100 A/μs		66		nC

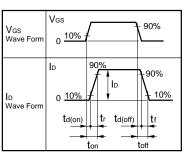
TEST CIRCUIT 1 AVALANCHE CAPABILITY

$\begin{array}{c|c} \text{D.U.T.} \\ \text{RG} = 25 \ \Omega \\ \text{PG.} \\ \hline \\ \text{V}_{\text{GS}} = 20 \rightarrow 0 \ \text{V} \end{array}$



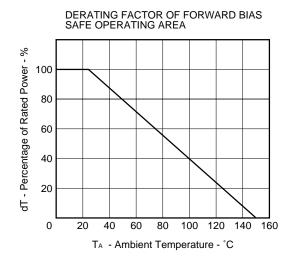
TEST CIRCUIT 2 SWITCHING TIME

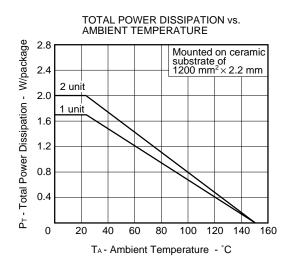


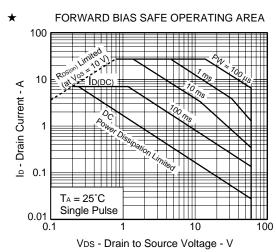


TEST CIRCUIT 3 GATE CHARGE

TYPICAL CHARACTERISTICS (TA = 25°C, All terminals are connected.)

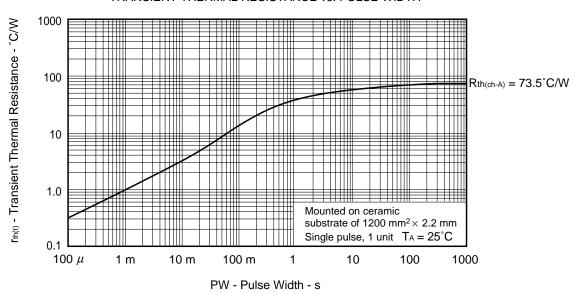




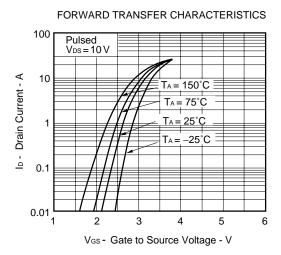


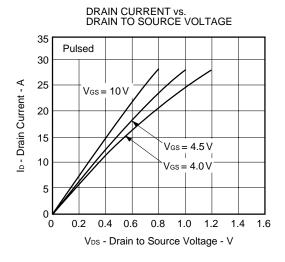
Remark Mounted on ceramic substrate of $1200 \text{ mm}^2 \times 2.2 \text{ mm}$

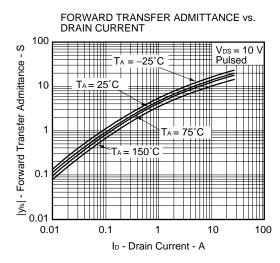
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

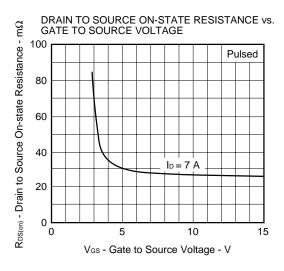


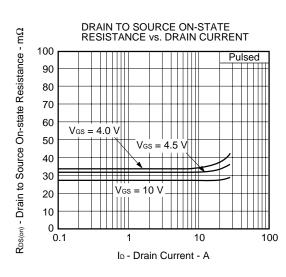
Data Sheet G14329EJ2V0DS

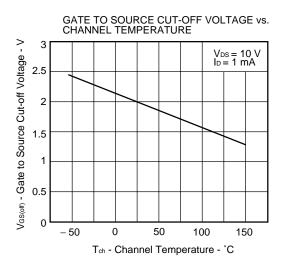


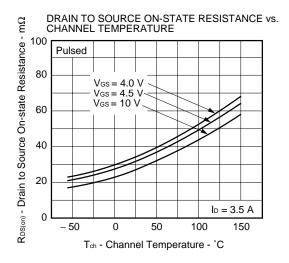


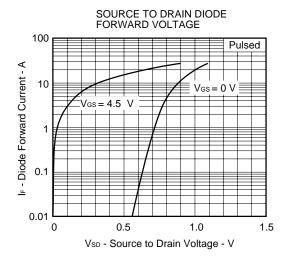


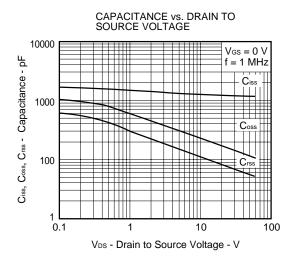


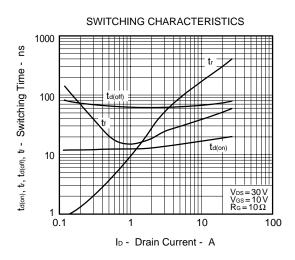


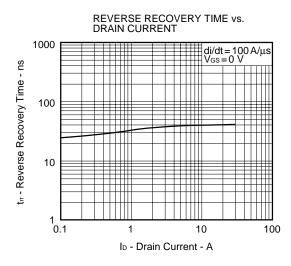


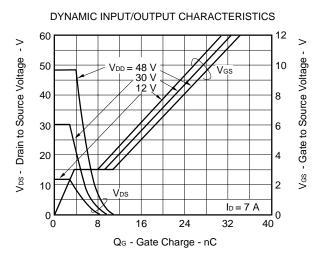




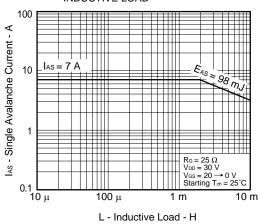




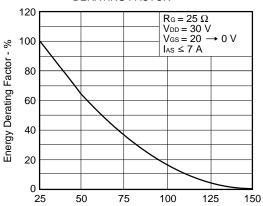




SINGLE AVALANCHE CURRENT vs. INDUCTIVE LOAD



SINGLE AVALANCHE ENERGY DERATING FACTOR



Starting Tch - Starting Channel Temperature - °C

NEC μ PA1764

[MEMO]

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