



DMS3016SSS

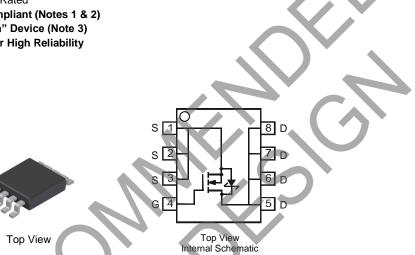
N-CHANNEL ENHANCEMENT MODE MOSFET WITH SCHOTTKY DIODE

Features

- DIOFET Utilizes a Unique Patented Process to Monolithically Integrate a MOSFET and a Schottky in a Single Die to Deliver:
 - Low R_{DS(ON)} Minimizes Conduction Losses
 - \bullet Low V_{SD} Reducing the Losses Due to Body Diode Conduction
 - Low Q_{rr} Lower Q_{rr} of the Integrated Schottky Reduces Body Diode Switching Losses
 - Low Gate Capacitance (Q_g/Q_{gs}) Ratio Reduces Risk of Shoot-Through or Cross Conduction Currents at High Frequencies
 - Avalanche Rugged I_{AR} and E_{AR} Rated
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Weight: 0.072 grams (Approximate)



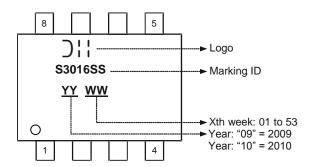
Ordering Information (Note 4)

Part Number	Case	Packaging
DMS3016SSS-13	SO-8	2500 / Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information





DMS3016SSS

$\begin{tabular}{ll} \textbf{Maximum Ratings} (@T_A = +25 ^{\circ}C, unless otherwise specified.) \end{tabular}$

Characteri	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	30	V		
Gate-Source Voltage	V _{GSS}	±12	V		
Continuous Drain Current (Note 5) V _{GS} = 4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +85^{\circ}C$	I _D	9.8 6.3	А
Pulsed Drain Current (Note 6)	I _{DM}	90	Α		
Avalanche Current (Note 6) (Note 7)			I _{AR}	13	Α
Repetitive Avalanche Energy (Note 6) (Note 7) L	E _{AR}	25.4	mJ		

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	1.54	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	$R_{ heta JA}$	81	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

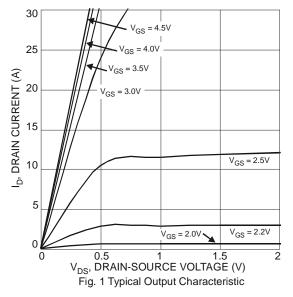
		_			
Symbol	Min	Тур	Max	Unit	Test Condition
BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_{D} = 250\mu A$
I _{DSS}	-		0.1	mA	$V_{DS} = 30V$, $V_{GS} = 0V$
I _{GSS}	- '		±100	nA	$V_{GS} = \pm 12V$, $V_{DS} = 0V$
V _{GS(TH)}	1.0	7	2.3	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
· ·		9	13	mO	$V_{GS} = 10V, I_D = 9.8A$
KDS(ON)	-	11	16		$V_{GS} = 4.5V, I_D = 9.8A$
Y _{fs}	7	5	-	S	$V_{DS} = 5V, I_{D} = 9.8A$
V _{SD}	-	0.4	1	V	$V_{GS} = 0V, I_{S} = 1A$
I _S	1	-	5	Α	-
C _{iss}	ľ	1849	-	pF	\\ 45\\\\\ 0\\
Coss	-	158	-	pF	V _{DS} =15V, V _{GS} = 0V, f = 1.0MHz
C _{rss}		123	-	pF	1 = 1.0W112
Rg	0.53	2.68	4.82	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Qg	,	18.5	-	nC	
Q_{g}	1	43	-	nC	$V_{DS} = 15V, V_{GS} = 10V,$
Qgs	-	4.7	-	nC	$I_D = 9.8A$
Qgd	-	4.0	-	nC	
t _{D(ON)}	-	6.62	-	ns	
t _r	ı	8.73	-	ns	$V_{GS} = 10V, V_{DS} = 10V,$
t _{D(OFF)}	ı	36.41	-	ns	$R_g = 3\Omega$, $R_L = 1.2\Omega$
t _f	-	4.69	-	ns	
	BVDSS IDSS IGSS VGS(TH) RDS(ON) IYfs VSD IS Ciss Coss Crss Rq Qg Qg Qgs Qgd tD(ON) tr tD(OFF)	BVDSS 30 IDSS -	BV _{DSS} 30 -	BVDSS 30 - -	BVDSS 30 - - V IDSS - - 0.1 mA mA mA mA mA mΩ mΩ mΩ

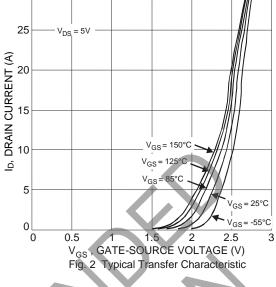
 Device mounted on minimum recommended layout. The value in any given application depends on the user's specific board design.
 Repetitive rating, pulse width limited by junction temperature.
 I_{AR} and E_{AR} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing. Notes:

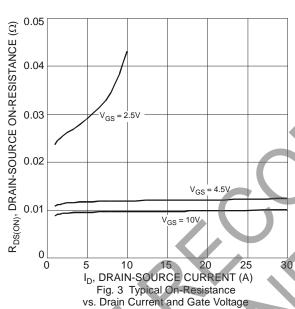


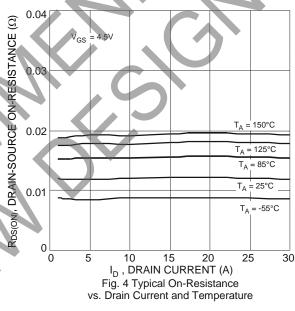
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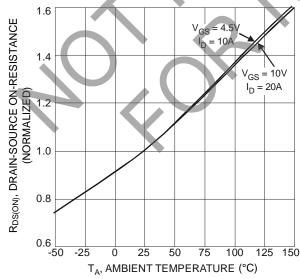
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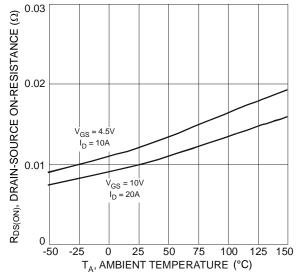


Fig. 5 On-Resistance Variation with Temperature

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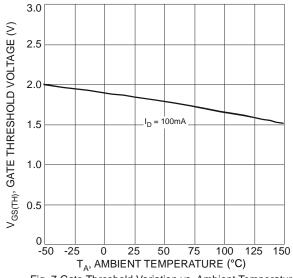
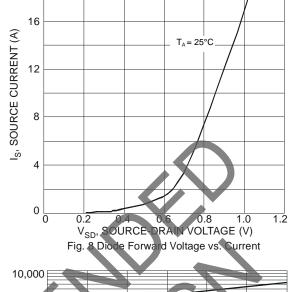
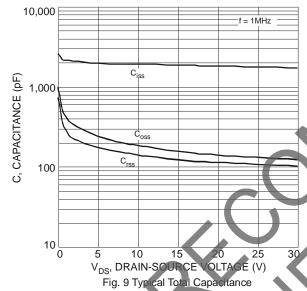
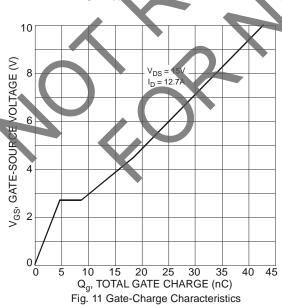
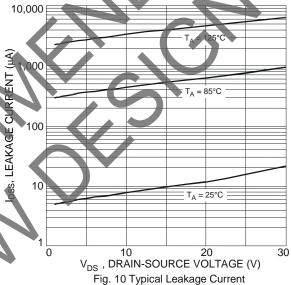


Fig. 7 Gate Threshold Variation vs. Ambient Temperature









vs. Drain-Source Voltage



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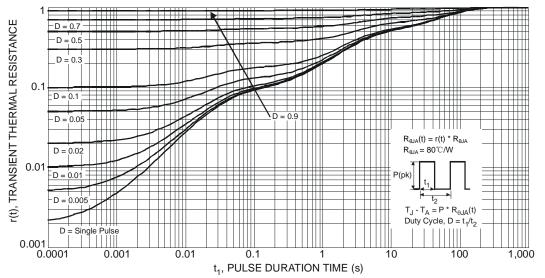


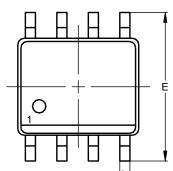
Fig. 12 Transient Thermal Response

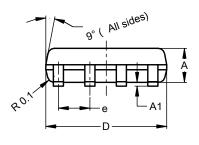
SO-8

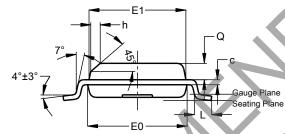


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



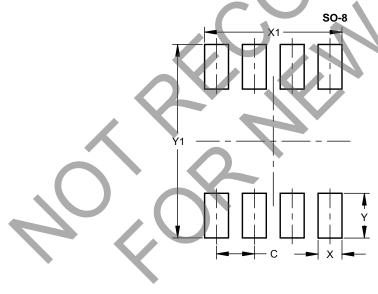




SO-8					
Dim	Min	Max	Тур		
Α	1.40	1.50	1.45		
A1	0.10	0.20	0.15		
Ь	0.30	0.50	0.40		
O	0.15	0.25	0.20		
D	4.85	4.95	4.90		
E	5.90	6.10	6.00		
E1	3.80	3.90	3.85		
E0	3.85	3.95	3.90		
е	1		1.27		
h	-		0.35		
7	0.62	0.82	0.72		
þ	0.60	0.70	0.65		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.27
Х	0.802
X1	4.612
Y	1.505
Y1	6.50



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