PNA1401LF, PNZ102F

Silicon NPN Phototransistors

For optical control systems

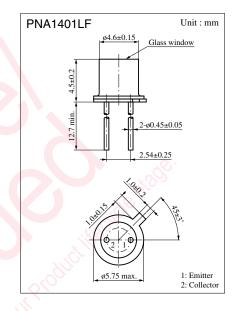
Features

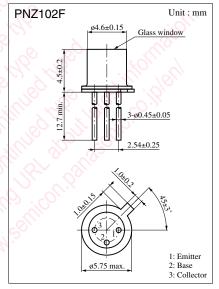
- Flat window design which is suited to optical systems
- Low dark current : $I_{CEO} = 5 \text{ nA (typ.)}$
- Fast response : t_r , $t_f = 3 \mu s$ (typ.)
- Wide directional sensitivity
- Base pin for easy circuit design (PNZ102F)

■ Absolute Maximum Ratings (Ta = 25°C)

Parameter	Symbol	Ratings	Unit
Collector to emitter voltage	V _{CEO}	30	V
Collector to base voltage	V _{CBO} *	40	V
Emitter to collector voltage	V _{ECO}	5	V
Emitter to base voltage	${ m V_{EBO}}^*$	5	VO
Collector current	I_{C}	50	mA
Collector power dissipation	$P_{\rm C}$	150	mW
Operating ambient temperature	Topr	-25 to +85	°C
Storage temperature	T _{stg}	-30 to +100	°C Ø

^{*} PNZ102F only





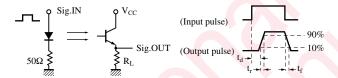


Electro-Optical Characteristics ($Ta = 25^{\circ}C$)

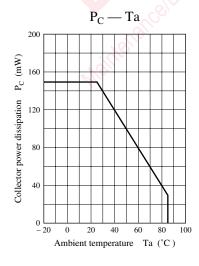
Parameter	Symbol	Conditions		typ	max	Unit
Dark current	I_{CEO}	$V_{CE} = 10V$		5	300	nA
Collector photo current	I _{CE(L)}	$V_{CE} = 10V, L = 100 lx^{*1}$		0.3		mA
Peak sensitivity wave length	λ_{P}	$V_{CE} = 10V$		800		nm
Acceptance half angle	θ	Measured from the optical axis to the half power point		40		deg.
Response time	t_r, t_f^{*2}	$V_{CC} = 10V, I_{CE(L)} = 5mA, R_L = 100\Omega$		3		μs
Collector saturation voltage	V _{CE(sat)}	$L = 500 \text{ lx}^{*1} \frac{\text{PNA1401LF } I_{\text{CE}(L)} = 0.1 \text{n}}{\text{PNZ102F } I_{\text{CE}(L)} = 0.1 \text{mA}}$	_	0.2	0.4	V

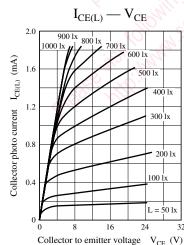
^{*1} Measurements were made using a tungsten lamp (color temperature T = 2856K) as a light source.

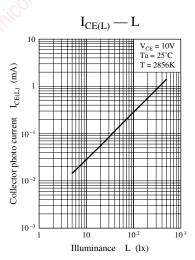
^{*2} Switching time measurement circuit

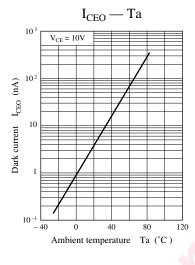


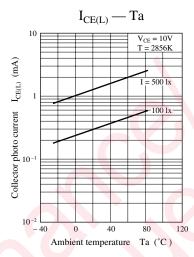
- t_d: Delay time
- t_r: Rise time (Time required for the collector photo current to increase from 10% to 90% of its final value)
- t_r: Fall time (Time required for the collector photo current to decrease from 90% to 10% of its initial value)

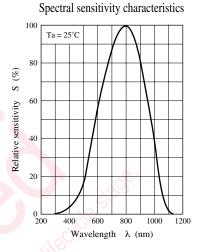


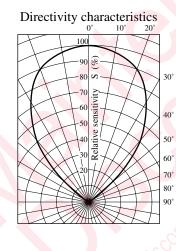


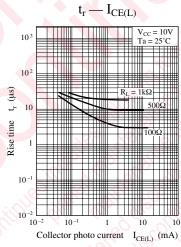


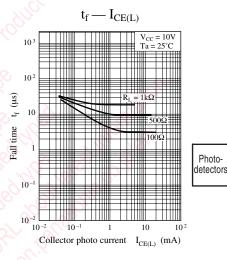












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