

Engineering/Process Change Notice

ECN/PCN No.: 4162

For Manufacturer				
Product Description: PLASTIC SMD MEMS OSCILLATOR	Abracon Part Number / Part Series: EMTB84	☐ Documentation only☐ ECN☒ EOL	⊠ Series □ Part Number	
Affected Revision:	New Revision:	Application:	☐ Safety	
В	EOL		⊠ Non-Safety	
Prior to Change:				
Active				
After Change:				
EOL				
Cause/Reason for Change:				
Discontinuation of manufacturing capabilit	<u> </u>			
	Change Plan			
Effective Date:	Additional Remarks:			
2/7/2022	N/A			
Change Declaration: N/A				
Issued Date: 2/7/2022	Issued By:	Issued Department:		
Approval:	Approval:	Approval:		
	For Abracon EOL only			
Last Time Buy (if applicable):	Alternate Part Num	her / Part Series:		
5/7/2022	Attenute Furt Hum	none		
Additional Approval:	Additional Approval:	Additional Approval:		
Additional Approval.	Additional Approval.	Additional Approval.		
Customer Approval (If Applicable)				
Qualification Status:	cuccomer rippi e an (rippi camere)			
Qualification Status: ☐ Approved ☐ Not accepted				
Note: It is considered approved if there is n	o feedback from the customer 1 month afte	er ECN/PCN is released.		
Customer Part Number:	Customer Project:			
Customer Project:				
Company Name:	Company Representative:	Representative Signature	•	
Company Name:	Company Representative:	Representative Signature	•	
Customer Remarks:				

Form #7020 | Rev. G | Effective: 02/22/2021 |













EMTB84 Series



REGULATORY COMPLIANCE











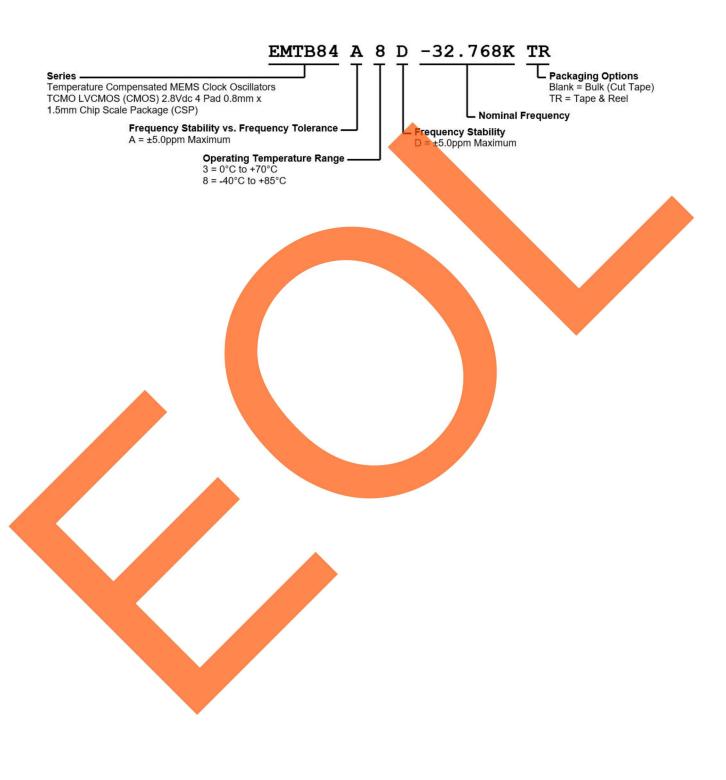
ITEM DESCRIPTION

Temperature Compensated MEMS Clock Oscillators TCMO LVCMOS (CMOS) 2.8Vdc 4 Pad 0.8mm x 1.5mm Chip Scale Package (CSP)

ELECTRICAL OPECIFICATIONS		
ELECTRICAL SPECIFICAT		
Nominal Frequency	32.768kHz	
Frequency Tolerance/Stability	Inclusive of Operating Temperature Range, Output Load Change (±20%), and Reflow, at Vdd=2.8Vdc	
	±5.0ppm Maximum	
Frequency Stability vs. Frequency	Measured at 25°C ±2°C, at Vdd=2.8Vdc, Post Reflow	
Tolerance	±5.0ppm Maximum	
Frequency Stability vs. Input Voltage	±0.75ppm Maximum (±10%)	
Frequency Stability vs. Aging	±1ppm/Year Maximum (at 25°C)	
Operating Temperature Range	0°C to +70°C	
	-40°C to +85°C	
Supply Voltage	2.8Vdc ±10%	
Core Operating Current	0.99μA Typical (at 25°C), 1.52μA Maximum	
Output Stage Operating Current	0.065μA/Vpp <mark>Typical</mark> , 0.125μA/Vpp Maximum	
Input Current	No Load, No <mark>minal V</mark> dd	
	1.2μΑ Typica <mark>l (at 25</mark> °C), 1.9μΑ Maximum	
Output Voltage Logic High (V _{OH})	IOH = -10µA	
	90% of Vdd Minimum	
Output Voltage Logic Low (VoL)	IOL = +10µA 10% of Vdd Maximum	
Rise/Fall Time	Measured at 10% to 90% of waveform	
Rise/Fail Time	100nSec Typical, 200nSec Maximum	
Duty Cycle	Measured at 50% of waveform	
Buty Gyele	50 ±2(%)	
Load Drive Capability	15pF Maximum	
Output Logic Type	CMOS	
Peak to Peak Jitter (tPK)	2,5µSec Maximum	
Period Jitter (RMS)	Measured at 25°C	
	33nSec Typical	
Power Supply Ramp	Measured at 0Vdc to 90% of Vdd	
	100mSec Maximum	
Start Up Time	Measured at Nominal Vdd	
	180mSec Typical, 380mSec Maximum at Operating Temperature Range of -40°C to +85°C	
Storage Townsystiae Bonns	180mSec Typical, 350mSec Maximum at Operating Temperature Range of 0°C to +70°C	
Storage Temperature Range	-00 C 10 +120 C	

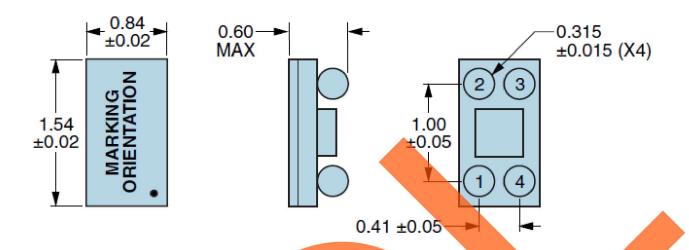


PART NUMBERING GUIDE

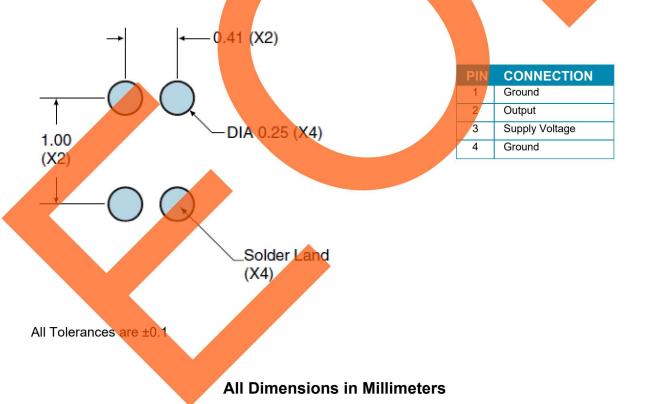




MECHANICAL DIMENSIONS



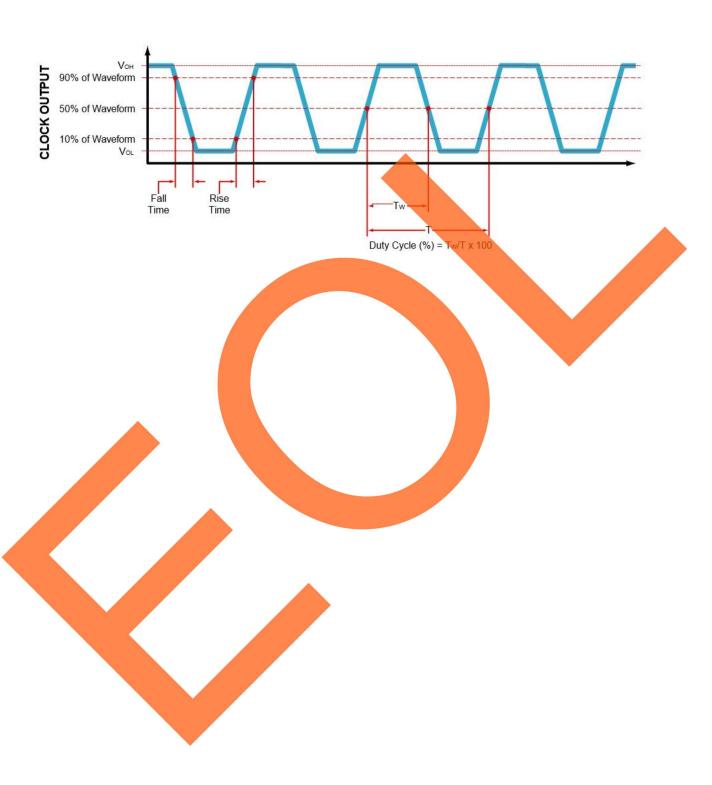
SUGGESTED SOLDER PAD LAYOUT



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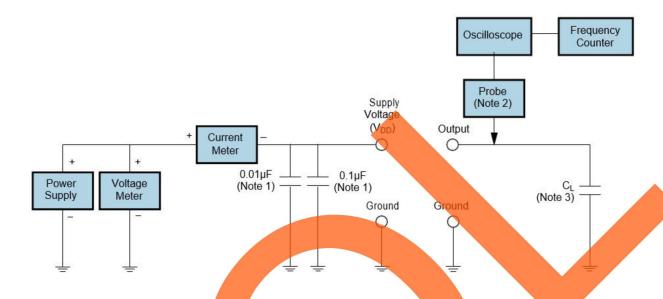


OUTPUT WAVEFORM





TEST CIRCUIT FOR CMOS OUTPUT



- Note 1: An external 0.01μF ceramic bypass capacitor in parallel with a 0.1μF high frequency ceramic bypass capacitor close (less Than 2mm) to the package ground and supply voltage pin is required.
 Note 2: A low input capacitance (<12pF), 10X Attenuation Factor, High Impedance (>10Mohms), and High bandwidth (>300MHz)
- Passive probe is recommended.
- Note 3: Capacitance value CL includes sum of all probe and fixture capacitance. See applicable specification sheet for 'Load Drive Capability'.

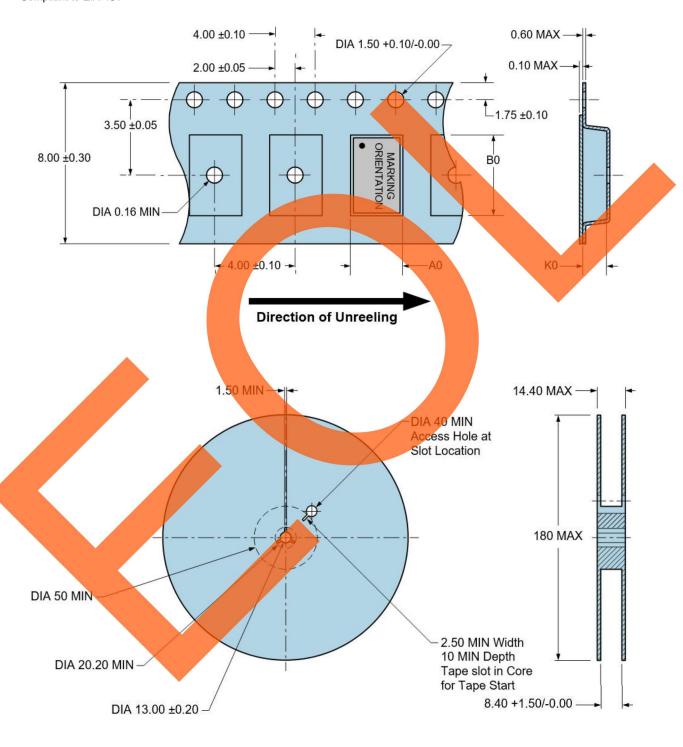
EMTB84 Series



TAPE & REEL DIMENSIONS

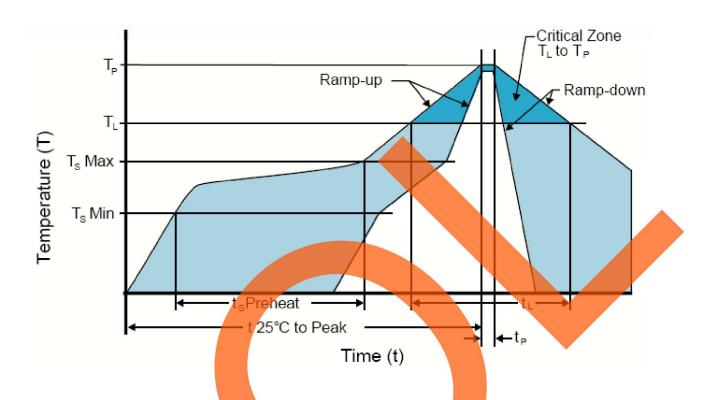
Quantity per Reel: 3,000 Units

All Dimensions in Millimeters
Compliant to EIA-481





RECOMMENDED SOLDER REFLOW METHOD



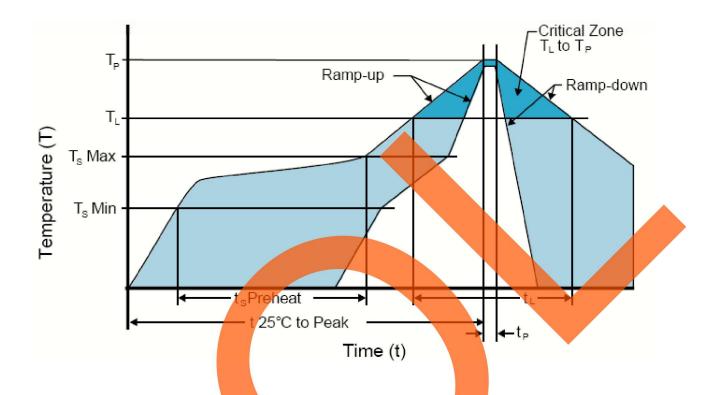
HIGH TEMPERATURE INFRARED/CONVECTION		
T _s MAX to T _L (Ramp-up Rate)	3°C/Second Maximum	
Preheat		
- Temperature Minimum (T _s MIN)	150°C	
- Temperature Typical (T _S TYP)	175°C	
- Temperature Maximum(T _s MAX)	200°C	
- Time (t _s MIN)	60 - 180 Seconds	
Ramp-up Rate (T _L to T _P)	3°C/Second Maximum	
Time Maintained Above:	_	
· omportation ()	217°C	
- Time (t _L)	60 - 150 Seconds	
Peak Temperature (T _P)	260°C Maximum for 10 Seconds Maximum	
Target Peak Temperature(Tp Target)	250°C +0/-5°C	
Time within 5°C of actual peak (tp)	20 - 40 Seconds	
Ramp-down Rate	6°C/Second Maximum	
Time 25°C to Peak Temperature (t)	8 Minutes Maximum	
Moisture Sensitivity Level	Level 1	
Additional Notes	Temperatures shown are applied to body of device.	

High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)



RECOMMENDED SOLDER REFLOW METHOD



LOW TEMPERATURE INFRARED/CONVECTION		
T _s MAX to T _L (Ramp-up Rate)	5°C/Second Maximum	
Preheat		
- Temperature Minimum (T _s MIN)	N/A	
- Temperature Typical (Ts TYP)	150°C	
- Temperature Maximum(T _s MAX)	N/A	
- Time (t _s MIN)	60 - 120 Seconds	
Ramp-up Rate (T _L to T _P)	5°C/Second Maximum	
Time Maintained Above:	<u> </u>	
- Temperature (T⊾)	150°C	
- Time (t _L)	200 Seconds Maximum	
Peak Temperature (T _P)	240°C Maximum	
Target Peak Temperature(Tp Target)	240°C Maximum 2 Times/230°C Maximum 1Time	
Time within 5°C of actual peak (tp)	10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time	
Ramp-down Rate	5°C/Second Maximum	
Time 25°C to Peak Temperature (t)	N/A	
Moisture Sensitivity Level	Level 1	
Additional Notes	Temperatures shown are applied to body of device.	

Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)