TOSHIBA

TOSHIBA RECTIFIER SILICON DIFFUSED TYPE

CMC02

For Strobe Discharge Circuit

- Repetitive peak reverse voltage: $V_{RRM} = 400 V$
- Average forward current: $I_F (AV) = 1.0 A$
- Low forward voltage: $V_{FM} = 1.0 V (max.)$
- Repetitive peak forward current: IFRM = 150 A (Note 2)
- Small surface-mount package, the "M-FLATTM" (Toshiba package name)

Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Repetitive peak reverse voltage	V _{RRM}	400 V		
Average forward current	I _{F (AV)}	1.0 (Note 1)	А	
Peak one-cycle surge forward current (non-repetitive)	I _{FSM}	30 (50 Hz) A		
Repetitive peak forward current (Note 2)	I _{FRM}	150 A		
Junction temperature	Tj	–40~150 °C		
Storage temperature range	T _{stg}	-40~150	°C	



Weight: 0.023 g (typ.)

Note 1: Ta = 92°C

Device mounted on a ceramic board Board size: 50 mm \times 50 mm Soldering land: 2 mm \times 2 mm Board thickness: 0.64t

Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Peak forward voltage	V _{FM (1)}	I _{FM} = 1.0 A (pulse test)	_	0.88	1.0	V	
Peak repetitive reverse current	I _{RRM}	V _{RRM} = 400 V (pulse test)	_	—	10	μA	
Thermal resistance (junction to ambient)	R _{th (j-a)}	Device mounted on a ceramic board (board size: 50 mm \times 50 mm) (soldering land: 2 mm \times 2 mm) (board thickness: 0.64 t)		—	60		
		Device mounted on a glass-epoxy board (board size: $50 \text{ mm} \times 50 \text{ mm}$) (soldering land: $6 \text{ mm} \times 6 \text{ mm}$) (board thickness: 1.6 t)	_	_	110		
		Device mounted on a glass-epoxy board (board size: $50 \text{ mm} \times 50 \text{ mm}$) (soldering land: 2.1 mm \times 1.4 mm) (board thickness: 1.6 t)	_	_	180		
Thermal resistance (junction to lead)	R _{th (j-ℓ)}	_		_	16	°C/W	

Unit: mm

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Note 2: Repetitive peak forward current waveform



Marking

Type code	Product No.		
C2	CMC02		

Standard Soldering Pad

Unit: mm



Handling Precautions

The maximum ratings denote the absolute maximum ratings, which are rated values and must not be exceeded during operation, even for an instant. The following are the general derating methods we recommend for designing a circuit using this device.

- VRRM: We recommend that the worst case voltage, including surge voltage, be no greater than 80% of the maximum rating of VRRM for a DC circuit, and no greater than 50% of that of VRRM for an AC circuit. VRRM has a temperature coefficient of 0.1%. Be sure to take this temperature coefficient into account when designing a device for use at low temperature.
- IF (AV): We recommend that the worst case current be no greater than 80% of the maximum rating of IF (AV). Carry out sufficient heat design. If it is not possible to design a circuit with excellent heat radiation, set a margin by using an allowable Tamax- IF (AV) curve.

This rating applies only to a strobe flash circuit. We recommend that the worst case current be kept within the maximum rating of I_{FRM} . The total number of repetitive currents should not exceed 7000 within the lifespan of the device.

This rating specifies the non-repetitive peak current in one cycle of a 50 Hz sine wave, condition angle 180°. Therefore the rating applies only to abnormal operation, which seldom occurs during the lifespan of a device.

For this device, we recommend a Tj of below 120 under the worst load and heat radiation conditions.

Thermal resistance between junction and ambient fluctuates depending on the mounting condition of the device. When using the device, be sure to design the circuit board and soldering land size to match the appropriate thermal resistance value.

Refer to the Rectifier databook for further information.

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Ta max – I_{F (AV)} Device mounted on a glass-epoxy board (board size:50 mm x 50 mm, soldering land 6 mm x 6 mm) 160 Maximum allowable ambient temperature Ta max (°C) Halfsine waveform 140 0° 🗲 →180 120 Conduction angle 180° 100 80 60 40 20 0 0.2 0.4 0.6 0.8 1.0 12 Average forward current IF (AV) (A)







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