## **UM10455**

# User manual for the BGU7003 100MHz LNA evaluation board Rev. 1.1 — 14 December 2011 User manual

**User manual** 

#### **Document information**

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## **BGU7003 100MHz LNA EVB**

## **Revision history**

Rev	Date	Description
1.1	20111214	Board order numbers added
1	20110729	Initial document

## **Contact information**

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#### **BGU7003 100MHz LNA EVB**

## 1. Introduction

NXP Semiconductors' BGU7003 is wideband Silicon Germanium Amplifier MMICs for high speed, low noise applications. It can be used mainly for LNA applications up to 6 GHz like GPS, Satellite radio, Cordless Phone. The BGU7003 contains one RF stage and internal bias that is temperature stabilized. It also contains a power down function to shutdown the amplifier by means of a logic signal on the enable pin.

The BGU7003 is ideal for use in portable electronic devices, such as mobile phones, Personal Digital Assistants (PDAs), Personal Navigation Devices (PNDs) etc.

The 100MHz LNA BGU7003 Universal Evaluation Board (EVB) see Fig 1 is designed to evaluate the performance of the BGU7003 applied as a 100MHz LNA. In this document, the application diagram, board layout, bill of material, and some typical results are given.

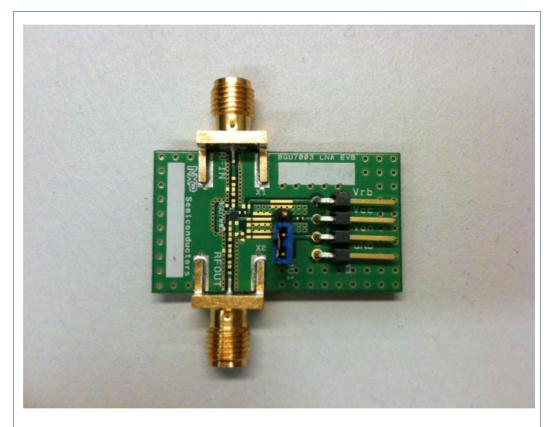
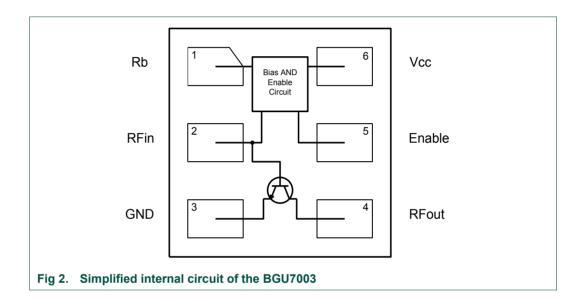


Fig 1. BGU7003 100MHz LNA evaluation board.

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## 2. General Description

NXP Semiconductors' BGU7003 consist of a wideband Silicon Germanium SiGe transistor with internal bias circuit. This bias circuit is temperature stabilized, which keeps the current constant over temperature. The bias current for the RF stage can be set via an external bias resistor enabling flexibility in choosing the bias current. The BGU7003 is also supplied with an enable function that allows it to be controlled with a logic signal. This disable mode it only consumes less than 0.4  $\mu A$ . In Fig 2 the simplified internal circuit of the BGU7003 is given.



The BGU7003 is not internally matched so for both input and output a matching circuit needs to be designed. The fact that no internal matching is available makes the product suitable for different application/frequency areas.

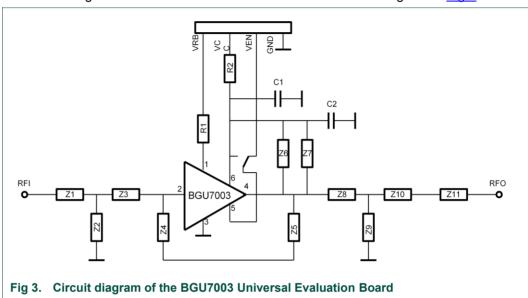
In the next paragraphs the BGU7003 applied as a 100MHz LNA is described.

## 3. Application Board

The BGU7003 Universal Evaluation Board 100MHz LNA simplifies the evaluation of the BGU7003 wideband amplifier MMIC, for the 100MHz application area. The BGU7003 Universal Evaluation Board enables testing of the device performance and requires no additional support circuitry. The board is fully assembled with the BGU7003 IC, including input- and output matching, to optimize the performance. The board is supplied with two SMA connectors for input and output connection to RF test equipment.

## 3.1 Application Circuit

The circuit diagram of the BGU7003 Universal Evaluation Board is given in Fig 3.



## 3.2 Board Layout

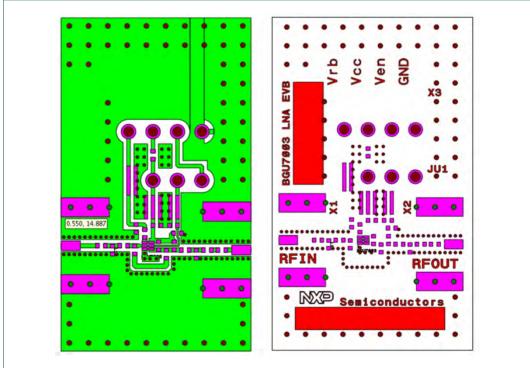
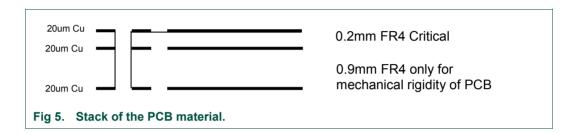


Fig 4. Printed circuit board lay-out of the BGU7003 Universal Evaluation Board.

#### **BGU7003 100MHz LNA EVB**

## 3.3 PCB layout

A good PCB Layout is an essential part of an RF circuit design. The BGU7003 Universal Evaluation Board can serve as a guideline for laying out a board using the BGU7003. Use controlled impedance lines for all high frequency inputs and outputs. Bypass  $V_{\rm CC}$  with decoupling capacitors, preferable located as close as possible to the device. For long bias lines it may be necessary to add decoupling capacitors along the line further away from the device. Proper grounding the GND pin is also essential for good RF performance. Either connect the GND pin directly to the ground plane or through vias, or do both. The material that has been used for the EVB is FR4 using the stack shown in Fig 5. Material supplier is ISOLA DURAVER; Er= 4.6-4.9:  $T\delta$ =0.02.



#### 3.4 Bill of materials

Table 1: BOM of the BGU7003 100MHz LNA evaluation board.

Designator	Description	Footprint	Value	Supplier Name/type	Comment
Z1	L	0402	82nH	muRata / LQW15 (wire wound)	Input Match
Z2	С	0402	16pF		Input Match
Z3	С	0402	470pF		DC-Block
Z4	С	0402	470pF		DC-Block
Z5	R	0402	1.8kΩ	Various	RF Feedback
Z6	n.m.	0402	-		-
<b>Z</b> 7	L	0402	270nH	muRata / LQG15	Output Match
Z8	С	0402	27pF		Output Match
Z9	R	0402	100Ω	Various	Output Match / Stability
Z10	С	0402	470pF		DC-Block
Z11	R	0402	Ω0	Various	Short
C1	С	0402	470pF		Decoupling
C2	С	0402	470pF		Decoupling
R1	R	0402	3k9Ω	Various	Bias setting.
R2	R	0402	0Ω	Various	Short
B3	MMIC		BGU7003	NXP	-

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## 4. Required Equipment

In order to measure the BGU7003 Universal Evaluation Board the following is necessary.

- DC Power Supply up to 30 mA at 2 to 2.8 V (up to 15V for bias control)
- An RF signal generator capable of generating an RF signal at the operating frequency of 1GHz.
- An RF spectrum analyzer that covers at least the operating frequency of 1GHz as well as a few of the harmonics, so up to 6 GHz should be sufficient.
- "Optional" a version with the capability of measuring noise figure is convenient.
- Amp meter to measure the supply current (optional).
- A network analyzer for measuring gain, return loss and reverse Isolation.
- Noise figure analyzer and noise source.
- Proper RF cables

## 5. Connections and Set\_up

The BGU7003 Universal Evaluation Board 100MHz LNA is fully assembled and tested. Please follow the steps below for a step-by-step guide to operate the BGU7003 Universal Evaluation Board and testing the device functions.

- 1. Connect the DC power supply set to 2.8V to the  $V_{CC}$ ,  $V_{Rb}$   $V_{en}$  and GND terminals. Set the power supply to the desired supply voltage, between 2.2 and 2.85V, but never exceed 3.0V as it might damage the BGU7003.
- Connect the RF signal generator and the Spectrum Analyzer; to the RF input and the RF output of the BGU7003 Universal Evaluation Board respectively. Do not turn on the RF output of the signal generator yet, set it to -40dBm output power at 100MHz, set the spectrum analyzer on 100MHz center frequency and a reference level of 0dBm.
- 3. Turn on the DC power supply and it should read approximately 5mA.
- 4. Enable the RF output of the generator; the spectrum analyzer displays a tone of 100MHz at around –26 dBm.
- In order to evaluate the board on different bias currents through RF stage of the MMIC the Voltage on Rb (V<sub>Rb</sub>) can be connected to a separate power supply. This is enabling the control of the bias current.
- To evaluate the enable function the V<sub>en</sub> terminal of the board can also be connected to a separate DC power supply that either gives a voltage >0.6V (amplifier on) or <0.5V amplifier off.</li>
- 7. Instead of using a signal generator and spectrum analyzer one can also use a network analyzer in order to measure Gain as well as in- and output return loss.
- 8. For Noise figure evaluation, either a noise-figure analyzer or a spectrum analyzer with noise option can be used. The use of a 5 dB noise source, like the Agilent 364A is recommended. When measuring the noise figure of the evaluation board, any kind of adaptors, cables etc between the noise source and the BGU7003 Universal Evaluation Board should be avoided, since this affects the noise performance.

## **BGU7003 100MHz LNA EVB**

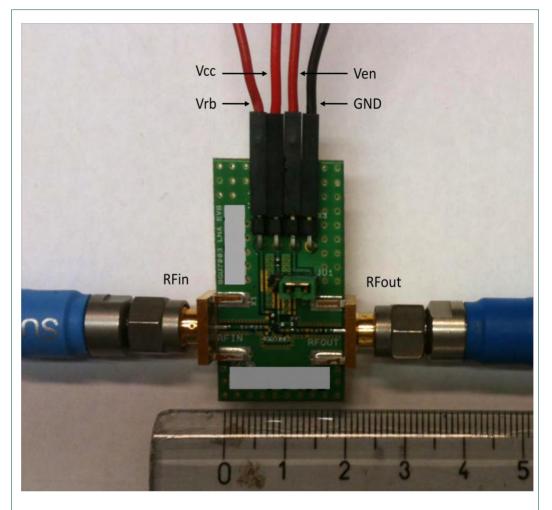


Fig 6. BGU7003 Universal Evaluation Board 100MHz LNA including its connections

#### **BGU7003 100MHz LNA EVB**

## 6. Typical EVB Results

The tables and figures in this paragraph show the results measured for a numbers of EVR's

Table 2: typical results measured on the Evaluation Boards.

Operating Frequency is 100MHz; Vcc and Ven = 2.8V (Icc~5mA) Temp = 25 °C, unless otherwise specified.

Parameter	Symbol	BGU7003 EVB	Unit	Comment
Noise Figure	NF	1.6	dB	ш
Power Gain	Gp	20.5	dB	[2]
Input Return Loss	IRL	15	dB	2
Output Return Loss	ORL	19	dB	[2]
Reverse Isolation		29	dB	<u> 21</u>
Output 1dB Gain	OP1dB	0	dBm	
Output third order	OIP3	16	dBm	31
on/off switching time	T on/off	0.7	μs	

<sup>[1]</sup> The NF and Gain figures are being measured at the SMA connectors of the EVB, so the losses of the connectors and the PCB are not subtracted. If you do so the NF will improve approximately 0.1 dB.

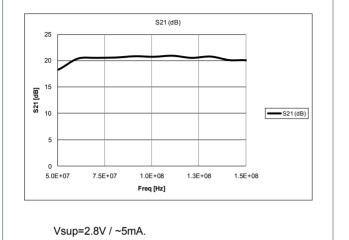
The figures on the next page give measured S-Parameter results of a typical BGU7003 100MHz LNA made with the Universal Board.

This 100MHz LNA was designed with a trade-off between optimal input-match and lowest NF.

<sup>[2]</sup> Pin=-45dBm

<sup>[3] 2-</sup>Tone test with F<sub>1</sub>=100MHz, F<sub>2</sub>=101MHz (Spacing 1MHz). Highest spurious used for calculations.

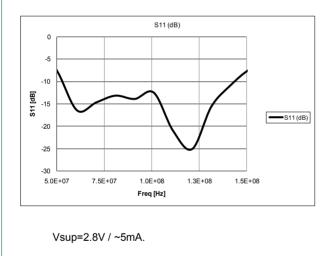
#### **BGU7003 100MHz LNA EVB**



S12(dB) -5 -10 -15 -20 ----S12(dB) -25 -30 -35 5.0E+07 7.5E+07 1.0E+08 1.3E+08 1.5E+08 Frea (Hz1 Vsup=2.8V / ~5mA.

Fig 7. BGU7003 UNI EVB 100MHz LNA: S<sub>21</sub>





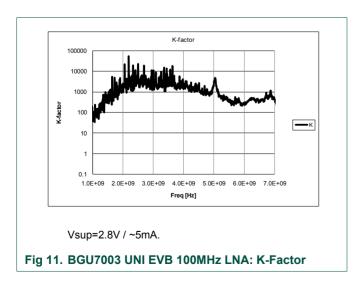
S22 (dB)

0
-5
-5
-20
-10
-20
-5.00E+07
-7.50E+07
-1.00E+08
-1.25E+08
-1.50E+08
-1.50E+08

Fig 9. BGU7003 UNI EVB 100MHz LNA: S<sub>11</sub>

Fig 10. BGU7003 UNI EVB 100MHz LNA: S<sub>22</sub>

Vsup=2.8V / ~5mA.



#### **BGU7003 100MHz LNA EVB**

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