## **ARM® DSTREAM**

## **Debug and Trace Adapter**





http://ds.arm.com/ds-5/debug/dstream/

Features		DSTREAM	
Debug & Trace			
Serial	Wire Debug (SWD)	$\checkmark$	
• JTAG		$\checkmark$	
CoreSi	ght	✓	
Supported ARM an	chitecture		
• ARM v	1 to v8	✓	
Interface Vref		1,0V - 5,0V	
		(configurable by target )	
Target Connector			
• 14-pin	& 20-pin	ARM JTAG	
• 14-pin		TI OMAP	
• 10-pin	& 20-pin	CoreSight	
• 38-pin		MICTOR	
• 34-pin		MIPI	
• 60-pin		MIPI via adaptor	
Order Code			
• DSTRE/	M DEBUG & TRACE UNIT	DSTRM-KT-0181A	
DSTREA	AM High-Speed Serial Trace	DSTRM-KT-HSSTA	
DSTREA	AM and HSSTP Probe Bundle	BUNDS-KT-DSHSS	

The ARM® DSTREAM High-Performance Debug and Trace unit enables powerful software debug and optimization on any ARM processor-based hardware target.

With features such as accelerated hardware bring-up for many development platforms and open debug interface for use with third-party and custom tools, DSTREAM paired with DS-5 Development Studio is a comprehensive solution for development and debug of complex SoCs based on an ARM Core.

DSTREAM ships with powerful software utilities to assist with SoC bring-up and hardware validation.

A low-level RDDI connection to DSTREAM can be used to gain access to the JTAG scan-chains inside the target device. This enables the unit to be used for simple tasks such as production testing.

## **Debug and Trace**

- 4GB trace buffer for extended trace capture
- Parallel Trace up to 9.6 Gbps
- Serial Trace up to 20 Gbps with HSST- Probe
- Download speed up to 2.5MB per second
- JTAG clock up to 60 MHZ
- Flexible architecture to support 3<sup>rd</sup> party



Trace visualization displays instructions executed, or a representation of memory accesses, branch instructions and other

## **Related Products**

- ARM<sup>®</sup> DS-5 Development Studio
- ARM<sup>®</sup> DS-5 Development Studio Vendor Edition

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