RoHS COMPLIANT

Vishay High Power Products

Schottky Rectifier, 1.0 A



- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION

The MBRA120TRPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	1.0	A	
V _{RRM}		20	V	
I _{FSM}	t _p = 5 μs sine	310	A	
V _F	1.0 Apk, T _J = 125 °C	0.34	V	
TJ	Range	- 65 to 150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	MBRA120TRPbF	UNITS	
Maximum DC reverse voltage	V _R	20	N/	
Maximum working peak reverse voltage	V _{RWM}	20	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	$V_{\rm V}$ 50 % duty cycle at T _L = 136 °C, rectangular waveform		1.0	
Maximum peak one cycle non-repetitive surge current		I _{FSM} 5 μs sine or 3 μs rect. pulse Following any rated load condition and with rated 10 ms sine or 6 ms rect. pulse V _{RRM} applied	310	А	
	IFSM		40		
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 1 \text{ A}, L = 4 \text{ mH}$		2.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	А





Anode

1.0 A

20 V

20 mA at 125 °C

SMA

PRODUCT SUMMARY

 $I_{F(AV)}$

 V_R

 I_{RM}

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MBRA120TRPbF

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	. TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum forward voltage drop		1 A	T.I = 25 °C	0.42	0.45	
		2 A	- 1j=25 C	0.46 0.52 0.33 0.37		
	V _{EM} ⁽¹⁾	1 A	T _{.1} = 100 °C		0.37	v
	V FM \	2 A	1j = 100 C	0.39	0.45	
		1 A	T,I = 125 °C	0.30 0.36	0.35	
		2 A	1j=125 C		0.43	
		T _J = 25 °C		0.015	0.2	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 100 °C	V _R = Rated V _R	2.0 7.0	6.0	mA
		T _J = 125 °C			20	
Typical junction capacitance	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		110	-	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	-	nH
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T_{J} ⁽¹⁾ , T_{Stg}		- 65 to 150	°C	
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation	35	°C/W	
Maximum thermal resistance, junction to ambient	R _{thJA}		80		
Approximate weight			0.07	g	
			0.002	oz.	
Device marking		Case style SMA (similar D-64)	V12A		

Notes

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB



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Fig. 1 - Maximum Forward Voltage Drop Characteristics













I_{F(AV)} - Average Forward Current (A)

Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current



Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D);

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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95018				
Part marking information	www.vishay.com/doc?95029			
Packaging information	www.vishay.com/doc?95034			



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