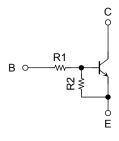
TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT process) (Bias Resistor Built-in Transistor)

RN1901FE, RN1902FE, RN1903FE RN1904FE, RN1905FE, RN1906FE

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Two devices are incorporated into an Extreme-Super-Mini (6-pin) package.
- Incorporating a bias resistor into a transistor reduces parts count.
 Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.
- Complementary to RN2901FE to RN2906FE

Equivalent Circuit and Bias Resistor Values



Type No.	R1 (kΩ)	R2 (kΩ)
RN1901FE	4.7	4.7
RN1902FE	10	10
RN1903FE	22	22
RN1904FE	47	47
RN1905FE	2.2	47
RN1906FE	4.7	47

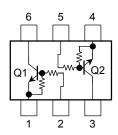
Unit: mm 1.6±0.05 1.2±0.05 9 1.6±0.05 1. EMITTER1 2. BASE1 (B1) 3. COLLECTOR2 (C2) 4. EMITTER2 (E2) 5. BASE2 (B2) 6. COLLECTOR1 (C1) ES6 **JEDEC** JEITA **TOSHIBA** 2-2N1G

Weight: 3 mg (typ.)

Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage	RN1901FE to	V_{CBO}	50	V	
Collector-emitter voltage	RN1906FE	V _{CEO}	50	V	
Emitter-base voltage	RN1901FE to RN1904FE	Veno	10	· V	
	RN1905FE RN1906FE	V _{EBO}	5		
Collector current		IC	100	mA	
Collector power dissipation	RN1901FE to	P _C (Note1)	100	mW	
Junction temperature	RN1906FE	Tj	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

Equivalent Circuit (top view)



Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: Total rating

Start of commercial production 2000-05

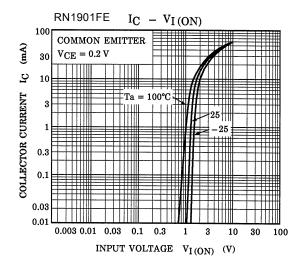


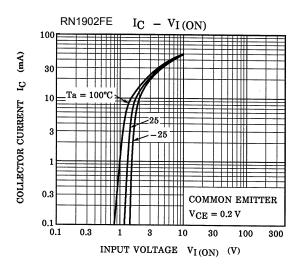
Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

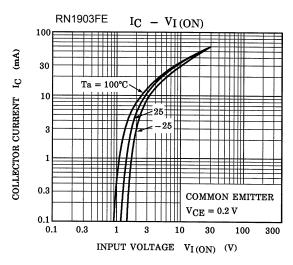
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	RN1901FE to RN1906FE	I _{CBO}	V _{CB} = 50 V, I _E = 0	_	_	100	nA
	KIN 190 II E (O KIN 1900I E	I _{CEO}	V _{CE} = 50 V, I _B = 0	_	_	500	IIA
Emitter cut-off current	RN1901FE	- I _{EBO}	V _{EB} = 10 V, I _C = 0	0.82	_	1.52	mA
	RN1902FE			0.38	_	0.71	
	RN1903FE			0.17	_	0.33	
	RN1904FE			0.082	_	0.15	
	RN1905FE		V _{EB} = 5 V, I _C = 0	0.078	_	0.145	
	RN1906FE			0.074	_	0.138	
DC current gain	RN1901FE		V _{CE} = 5 V, I _C = 10 mA	30	_	_	
	RN1902FE			50	_	_	
	RN1903FE			70	_	_	
	RN1904FE	h _{FE}		80	_	_	
	RN1905FE			80	_	_	
	RN1906FE			80	_	_	
Collector-emitter saturation voltage	RN1901FE to RN1906FE	V _{CE} (sat)	I _C = 5 mA, I _B = 0.25 mA		0.1	0.3	٧
	RN1901FE		$V_{CE} = 0.2 \text{ V}, I_{C} = 5 \text{ mA}$	1.1	_	2.0	. V
	RN1902FE			1.2	_	2.4	
	RN1903FE			1.3	_	3.0	
Input voltage (ON)	RN1904FE	V _{I (ON)}		1.5	_	5.0	
	RN1905FE			0.6	_	1.1	
	RN1906FE			0.7	_	1.3	
Input voltage (OFF)	RN1901FE to RN1904FE	.,	V _{CE} = 5 V, I _C = 0.1 mA	1.0	_	1.5	V
	RN1905FE, RN1906FE	V _I (OFF)		0.5	_	0.8	
Transition frequency	RN1901FE to RN1906FE	f _T	$V_{CE} = 10 \text{ V}, I_{C} = 5 \text{ mA}$		250	_	MHz
Collector output capacitance	RN1901FE to RN1906FE	C _{ob}	V _{CB} = 10 V, I _E = 0, f = 1 MHz	_	3	6	pF
Input resistor	RN1901FE		_	3.29	4.7	6.11	kΩ
	RN1902FE			7	10	13	
	RN1903FE	D4		15.4	22	28.6	
	RN1904FE	R1		32.9	47	61.1	
	RN1905FE			1.54	2.2	2.86	
	RN1906FE			3.29	4.7	6.11	
Resistor ratio	RN1901FE to RN1904FE		_	0.9	1.0	1.1	
	RN1905FE	R1/R2		0.0421	0.0468	0.0515	
	RN1906FE			0.09	0.1	0.11	

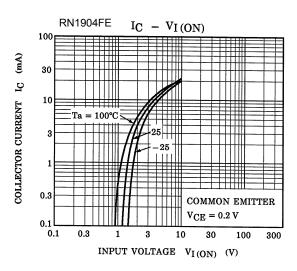
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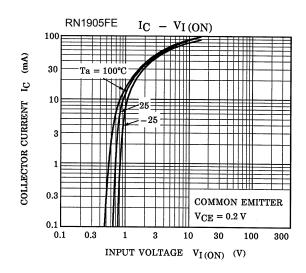
Q1, Q2 Common

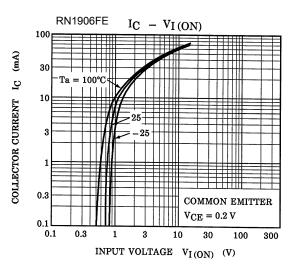






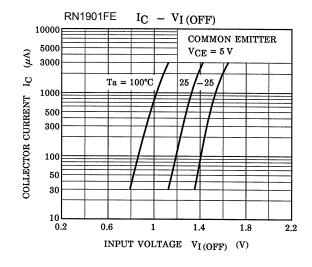


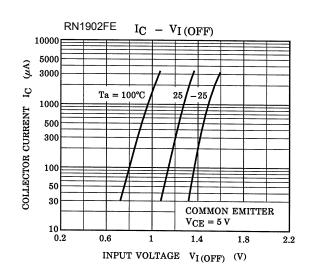


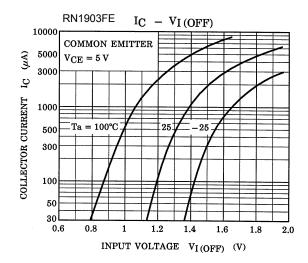


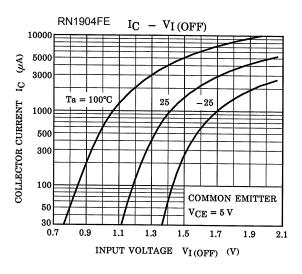
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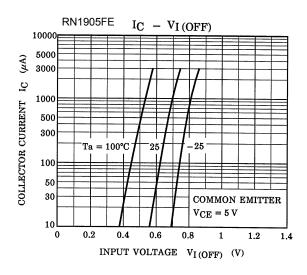
Q1, Q2 Common

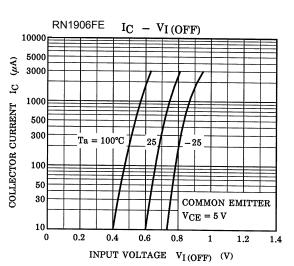


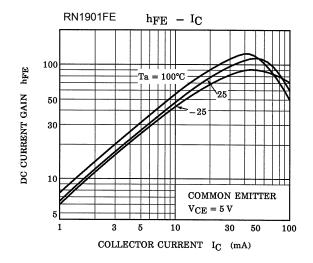


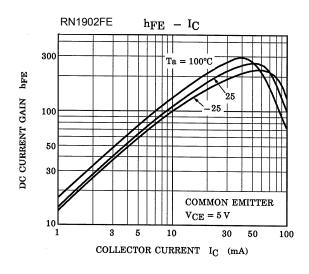


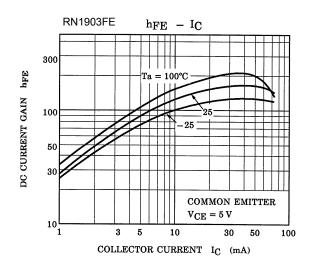


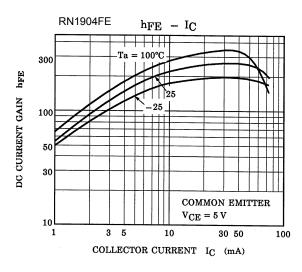


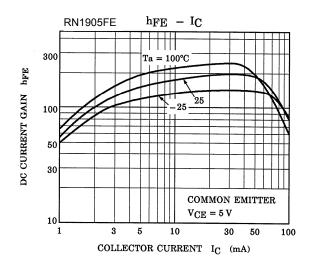


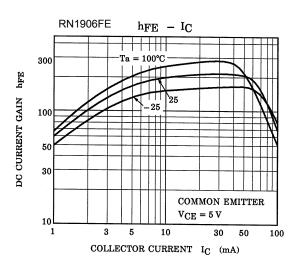


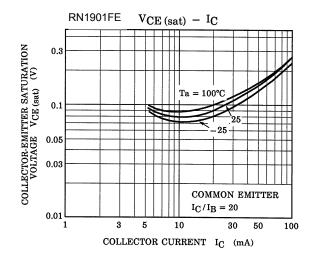


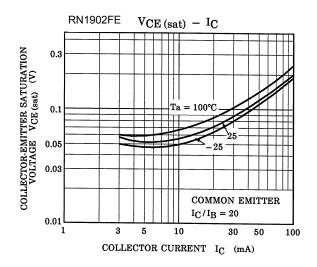


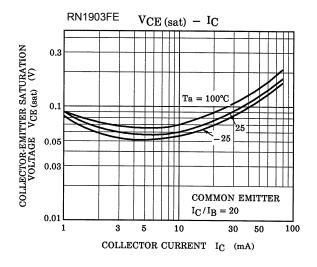


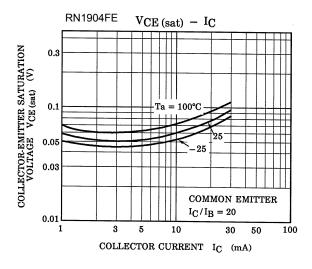


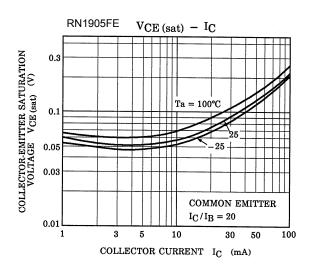


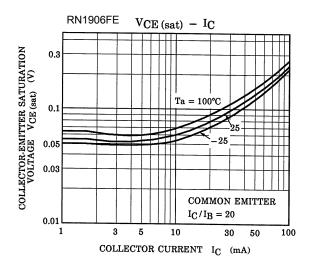




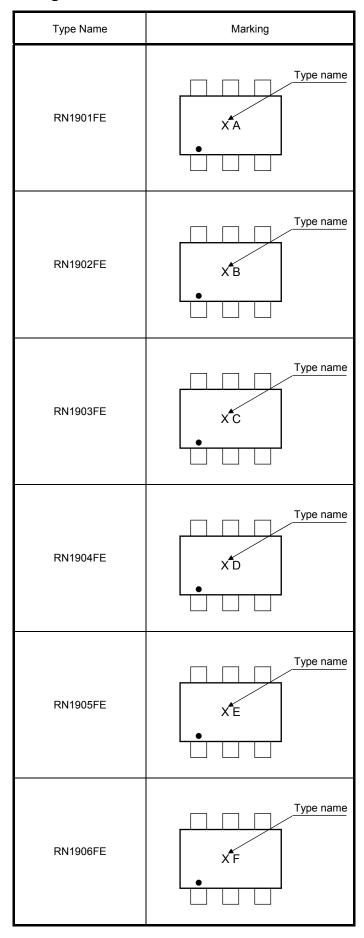








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