



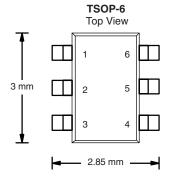
N-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
60	0.10 at V _{GS} = 10 V	3.2		
	0.13 at V _{GS} = 4.5 V	2.8		

FEATURES

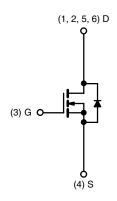
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si3458DV-T1-E3 (Lead (Pb)-free)

Si3458DV-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	60	V	
Gate-Source Voltage		V_{GS}	± 20	7 v	
Continuous Dunin Courset /T 150 00\alpha,b	T _A = 25 °C	I _D	3.2		
Continuous Drain Current (T _J = 150 °C) ^{a, b}	T _A = 70 °C		2.5		
Pulsed Drain Current		I _{DM}	15	A	
Silngle Avalanche Current		I _{AS}	10		
Mariana Barra Biraira dan	T _A = 25 °C	В	2	w	
Maximum Power Dissipation ^{a,b}	T _A = 70 °C	P _D	1.3	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marinum Lucation to Ambienta	t ≤ 5 s	- R _{thJA}		62.5	
Maximum Junction-to-Ambient ^a	Steady State		106		°C/W
Maximum Junction-to-Lead	Steady State	R _{thJL}	35		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b. $t \le 5$ s.

Si3458DV

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	60			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1				
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zara Cata Valtaga Drain Current	I _{DSS}	V _{DS} = 48 V, V _{GS} = 0 V			1		
Zero Gate Voltage Drain Current		V _{DS} = 48 V, V _{GS} = 0 V, T _J = 150 °C			50	- μΑ	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	10			Α	
Drain-Source On-State Resistance ^a	В	$V_{GS} = 10 \text{ V}, I_D = 3.2 \text{ A}$		0.085	0.10	Ω	
	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 2.8 \text{ A}$		0.110	0.13		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 4.5 \text{ V}, I_D = 3.2 \text{ A}$		8		S	
Dynamic ^b							
Total Gate Charge	Qg			8	16	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3.2 \text{ A}$		4.0			
Gate-Drain Charge	Q_{gd}			2.0			
Gate Resistance	R_g		1		3.9	Ω	
Turn-On Delay Time	t _{d(on)}			10	20		
Rise Time	t _r	V_{DD} = 30 V, R_L = 30 Ω		10	20	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ 1 A, V_{GEN} = 10 V, R_g = 6 Ω		20	40		
Fall Time	t _f			10	20		
Source-Drain Rating Characteristics ^b				•			
Continuous Current	Is				1.7	^	
Pulsed Current	I _{SM}				15	A	
Diode Forward Voltage ^a	V _{SD}	I _S = 1.7 A, V _{GS} = 0 V			1.2	V	
Source-Drain Reverse Recovery Time	t _{rr}	$I_F = 1.7 \text{ A}, dI/dt = 100 \text{ A/}\mu\text{s}$		50	90	ns	

Notes:

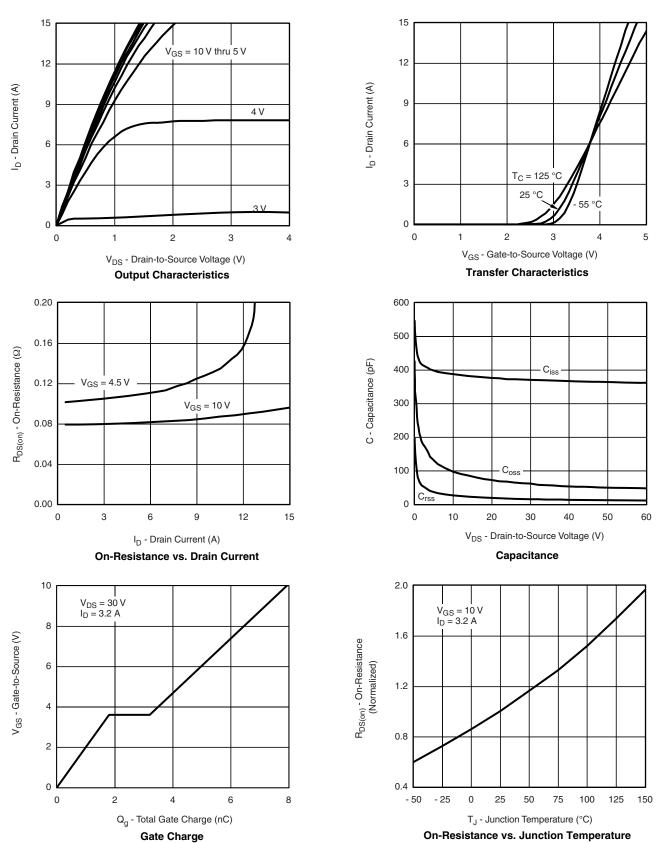
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





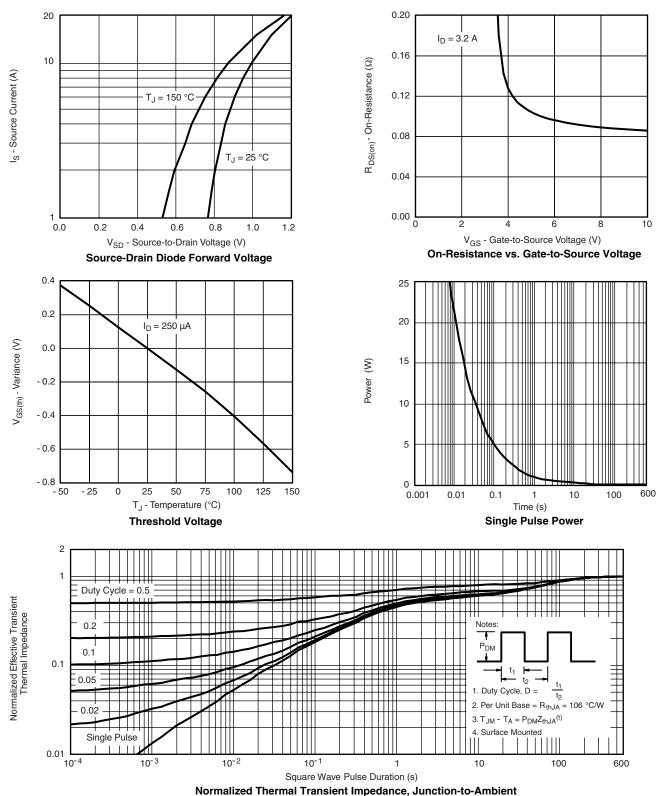
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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