**Product data sheet** 

## 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

## 2. Features and benefits

- Low threshold voltage
- Very fast switching
- Trench MOSFET technology

## 3. Applications

- Relay driver
- High-speed line driver
- Low-side load switch
- Switching circuits

## 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	20	٧
V <sub>GS</sub>	gate-source voltage			-8	-	8	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	-	2.5	Α
Static characte	Static characteristics						
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS} = 4.5 \text{ V}; I_D = 2.5 \text{ A}; T_j = 25 \text{ °C}$		-	41	50	mΩ

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.



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# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D
2	S	source		
3	D	drain		G (F)
			1 2	mbb076 S
			SOT23	

## 6. Ordering information

**Table 3. Ordering information** 

Type number	Package					
	Name	Description	Version			
NXV40UN	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

## 7. Marking

### Table 4. Marking codes

Type number	Marking code[1]
NXV40UN	%5J

<sup>[1] % =</sup> placeholder for manufacturing site code

# 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	20	V
$V_{GS}$	gate-source voltage			-8	8	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	2.5	А
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 100 °C	[1]	-	1.6	А
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \mu s$		-	10	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	340	mW
			[1]	-	480	mW
		T <sub>sp</sub> = 25 °C		-	2.1	W
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-drain	n diode					
Is	source current	T <sub>amb</sub> = 25 °C	[1]	-	0.4	А

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.

NXV40UN

<sup>[2]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

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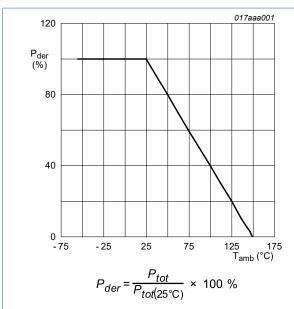


Fig. 1. Normalized total power dissipation as a function of ambient temperature

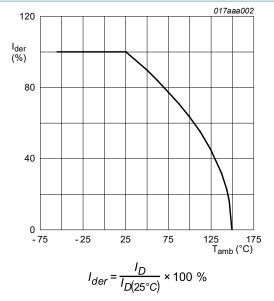


Fig. 2. Normalized continuous drain current as a function of ambient temperature

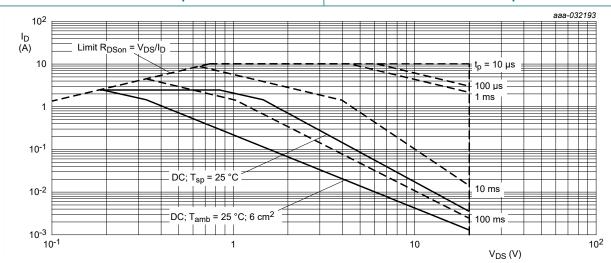


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

### 20 V, N-channel Trench MOSFET

## 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from	in free air	[1]	-	325	370	K/W
junction to ambient		[2]	-	230	260	K/W	
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	50	60	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.

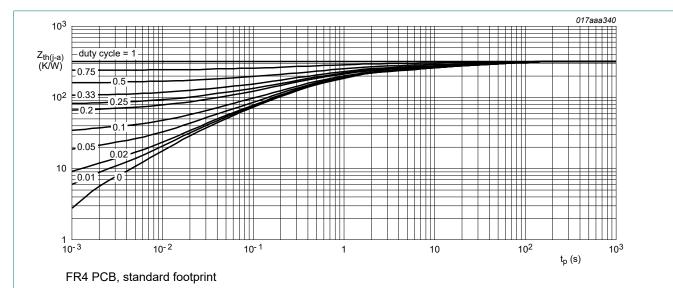


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

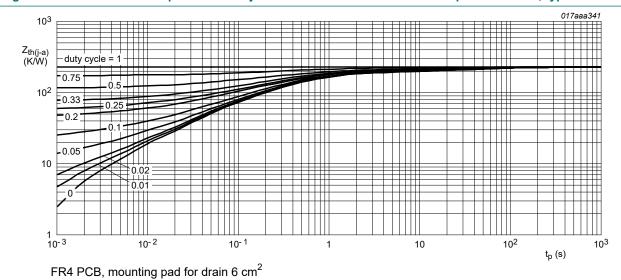


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

## 20 V, N-channel Trench MOSFET

# 10. Characteristics

### **Table 7. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	20	-	-	V
$V_{GSth}$	gate-source threshold voltage	$I_D = 250 \mu A; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$	0.45	0.7	0.95	V
I <sub>DSS</sub>	drain leakage current	$V_{DS} = 20 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	1	μΑ
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 8 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	100	nA
		$V_{GS} = -8 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	-100	nA
R <sub>DSon</sub>	drain-source on-state	$V_{GS} = 4.5 \text{ V}; I_D = 2.5 \text{ A}; T_j = 25 \text{ °C}$	-	41	50	mΩ
	resistance	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 2.5 A; T <sub>j</sub> = 150 °C	-	61	74	mΩ
		$V_{GS} = 2.5 \text{ V}; I_D = 2.1 \text{ A}; T_j = 25 ^{\circ}\text{C}$	-	48	72	mΩ
		V <sub>GS</sub> = 1.8 V; I <sub>D</sub> = 1.9 A; T <sub>j</sub> = 25 °C	-	57	90	mΩ
		$V_{GS} = 1.5 \text{ V}; I_D = 1.8 \text{ A}; T_j = 25 ^{\circ}\text{C}$	-	69	100	mΩ
9fs	forward transconductance	$V_{DS} = 5 \text{ V}; I_D = 2.5 \text{ A}; T_j = 25 \text{ °C}$	-	10	-	S
$R_{G}$	gate resistance	f = 1 MHz	-	1.6	-	Ω
Dynamic ch	aracteristics		'	'		
Q <sub>G(tot)</sub>	total gate charge	$V_{DS} = 10 \text{ V}; I_D = 2.5 \text{ A}; V_{GS} = 4.5 \text{ V};$	-	6	9	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C	-	0.4	-	nC
$Q_{GD}$	gate-drain charge	]	-	1.6	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 10 V; f = 1 MHz; V <sub>GS</sub> = 0 V;	-	347	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	60	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	54	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 10 V; $I_{D}$ = 2.5 A; $V_{GS}$ = 4.5 V;	-	5	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	17	-	ns
t <sub>d(off)</sub>	turn-off delay time	]	-	30	-	ns
t <sub>f</sub>	fall time	]	-	9	-	ns
Source-drai	in diode		'			
V <sub>SD</sub>	source-drain voltage	$I_S = 0.4 \text{ A}; V_{GS} = 0 \text{ V}; T_i = 25 ^{\circ}\text{C}$	-	0.7	1.2	V

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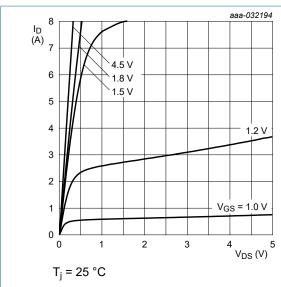


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

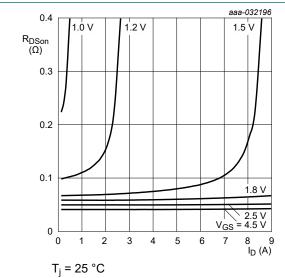


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

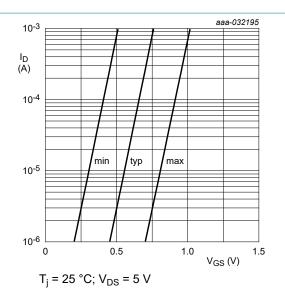


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

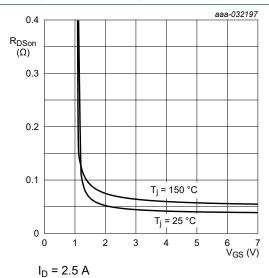


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

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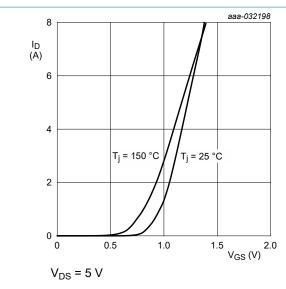


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

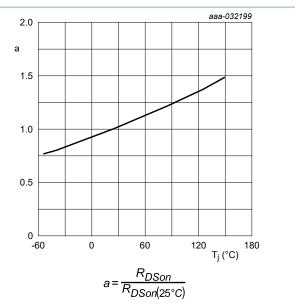


Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

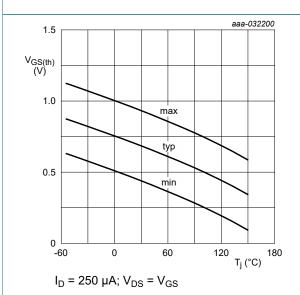
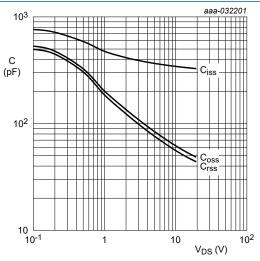


Fig. 12. Gate-source threshold voltage as a function of junction temperature



 $f = 1 MHz; V_{GS} = 0 V$ 

Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

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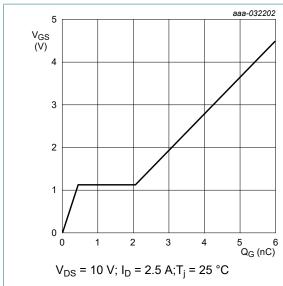


Fig. 14. Gate-source voltage as a function of gate charge; typical values

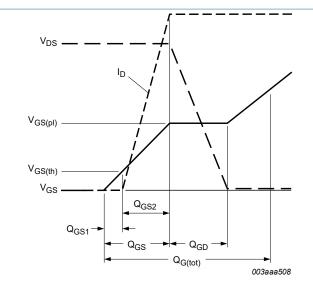


Fig. 15. Gate charge waveform definitions

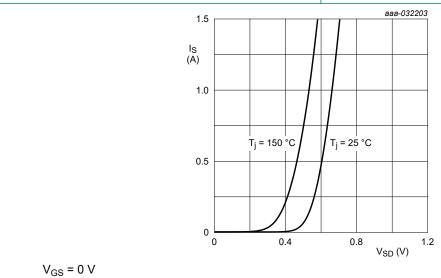
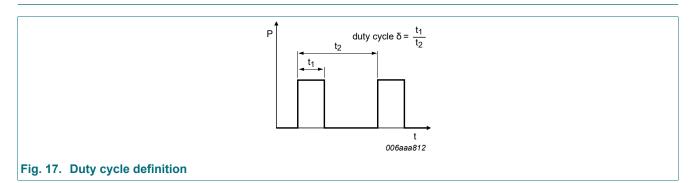


Fig. 16. Source current as a function of source-drain voltage; typical values

## 11. Test information



### 20 V, N-channel Trench MOSFET

# 12. Package outline

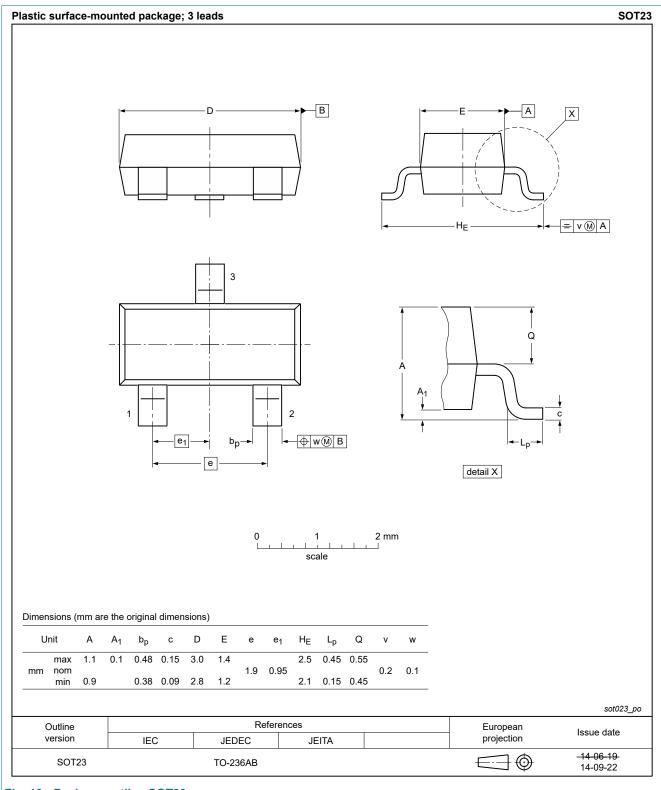
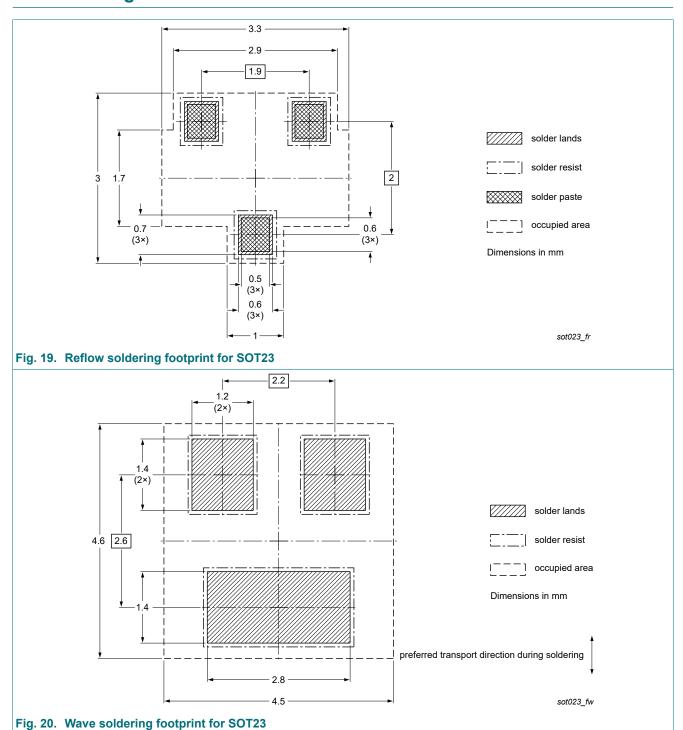


Fig. 18. Package outline SOT23

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# 13. Soldering



# 14. Revision history

**Table 8. Revision history** 

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
NXV40UN v.1	20201019	Product	-	-

NXV40UN

### 20 V, N-channel Trench MOSFET

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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