

CMOS LDO Regulator Series for Portable Equipments Versatile Package FULL CMOS LDO Regulator

BUxxUC3WG series

General Description

BUxxUC3WG series is high-performance FULL CMOS regulator with 300-mA output, which is mounted on versatile package SSOP5 (2.9 mm \times 2.8 mm \times 1.25 mm). It has excellent noise characteristics and load responsiveness characteristics despite its low circuit current consumption of 50µA. It is most appropriate for various applications such as power supplies for logic IC, RF, and camera modules ROHM's.

Features

- High accuracy detection
- Iow current consumption
- Compatible with small ceramic capacitor (Cin=Co=1.0uF)
- With built-in output discharge circuit
- High ripple rejection
- ON/OFF control of output voltage
- With built-in over current protection circuit and thermal shutdown circuit
- Package SSOP5 is similar to SOT-23-5 (JEDEC)
- Low dropout voltage

Typical Application Circuit



Figure 1. Application Circuit

Key Specifications

Output voltage:	1.0V to 3.3V
Accuracy output voltage:	±1.0% (±25mV)
Low current consumption:	50µA

■ Operating temperature range: -40°C to +85°C

Applications

Battery-powered portable equipment, etc.

Package

SSOP5:



2.90mm x 2.80mm x 1.25mm

Connection Diagram SSOP5



Pin Descriptions

SSOP5					
PIN No.	Symbol	Function			
1	VIN	Power Supply Voltage			
2	GND	Grounding			
3	STBY	ON/OFF control of output voltage			
3	3101	(High: ON, Low: OFF)			
4	N.C.	Unconnected Terminal			
5	VOUT	Output Voltage			







●Lineup

Marking	Q0	Q2	Q8	R6	Y0	Y6	Y7
Output Voltage	1.0V	1.1V	1.8V	2.5V	2.8V	3.2V	3.3V
Part Number	BU10	BU11	BU18	BU25	BU28	BU32	BU33

●Absolute Maximum Ratings (Ta=25°C)

PARAMETER	Symbol	Limit	Unit
Power Supply Voltage	VMAX	-0.3 ~ +6.0	V
Power Dissipation	Pd	540(*1)	mW
Maximum junction temperature	TjMAX	+125	S
Operating Temperature Range	Topr	-40 ~ +85	°C
Storage Temperature Range	Tstg	-55 ~ +125	C°

(*1)Pd deleted at 5.4mW/°C at temperatures above Ta=25°C, mounted on 70×70×1.6 mm glass-epoxy PCB.

RECOMMENDED OPERATING RANGE (not to exceed Pd)

PARAMETER	Symbol	Limit	Unit
Power Supply Voltage	VIN	1.7~5.5	V
Maximum Output Current	IMAX	300	mA

OPERATING CONDITIONS

PARAMETER	Symbol	MIN.	TYP.	MAX.	Unit	CONDITION
Input Capacitor	Cin	0.47(*2)	1.0	-	μF	Ceramic capacitor
Output Capacitor	Со	0.47(*2)	1.0	-	μF	recommended

(*2)Make sure that the output capacitor value is not kept lower than this specified level across a variety of temperature and DC bias characteristic.

• Electrical Characteristics

(Ta=25°C, VIN=VOUT+1.0V (*3), STBY=VIN, Cin=1.0µF, Co=1.0µF, unless otherwise noted.)

PARAMETER		Cumphel	Limit		Unit	Conditions	
		Symbol	MIN.	TYP.	MAX.	Unit	Conditions
Overall Device					1 1		
		VOUT	VOUT×0.99	VOUT	VOUT×1.01	V	IOUT=10 µ A,VOUT≧2.5V
Output Voltage		0001	VOUT-25mV	001	VOUT+25mV	V	IOUT=10 μ A,VOUT<2.5V
Operating Current		lin	-	50	90	μA	IOUT=0mA
Operating Current (STI	BY)	ISTBY	-	-	1.0	μA	STBY=0V
Ripple Rejection Ratio		RR	45	70	-	dB	VRR=-20dBv,fRR=1kHz,IOUT=10mA, VIN=3.6V
			-	470	700	mV	1.0V≦VOUT<1.2V(IOUT=300mA)
		VSAT	-	350	500	mV	1.2V≦VOUT<1.5V(IOUT=300mA)
DranautValtara			-	280	380	mV	1.5V≦VOUT<1.7V(IOUT=300mA)
Dropout Voltage			-	250	320	mV	1.7V≦VOUT<2.1V(IOUT=300mA)
			-	220	260	mV	2.1V≦VOUT<2.5V(IOUT=300mA)
			-	200	220	mV	2.5V≦VOUT(IOUT=300mA)
Line Regulation		VDL	-	2	20	mV	VIN=VOUT+1.0V to 5.5V(*4), IOUT=10µA
Load Regulation		VDLO	-	25	45	mV	IOUT=0.01mA to 300mA
Over-current Protection	n (OCP	?)					
Limit Current		ILMAX	370	550	-	mA	Vo=VOUT*0.95
Short Current		ISHORT	50	150	300	mA	Vo=0V
Standby Block							
Discharge Resistor		RDSC	20	50	80	Ω	VIN=5.5V, STBY=0V, VOUT=2.6V
STBY Pin Pull-down C	urrent	ISTB	0.1	0.9	8.0	μA	STBY=1.5V
STBY Control Voltage	ON	VSTBH	1.2	-	5.5	V	
	OFF	VSTBL	-0.3	-	0.3	V	

OThis product is not designed for protection against radioactive rays. (*3) VIN=2.5V for VOUT≦1.5V

(*4) VIN=2.5V to 3.6V for VOUT≦1.5V

Block Diagrams



Figure 2. Block Diagrams



Figure 3.



Figure 4.







Figure 7.



Figure 6.



Figure 8.







Figure 11.



POWER-SUPPLY RIPPLE REJECTION vs FREQUENCY

100

Figure 12.



LOAD TRANSIENT RESPONSE

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Figure 21.



Figure 22.



Figure 23.



Figure 24.







Figure 27.



Figure 26.



Figure28.











Figure 32.



















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Figure 41.



Figure 42.



Figure 43.





Figure 46.



Figure 48.



Figure 45.



Figure 47.











Figure 52.















Figure 57.



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Figure 63.



Figure 64.



Figure 65.



Figure 67.



Figure 66.



Figure 68.











POWER-SUPPLY RIPPLE REJECTION vs FREQUENCY

Figure 72.











Figure 77.











About power dissipation (Pd)

As for power dissipation, an approximate estimate of the heat reduction characteristics and internal power consumption of IC are shown, so please use these for reference. Since power dissipation changes substantially depending on the implementation conditions (board size, board thickness, metal wiring rate, number of layers and through holes, etc.), it is recommended to measure Pd on a set board. Exceeding the power dissipation of IC may lead to deterioration of the original IC performance, such as causing operation of the thermal shutdown circuit or reduction in current capability. Therefore, be sure to prepare sufficient margin within power dissipation for usage.

Calculation of the maximum internal power consumption of IC (PMAX)

PMAX=(VIN-VOUT)×IOUT(MAX.) (VIN : Input voltage VOUT : Output voltage IOUT(MAX) : Maximum output current)

O Measurement conditions





Figure 83. SSOP5 Power dissipation heat reduction characteristics (Reference)

Operation Notes

1.) Absolute maximum ratings

Use of the IC in excess of absolute maximum ratings (such as the input voltage or operating temperature range) may result in damage to the IC. Assumptions should not be made regarding the state of the IC (e.g., short mode or open mode) when such damage is suffered. If operational values are expected to exceed the maximum ratings for the device, consider adding protective circuitry (such as fuses) to eliminate the risk of damaging the IC.

2.) GND potential

The potential of the GND pin must be the minimum potential in the system in all operating conditions.

Never connect a potential lower than GND to any pin, even if only transiently.

3.) Thermal design

Use a thermal design that allows for a sufficient margin for that package power dissipation rating (Pd) under actual operating conditions.

4.) Inter-pin shorts and mounting errors

Use caution when orienting and positioning the IC for mounting on printed circuit boards. Improper mounting or shorts between pins may result in damage to the IC.

5.) Common impedance

Wiring traces should be as short and wide as possible to minimize common impedance. Bypass capacitors should be use to keep ripple to a minimum.

6.) Voltage of STBY pin

To enable standby mode for all channels, set the STBY pin to 0.3 V or less, and for normal operation, to 1.2 V or more. Setting STBY to a voltage between 0.3 and 1.2 V may cause malfunction and should be avoided. Keep transition time between high and low (or vice versa) to a minimum.

Additionally, if STBY is shorted to VIN, the IC will switch to standby mode and disable the output discharge circuit, causing a temporary voltage to remain on the output pin. If the IC is switched on again while this voltage is present, overshoot may occur on the output. Therefore, in applications where these pins are shorted, the output should always be completely discharged before turning the IC on.

7.) Over-current protection circuit (OCP)

This IC features an integrated over-current and short-protection circuitry on the output to prevent destruction of the IC when the output is shorted. The OCP circuitry is designed only to protect the IC from irregular conditions (such as motor output shorts) and is not designed to be used as an active security device for the application. Therefore, applications should not be designed under the assumption that this circuitry will engage.

8.) Thermal shutdown circuit (TSD)

This IC also features a thermal shutdown circuit that is designed to turn the output off when the junction temperature of the IC exceeds about 150°C. This feature is intended to protect the IC only in the event of thermal overload and is not designed to guarantee operation or act as an active security device for the application. Therefore, applications should not be designed under the assumption that this circuitry will engage.

9.) Input/output capacitor

Capacitors must be connected between the input/output pins and GND for stable operation, and should be physically mounted as close to the IC pins as possible. The input capacitor helps to counteract increases in power supply impedance, and increases stability in applications with long or winding power supply traces. The output capacitance value is directly related to the overall stability and transient response of the regulator, and should be set to the largest possible value for the application to increase these characteristics. During design, keep in mind that in general, ceramic capacitors have a wide range of tolerances, temperature coefficients and DC bias characteristics, and that their capacitance values tend to decrease over time. Confirm these details before choosing appropriate capacitors for your application.(Please refer the technical note, regarding ceramic capacitor of recommendation)

10.) About the equivalent series resistance (ESR) of a ceramic capacitor

Capacitors generally have ESR (equivalent series resistance) and it operates stably in the ESR-IOUT area shown on the right. Since ceramic capacitors, tantalum capacitors, electrolytic capacitors, etc. generally have different ESR, please check the ESR of the capacitor to be used and use it within the stability area range shown in the right graph for evaluation of the actual application.



Cout=1.0uF Cin=1.0uF Temp=25°C

Figure 84. Stable region (example)

Revision History

Date	Revision	Changes			
27.Jun.2013	001	New Release			
02.Jul.2013	002	Absolute Maximum Ratings of Power Supply Voltage is changed. Adding reference data.			
16.Apr.2014	003	Adding reference data.			
23.Jan.2015	004	Line up is changed.			
29.Jan.2015	005	Reference data is changed.			

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