

# RVT101HVLNWN00

# IPS LVDS 10.1" LCD TFT DATASHEET

Rev.1.3 2023-01-19

ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally Black/IPS	/
Size	10.1	Inch
Viewing Direction	Free	/
Outside Dimensions (W x H x D)	229.46 x 149.10 x 7.50	mm
Active Area (W x H)	216.96 x 135.60	mm
Pixel Pitch (W x H)	0.1695 x 0.1695	mm
Resolution	1280 x 800 (RGB)	/
Brightness	1000	cd/m²
Color Depth	16.7 M	/
Pixel Arrangement	RGB Vertical Stripe	/
LCD Driver	EK79202B	/
Interface	LVDS	/
With/Without Touch	Without Touch Panel	/
Weight	260	g

Note 1. RoHS3 compliant

Note 2. LCM weight tolerance: ± 5%.



## **1.** REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2020-10-22	Initial Release	
1.1	2021-05-26	Modify Electrical Specification and power on/off sequence	
1.2	2021-07-28	Updating new template Correcting the Operating/standby current From: Operating Current kore-30' - 15 20 mA Standby Current kore-30' - 280 420 mA Standby Current kore-30' - 15 2.0 mA	
1.3	2023-01-19	Modify DE signal: Active High	



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# **3.** MODULE CLASSIFICATION INFORMATION

RV									
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.

NO.	PARAMETER	SYMBOL
1.	BRAND	RV – Riverdi
2.	PRODUCT TYPE	T – TFT Standard
3.	DISPLAY SIZE	101 – 10.1"
4.	MODEL SERIAL NO.	H – High Brightness, IPS
5.	RESOLUTION	V – 1280 x 800 px
6.	INTERFACE	L – TFT LCD, LVDS
7.	FRAME	N – Without Mounting Metal Frame
8.	BACKLIGHT TYPE	W – LED White
9.	TOUCH PANEL	N – Without Touch Panel
10.	VERSION	00 – (00-99)





# **5.** ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT
Supply Voltage for Module	VDD	-0.3	3.9	V
Operating Temperature	Тор	-20	70	٥٢
Storage Temperature	T <sub>ST</sub>	-30	80	

**Note 1.** The absolute maximum rating values must not be exceeded at any times. The module MUST NOT be used when any of the absolute maximum ratings is exceeded.

The characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

# 6. ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Supply Voltage	V <sub>DD</sub>	2.6	3.3	3.6	V
Operating Current	I <sub>VDD=3.3V</sub>	-	280	420	mA
Standby Current	I <sub>ST</sub>	-	1.5	2.0	mA

### 7. BACKLIGHT ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Backlight Driving Voltage	VF	15.0	16.0	17.0	V	
Backlight Driving Current	I <sub>F</sub>	315	360	405	mA	
Backlight Power Consumption	W <sub>BL</sub>	-	5760	-	mW	
LED Lifetime	-	-	50,000	-	hours	Note 1

**Note 1.** Each LED: I<sub>F</sub> =40 mA, V<sub>F</sub>=3.2 ±0.2V.

Note 2. Optical performance should be evaluated at  $T_a$ =25 °C only.

**Note 3.** Operating life means the period in which the LED brightness goes down to 50% of the initial brightness. Typical operating lifetime is the estimated parameter.





# 8. ELECTRO-OPTICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	RMK	NOTE
Response Time	Tr+Tf		-	25	35	ms	FIG 1.	4
Contrast Ratio	Cr	θ=O°	800	1000	-			1
Luminance Uniformity	δ WHITE	ø=0° Ta=25 °C	-	75	-	%	FIG 2.	3
Surface Luminance	Lv	1d-25 C	-	1000	-	cd/m²		2
		ø = 90°	75	85	-	deg		
Viewing Angle	θ	ø = 270°	75	85	-	deg	FIG 3.	6
Range	Ð	ø = 0°	75	85	-	deg	FIU J.	0
		ø = 180°	75	85	-	deg		
	Rx		0.22	0.26	0.30	-		
	Ry		0.20	0.24	0.28	-		
	Gx	0-00	0.34	0.38	0.42	-		
CIE (x, y)	Gy	θ=O° ø=O°	0.50	0.54	0.58	-	FIG 2.	5
Chromaticity	Bx	<sup>©</sup> =0 Ta=25 °C	0.10	0.14	0.18	-	FIU Z.	5
	Ву	1d-25 C	0.09	0.13	0.17	-		
	Wx		0.28	0.32	0.36	-		
	Wy		0.29	0.33	0.37	-	-	

**Note 1.** Contrast Ratio (CR) is defined mathematically as below, for more information see Figure 2.

Contrast Ratio =  $\frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$ 

**Note 2.** Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Note 3. The uniformity in surface luminance  $\delta$  WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the minimum luminance of 5 points luminance by maximum luminance of 5 points luminance. For more information see Figure 2.

 $\delta \text{ WHITE } = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$ 

**Note 4.** Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see Figure 1. The test equipment is BM-7A.

**Note 5.** CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then calculating the average value.

**Note 6**. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to LCD surface. For more information see Figure 3.



**Note 7.** Viewing angle is measured at the center point of the LCD by CONOSCOPE (ergo-80). For response time testing, the testing data is based on BM-7A. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, Chromaticity the test data is based on SR-3A.

Figure 1. The definition of response time



*Figure 2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity* 



Figure 3. The definition of viewing angle





# 9. INTERFACES DESCRIPTION

## 9.1 TFT assignment

Matched Riverdi ZIF connector: ZIF0540DH-CF25

1NC-No Connection2V <sub>DD</sub> PPower Supply, 3.3V3V <sub>DD</sub> PPower Supply, 3.3V4-6NC-No Connection7GNDPGround8Rxin0-I-LVDS Differential Data Input9Rxin0+I+LVDS Differential Data Input10GNDPGround11Rxin1+I-LVDS Differential Data Input12Rxin1+I-LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround23NC-No Connection	
3VDDPPower Supply, 3.3V4-6NC-No Connection7GNDPGround8Rxin0-I-LVDS Differential Data Input9Rxin0+I+LVDS Differential Data Input10GNDPGround11Rxin1-I-LVDS Differential Data Input12Rxin1+I+LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround	
4-6NC-No Connection7GNDPGround8Rxin0-I-LVDS Differential Data Input9Rxin0+I+LVDS Differential Data Input10GNDPGround11Rxin1-I-LVDS Differential Data Input12Rxin1+I+LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround	
7GNDPGround8Rxin0-I-LVDS Differential Data Input9Rxin0+I+LVDS Differential Data Input10GNDPGround11Rxin1-I-LVDS Differential Data Input12Rxin1+I+LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround	
8Rxin0-I-LVDS Differential Data Input9Rxin0+I+LVDS Differential Data Input10GNDPGround11Rxin1-I-LVDS Differential Data Input12Rxin1+I+LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround	
9Rxin0+I+LVDS Differential Data Input10GNDPGround11Rxin1-I-LVDS Differential Data Input12Rxin1+I+LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround	
10GNDPGround11Rxin1-I-LVDS Differential Data Input12Rxin1+I+LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround	
11Rxin1-I-LVDS Differential Data Input12Rxin1+I+LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround	
12Rxin1+I+LVDS Differential Data Input13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround	
13GNDPGround14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround	
14Rxin2-I-LVDS Differential Data Input15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround	
15Rxin2+I+LVDS Differential Data Input16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround	
16GNDPGround17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround	
17RxCLK-I-LVDS Differential Data Input18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround	
18RxCLK+I+LVDS Differential Data Input19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround	
19GNDPGround20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround	
20Rxin3-I-LVDS Differential Data Input21Rxin3+I+LVDS Differential Data Input22GNDPGround	
21Rxin3+I+LVDS Differential Data Input22GNDPGround	
22 GND P Ground	
23 NC - No Connection	
24 NC - No Connection	
25 GND P Ground	
26-29 NC - No Connection	
30 GND P Ground	
31 LED- P LED Cathode	
32 LED- P LED Cathode	
33 NC - No Connection	
34 NC - No Connection	
35 NC - No Connection	
36 NC - No Connection	
37 NC - No Connection	
38 NC - No Connection	
39 LED+ P LED Anode	
40 LED+ P LED Anode	

Note 1. I: input, P:Power



# **10.** TIMING CHARACTERISTICS

#### 10.1 LVDS interface characteristic

#### VESA Format: 8-bit LVDS input, (LVBIT=H, LVFMT=H)



Note 1: Control signals DE: Active High VS HS: Active Low

## 10.2 Timing table

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Clock Frequency	FDCLK	66.3	72.4	78.9	MHz
(Rate=60Hz (LVDS))					
HSYNC Period Time	T <sub>H</sub>	1380	1440	1500	DCLK
Horizontal Display area	T <sub>HD</sub>		1280		DCLK
Hsync pulse Width	T <sub>HPW</sub>	1	-	40	Тс
Hsync Back Porch	T <sub>HBP</sub>	88	88	88	DCLK
(With pulse width)					
Hsync Front Porch	T <sub>HFP</sub>	12	72	132	DCLK
VSYNC Period Time	Tv	824	838	872	
Vertical Display area	T <sub>VD</sub>		800		Н
Vsync pulse Width	Tvw	1	-	20	
Vsync Back Porch	T <sub>VBP</sub>	23	23	23	]
(With pulse width)					
Vsync Front Porch	T <sub>VFP</sub>	1	15	49	



# 10.3 Power ON/OFF sequence



PARAMETER		UNIT		
	MIN.	TYP.	MAX.	
П	0.5	2	10	
T2	0	5	50	ms
T3	130	136	210	

#### 10.3.2 Power off sequence



PARAMETER	VALUE			UNIT
	MIN.	TYP.	MAX.	
T4	0.5	2	10	
T5	0	7	50	ms
T6	0	2	100	



# 11. INSPECTION

Standard acceptance/rejection criteria for TFT module

## **11.1** Inspection condition

Ambient conditions:

- Temperature: 25 ± 2°C
- Humidity: (60 ± 10) %RH
- Illumination: Single fluorescent lamp non-directive (300 to 700 lux)

Viewing distance: 35 ± 5cm between inspector bare eye and LCD.

Viewing Angle: U/D: 45°/45°, L/R: 45°/45°





# 11.2 Inspection standard

The LCD TFT has zero bad pixels. Please refer the item "Bright/Dark dots".

ITEM		CRITER	ION				
Black spots, white spots, light leakage, Foreign Particle (round Type)		Size = 10.1"					
		Average Diameter		Qualified Qty			
		D ≤ 0.2 mm		Ignored			
	D=(x+y)/2	0.2 mm <	0.2 mm < D ≤ 0.3 mm		N≤4		
	Spots density: 10 mm	pots density: 10 mm 0.5mm <		D		Not allowed	
LCD black spots, white spots, light leakage (line Type)	Width ► -	Size = 10.1"					
	Length	Length		Width		Qualified Qty	
		-		W ≤ 0.05		Ignored	
		L ≤ 5.0		0.05< W ≤ 0.1		N≤3	
		5.0 < L		0.10< W 5.0 < L		Not allowed	
	Spots density: 10 mm	Size = <sup>-</sup>					
	ltem		Qualified Qty				
Bright/Dark	Bright dots		0				
Dots	Dark dots		0				
	Cluster Bright Dots or Dark Dots		0				
	Total Bright and Dark Dots		0				
Clear spots	Size ≥ 5.0"						
	Average Diameter		Qualified Qty				
	D < 0.2 mm		Ignored				
	0.2 mm < D < 0.3 mm		4				
	0.3 mm < D < 0.5 mm			2			
	0.5 mm < D			0			
	Spots density: 10 mm						



## 12. RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION	NOTE	
1	High Temperature Storage	80°C/120 hours		
2	Low Temperature Storage	-30°C/120 hours		
3	High Temperature Operating	70 °C /120 hours	Note 1	
4	Low Temperature Operating -20°C/120 hours			
5	High Temperature and High Humidity	Humidity 40°C, 90%RH, 120Hrs		
6	Thermal Cycling Test (No operation)	-20°C for 30min, 70°C for 30 min. 100 cycles. Then test at room temperature after 1 hour	Note 2	
7	Vibration Test	Frequency: 10 ÷ 55 Hz. Stroke: 1.5 mm. Sweep: 10Hz ÷ 55Hz ÷ 10 Hz. 2 hours for each direction of X, Y, Z (Total 6 hours)		
8	Package Drop Test	Height: 60 cm 1 corner, 3 edges, 6 surfaces		

Note 1. Sample quantity for each test item is 5 ÷ 10 pcs.

**Note 2**. Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.



# **13.** LEGAL INFORMATION

CE marking is usually obligatory only for a complete end product. Riverdi display modules are semi-finished goods which are used as inputs to become part of the finished products.

Therefore, Riverdi display modules are not CE marked.

Riverdi grants the guarantee for the proper operation of the goods for a period of 12 months from the date of possession of the goods. If in a consequence of this guaranteed execution the customer has received the defects-free item as replacement for the defective item, the effectiveness period of this guarantee shall start anew from the moment the customer receives the defects-free item.

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