

Microsemi Corporation

December 17, 2015

Customer Notification No: 153920**Customer Notice (CN) – No Action Required****Subject: Timberwolf ZL380x0 Products Datasheet Update Notification****Description of Change:**

There is no change to the products, but Microsemi is informing customers of the following updates to the October-November 2015 Timberwolf ZL38040/ZL38050/ZL38060/ZL38080 product datasheets:

1. A pull-down resistor is now required between the PCLKA/I2S_SCKA pin and ground. If TDMA is unused, then the pin should be tied to ground. If the TDMA bus is acting as a timing slave (host processor is driving the PCLKA/I2S_SCKA pin), then one of the following conditions must be met:
 - a. Host drives PCLKA/I2S_SCKA pin low during reset, or
 - b. Host tri-states PCLKA/I2S_SCKA during reset (the 100kOhm resistor will keep PCLKA low), or
 - c. Host drive PCLKA/I2S_SCKA at its normal frequency.
2. The TDM Interface section of the Datasheet (Section 3.1) has been revised to better specify the combinations of TDMA/TDMB clocking options.
3. Throughout the datasheet, clarified that the UART peripheral port is intended for Tuning and Debug operation, and not for host processor control of the device.
4. Power consumption is updated to reflect characterization data on newer firmware modes of operation.
5. 3.072 MHz clock operation added to DMIC_CLK in section 2.1.
6. The documentation for several functions that can vary based on firmware loading and configuration have been removed from the Datasheet and added to the firmware manual. These sections include: Fixed Function GPIO, Cross Point Switch, and Host Interrupt Event
7. t_{FSH} maximum added in section 10.1.1 (GCI and PCM Timing Parameters)
8. Clarification and digital and analog grounds added to ECM Circuit figure.
9. Clarified HCS asserted to HCLK Rising Edge specification (section 10.2.1, SPI Slave Port Timing Parameters)
10. The Power Supply Considerations section has been rewritten to reflect Microsemi's present recommendations and better clarify device operation.

Details and a complete revision history can be found at the end of the datasheet.

There is no change to the form, fit, or reliability of the products.



Reason for Change:

The changes to the datasheet are primarily intended to clarify the operation of the device, and also to specify an application circuit requirement (PCLKA pull-down) that was previously unforeseen.

Application Impact:

No impact to the form, fit, quality, or reliability. There is no change to the product manufacturing or testing.

Method of Identifying Changed Product:

Not applicable as there is no change to the product.

Products Affected by this Change:

ZL38040LDF1, ZL38040LDG1,
ZL38050LDF1, ZL38050LDG1, ZL38050UGB2
ZL38060LDF1, ZL38060LDG1,
ZL38080LDF1, ZL38080LDG1,

Datasheet Availability:

Copies of the datasheets are now available via the Microsemi.com website.

http://www.microsemi.com/document-portal/doc_download/134994-zl38040-preliminary-datasheet

http://www.microsemi.com/document-portal/doc_download/135194-zl38050-datasheet

http://www.microsemi.com/document-portal/doc_download/133941-zl38060-preliminary-datasheet

http://www.microsemi.com/document-portal/doc_download/133943-zl38080-preliminary-datasheet

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Regards,

Microsemi Corporation

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