



## 3D8W\_1.5RP series

3W - Single/Dual Output DC-DC Converter - Wide Input - Isolated & Regulated

### DC-DC Converter

3 Watt

- ⊕ Wide input range (2:1)
- ⊕ Ultra compact DIP package
- ⊕ 1.5kVDC isolation
- ⊕ High efficiency up to 82%
- ⊕ RoHS Compliance
- ⊕ Short circuit protection (SCP)

- ⊕ Operating temperature range:  
-40°C ~ +85°C
- ⊕ Industry standard pin-out
- ⊕ No external component required
- ⊕ EN62368, UL62368 approved

The 3D8W\_1.5RP series of isolated 3W DC-DC converter products with a 2:1 input voltage range. The product has a ultra-compact DIP/SMD package with efficiencies of up to 82%, operating temperature of -40°C to +85°C and continuous short circuit protection. The ultra-small dimension design makes the converters an ideal solution for communications, instrumentation and industrial electronics applications.



**C** **UL** **us**

UL-62368-1 (E347551)

#### Common specifications

Short circuit protection:	Continuous
Cooling:	Free air convection
Operation temperature range:	-40°C~+85°C (See Fig. 1)
Storage temperature:	-55°C~+125°C
Storage humidity range:	< 95% non-condensing
Pin soldering resistance temperature:	300°C MAX, 1.5mm away from case for 10s.
Reflow soldering temperature:	Peak temperature ≤245°C, duration ≤60s max. over 217°C. see also IPC/JEDEC J-STD-020D.1.
Case material:	Black flame-retardant, heat-resistant plastic
MTBF (MIL-HDBK-217F@25°C):	>1,000,000 hours
Weight:	2.2g
Dimensions:	SD 14.00 × 14.00 × 9.00 mm ST 15.00 × 14.00 × 9.10 mm

#### Output specifications

Item	Test condition	Min	Typ	Max	Units
Voltage accuracy (5%-100% load, Vin range)	• Single output • Dual output Vo1 Vo2		±1 ±1 ±3	±3 ±3 ±5	%
No load output voltage accuracy (Single output)	input voltage range • 3.3VDC output • others output		±5 ±1.5	±7 ±5	%
No load output voltage accuracy (Dual output)	input voltage range • Vo1 • Vo2		±2 ±5	±5 ±8	%
Line regulation	Input voltage from low to high @full load		±0.2	±0.5	%
Load regulation	5%-100% load		±0.5	±1	%
Temperature drift	full load			±0.03	%/°C
Transient recovery time	25% load step change		1	3	ms
Transient response deviation	25% load step change • Single output • Dual output		±2.5 ±3	±5 ±5	%
Switching frequency	Full load, nominal input		300		KHz

#### Input specifications

Item	Test condition	Min	Typ	Max	Units
Input current (full load/no load)	<b>Single output</b> • 12VDC • 24VDC <b>Dual output</b> • 12VDC • 24VDC	314/30 154/20	338/50 163/40	mA	
Reflected ripple current	• 12VDC • 24VDC	321/30 156/20	329/50 165/40	mA	
Surge voltage (1sec. max)	• 12VDC • 24VDC	-0.7 -0.7	25 50	VDC	
Start-up voltage	• 12VDC • 24VDC		9 18	VDC	
Input Filter	Capacitance filter				
Hot plug	unavailable				

#### EMC specifications

EMI	CE	CISPR32/EN55032 CLASS B (See EMC recommended circuit, ②)
EMI	RE	CISPR32/EN55032 CLASS B (See EMC recommended circuit, ②)
EMS	ESD	IEC/EN61000-4-2 Contact ±6KV perf. Criteria B
EMS	RS	IEC/EN61000-4-3 10V/m perf. Criteria A
EMS	EFT	IEC/EN61000-4-4 ±2KV perf. Criteria B (External Circuit Refer to recommended circuit, ①)
EMS	Surge	IEC/EN61000-4-5 line to line ±2KV perf. Criteria B (External Circuit Refer to recommended circuit, ①)
EMS	CS	IEC/EN61000-4-6 3 Vr.m.s perf. Criteria A

#### Example:

3D8W\_1205S1.5RP

3 = 3Watt; D8 = DIP8; W = Wide input; 12 = 12Vin; 05 = 5Vout; S = Single output; 1.5 = 1.5kVDC; R = Regulated output; P = Short circuit protection

#### Isolation specifications

Item	Test condition	Min	Typ	Max	Units
Isolation voltage	Input-output electric Strength test for 1 min. with a leakage current of 1mA max.	1500			VDC
Isolation resistance	500VDC	1000			MΩ
Isolation capacitance	100KHz/0.1V	100			pF

#### Note:

- Unless otherwise specified, data in this data sheet should be tested under the conditions of Ta = 25°C, nominal input voltage and rated output current;
- The maximum capacitive load offered was tested at input voltage range and full load;
- All index testing methods in this datasheet are based on our Company's corporate standards.
- We can provide product customization service, please contact our technicians directly for specific information.

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### Product Selection Guide

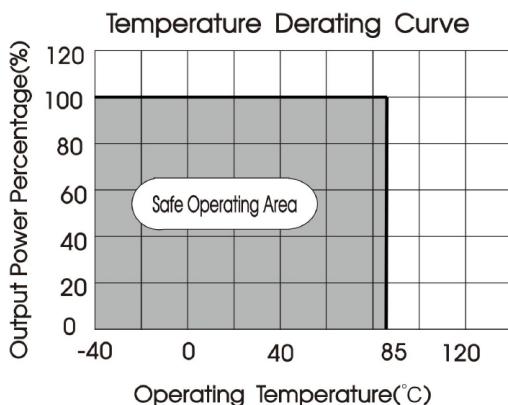
Part Number	Input Voltage [V, Vin (Range)]	Output Voltage [VDC]	Output current [mA, max/min]	Ripple & Noise [mVp-p; typ/max]	Efficiency [%; min/typ]	Capacitive load [μF]
3D8W_1203S1.5RP	12 (9-18)	3.3	758/38	50/100	73/75	2700
3D8W_1205S1.5RP	12 (9-18)	5	600/30	50/100	77/79	2200
3D8W_1212S1.5RP	12 (9-18)	12	250/13	50/100	80/82	680
3D8W_1215S1.5RP	12 (9-18)	15	200/10	50/100	81/83	470
3D8W_1224S1.5RP	12 (9-18)	24	125/6	50/100	79/81	330
3D8W_2403S1.5RP	24 (18-36)	3.3	758/38	50/100	72/74	2700
3D8W_2405S1.5RP	24 (18-36)	5	600/30	50/100	79/81	2200
3D8W_2412S1.5RP	24 (18-36)	12	250/13	50/100	81/83	680
3D8W_2415S1.5RP	24 (18-36)	15	200/10	50/100	81/83	470
3D8W_2424S1.5RP	24 (18-36)	15	125/6	50/100	81/83	330

Part Number	Input Voltage [V, Vin (Range) max.]	Output Voltage [VDC]	Output current [mA, max/min]	Ripple & Noise [mVp-p; typ/max]	Efficiency [%; min/typ]	Capacitive load [μF]
3D8W_1205D1.5RP	12 (9-18)	20	±5	±300/±15	76/78	1000
3D8W_1209D1.5RP	12 (9-18)	20	±9	±167/±9	76/78	680
3D8W_1212D1.5RP	12 (9-18)	20	±12	±125/±7	77/79	470
3D8W_1215D1.5RP	12 (9-18)	20	±15	±100/±5	77/79	330
3D8W_2405D1.5RP	24 (18-36)	40	±5	±300/±15	76/78	1000
3D8W_2409D1.5RP	24 (18-36)	40	±9	±167/±9	78/80	680
3D8W_2412D1.5RP	24 (18-36)	40	±12	±125/±7	80/82	470
3D8W_2415D1.5RP	24 (18-36)	40	±15	±100/±5	79/81	330

1. Exceeding the maximum input voltage may cause permanent damage;
2. Ripple & noise testing condition at nominal input voltage and 5%-100% load, the "tip and barrel" method is used for ripple and noise test, please refer to DC-DC Converter Application Notes for specific information.
3. The specified maximum capacitive load for positive and negative output is identical.

### Typical characteristics

Single Output



Dual Output

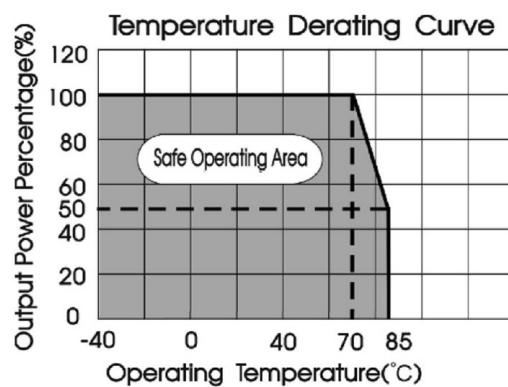


Fig-1

## 3D8W\_1.5RP series

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### Efficiency (Single)

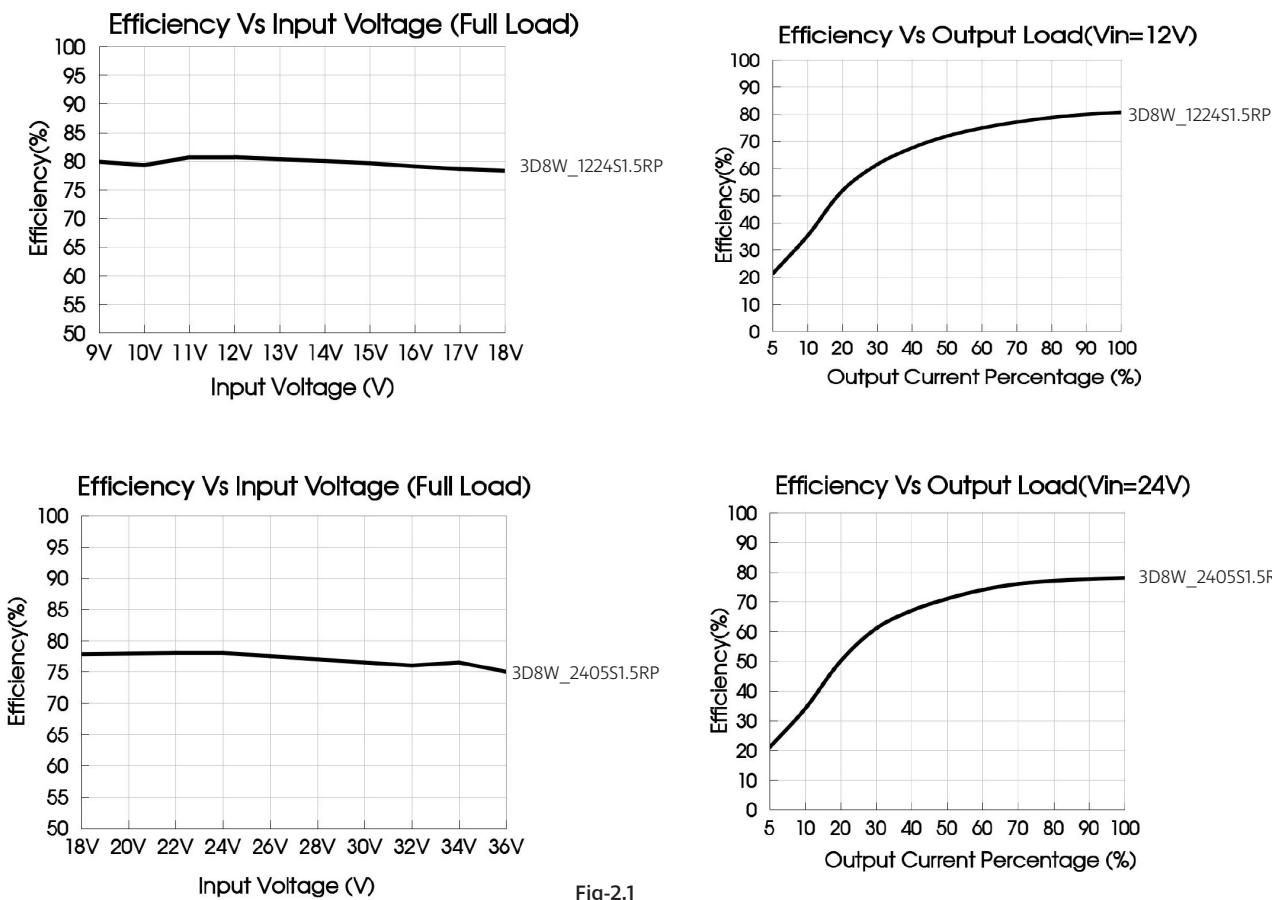


Fig-2.1

### Efficiency (Dual)

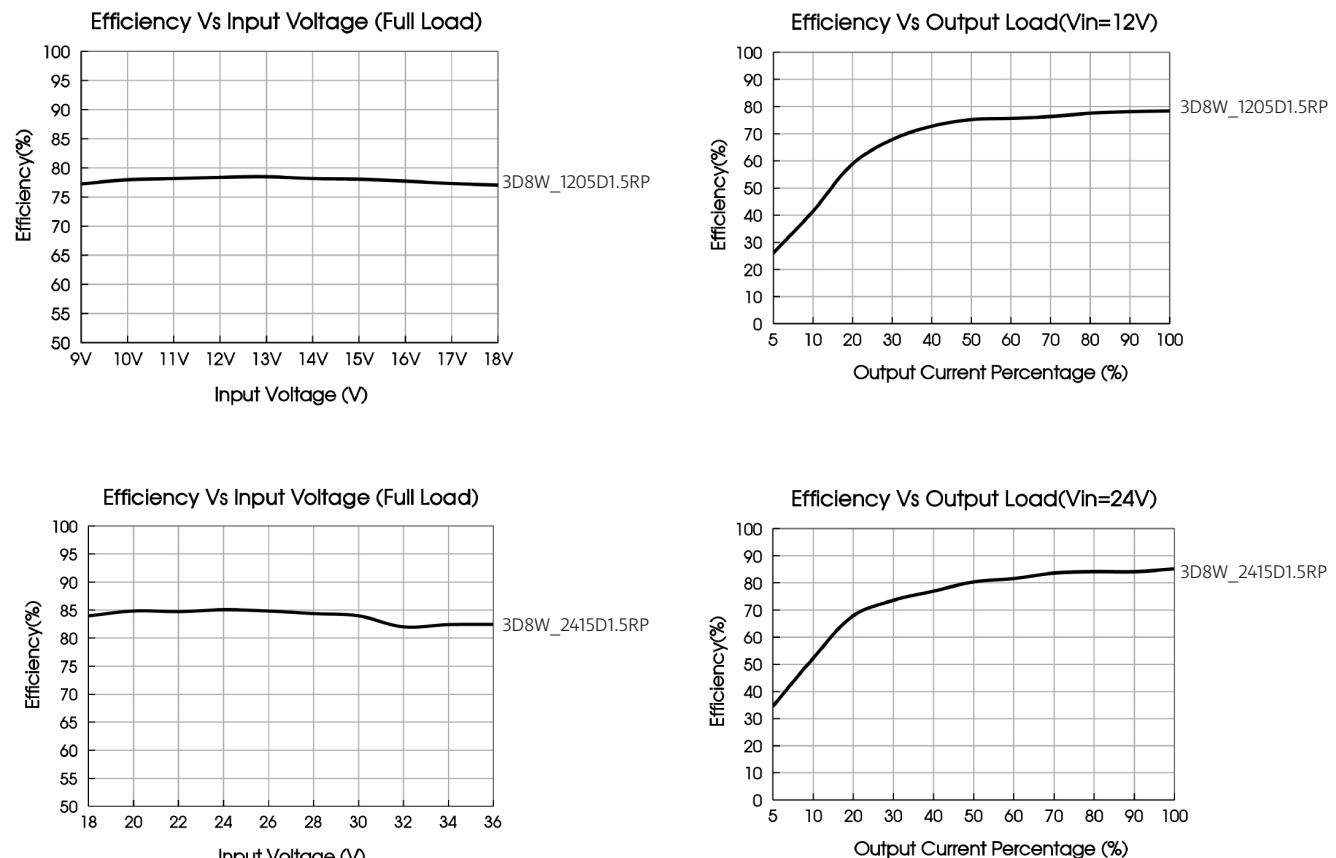


Fig-2.2

## 3D8W\_1.5RP series

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### Recommended circuit

All the DC/DC converters of this series are tested before delivery using the recommended circuit shown below. Input and/or output ripple can be further reduced by appropriately increasing the input & output capacitor values  $C_{in}$  and  $C_{out}$ , connecting a "Y" capacitor between input "GND" and output "0V", and/or by selecting capacitors with a low ESR (equivalent series resistance). Also make sure that the capacitance is not exceeding the max. capacitive load value of the product.

#### Single Output



#### Dual Output

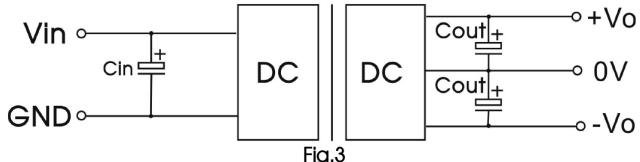


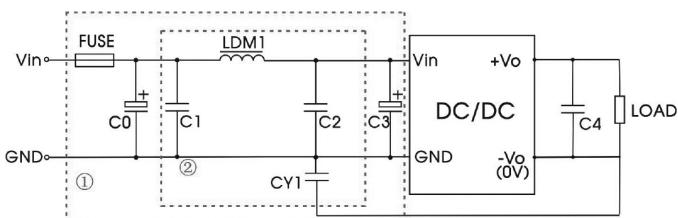
Fig.3

Vin (VDC)	12VDC	24VDC
$C_{in}$	47μF/25V	47μF/50V
Vin (VDC)	5VDC, 9VDC	12VDC, 15VDC
$C_{in}$	100μF/6.3V	10μF/35V

Vin (VDC)	12VDC	24VDC
$C_{in}$	47μF/50V	47μF/50V
Vin (VDC)	5VDC, 9VDC	12VDC, 15VDC
$C_{in}$	47μF/16V	10μF/25V

### EMC compliance circuit

#### Single Output



Vin	12VDC	24VDC
FUSE	slow blow, choose according to actual input current	
$C_0$	1000μF/25V	680μF/50V
$C_1$		4.7μF/50V
LDM1		15μH
$C_2$		4.7μF/50V
$C_3$		330μF/50V
CY1		1nF/2KV
$C_4$	Refer to the $C_{out}$ in recommended circuit	

#### Dual Output

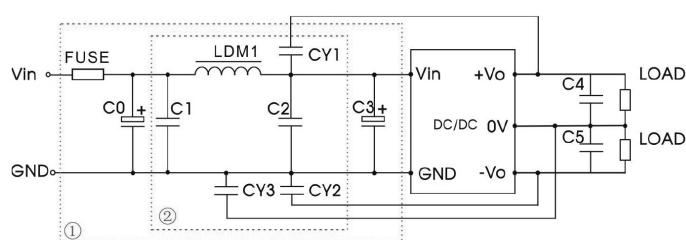
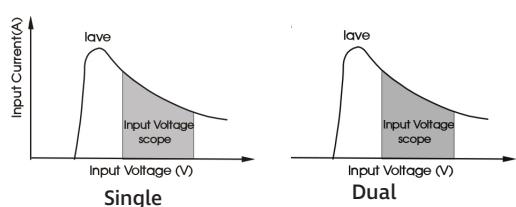


Fig.4

Vin	12VDC	24VDC
Vout	±5V, ±9V, ±12V	±15V
FUSE	slow blow, choose according to actual input current	
$C_0$	1000μF/25V	680μF/50V
$C_1$		4.7μF/50V
LDM1		10μH
$C_2$		10μF/50V
$C_3$		330μF/50V
CY1	1nF/2000V	470pF/2000V
CY2	1nF/2000V	470pF/2000V
CY3	1nF/2000V	470pF/2000V
$C_4, C_5$	Refer to the $C_{out}$ Fig.3	



### Input current

When the electricity is provided by the unstable power supply, please make sure that the range of the output voltage fluctuation and the ripple voltage of the power supply do not exceed the indicators of the modules. Input current of power supply should afford the flash startup current of this kind of DC/DC module(see Fig. on the right).

Generally:  $Vin=12V$  series  $Iave = 205mA$  (Single Vout)  $Iave = 600mA$  (Dual Vout)  
 $Vin=24V$  series  $Iave = 104mA$  (Single Vout)  $Iave = 300mA$  (Dual Vout)

### Output load requirements

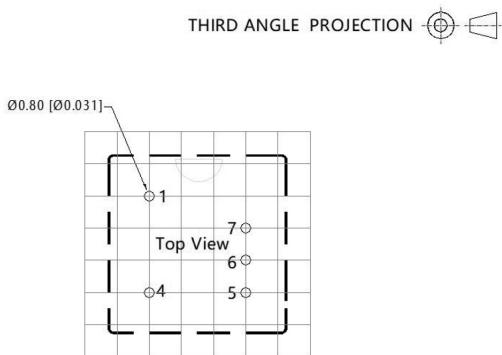
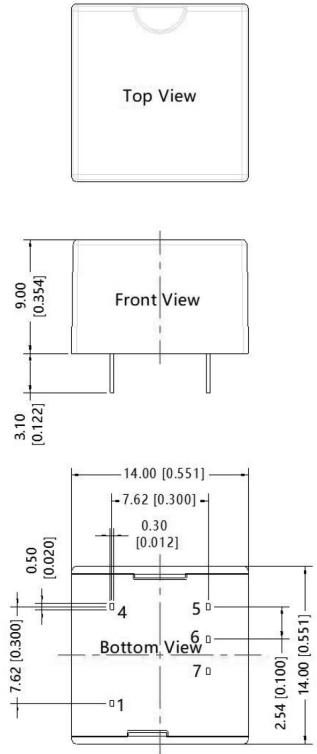
When using, the minimum load of the module output should not be less than 5% of the nominal load. In order to meet the performance parameters of this datasheet, please connect a 5% dummy load in parallel at the output end, the dummy load is generally a resistor, please note that the resistor needs to be used in derating.

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### Mechanical dimensions

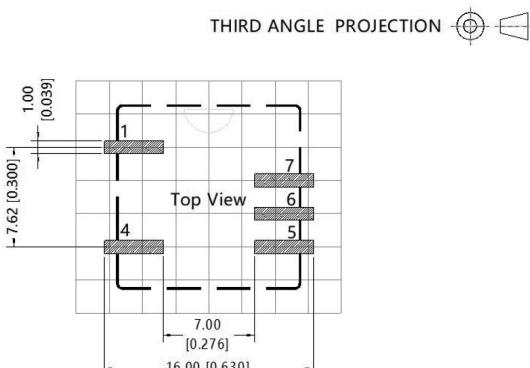
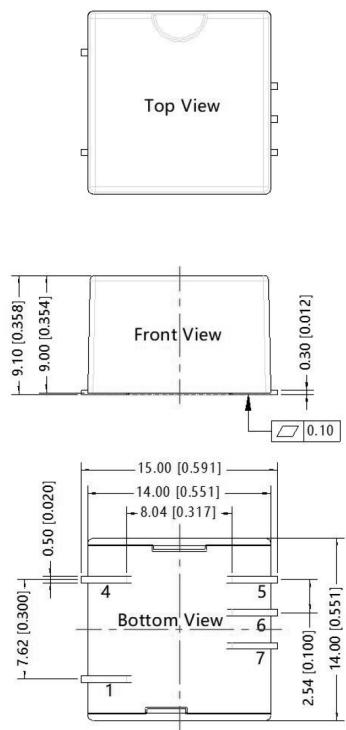
Through hole package



Pin-Out	
Pin	Function
1	GND
4	Vin
5	+Vo
6	0V
7	-Vo

Note:  
Unit: mm[inch]  
Pin diameter tolerances:  $\pm 0.10[\pm 0.004]$   
General tolerances:  $\pm 0.50[\pm 0.020]$

SMD package



Pin-Out	
Pin	Function
1	GND
4	Vin
5	+Vo
6	0V
7	-Vo

Note:  
Unit: mm[inch]  
Pin diameter tolerances:  $\pm 0.10[\pm 0.004]$   
General tolerances:  $\pm 0.50[\pm 0.020]$