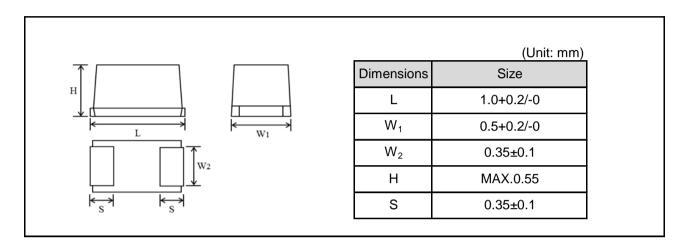
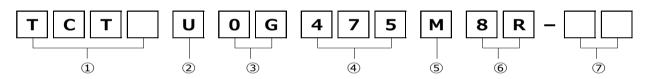
#### Features

- 1) Bottom electrode configuration results in significantly greater compactness.
- 2) Filet formation enables easy visibility after mounting.
- 3) Ideal for noise removal on power supply lines with limited space.
- 4) Eco-friendly halogen-free products.

#### Dimensions



#### Part No. Explanation



① Series name TCT

Case style

④ Nominal capacitance

Nominal capacitance in pF in 3 digits:

2 significant figures followed by the figure representing the number of 0's.

U : 1005-1005(055)size

### ③ Rated voltage

Rated voltage(V)
2.5
2.0
4
6.3
10
16
20
25
35
50

- (5) Capacitance tolerance M: ±20%
- 6 Taping
  - 8: Tape width

R: Positive electrode on the side opposite to sprocket hole

⑦ Discrimination code

#### Rated table

Impedance(Ω)

Capa	citance	Rated voltage (V.DC)								
(H	(µF)		4	6.3	10	16	20	25	35	50
0.33	(334)						30			
0.47	(474)			35						
1.0	(105)			20						
2.2	(225)			20						
3.3	(335)									
4.7	(475)		20	25						
6.8	(685)									
10	(106)									
15	(156)	25								
22	(226)									
33	(336)									
47	(476)									
68	(686)									
100	(107)									

#### Marking

The indications listed below should be given on the surface of a capacitor.

(1) Polarity: The polarity should be shown by bar. (on the anode side)

(2) Rated DC voltage: A voltage code is shown as below table.

(3) Capacitance: A capacitance code is shown as below table.

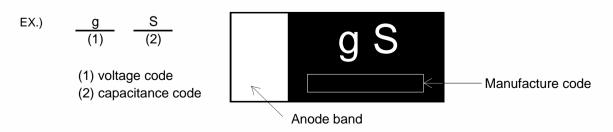
Voltage Code	Rated DC
Vollage Code	Voltage (V)
е	2.5
g	4
j	6.3
А	10
С	16
D	20
E	25
V	35
Н	50

Capacitance	Nominal	Capacitance	Nominal
Code	Capacitance (µF)	Code	Capacitance (µF)
<u>E</u>	0.15	е	15
<u>N</u>	0.33	j	22
<u>S</u>	0.47	n	33
А	1.0	S	47
E	1.5	×	68
J	2.2	а	100
Ν	3.3	e	150
S	4.7	j	220
W	6.8	n	330
а	10	s	470

Visual typical example

voltage code and capacitance code are variable with parts number.

[TCT series U case]





## Datasheet

#### Characteristics

Item		Performance	Test conditions (based on JIS C 5101-1 and JIS C 5101-3)				
Operating Temp	erature	-55°C~+125°C	Voltage reduction when temperature exceeds +85°C				
Maximum opera	ting	+85℃					
temperature with	n no						
voltage derating							
Rated voltage (V	/.DC)	Refer to " Standard list ".	at 85℃				
Category voltage	e (V.DC)	Refer to " Standard list ".	at 125°C				
Surge voltage (V	/.DC)	Refer to " Standard list ".	at 85℃				
DC Leakage cur	rent	Shall be satisfied the value on	As per 4.9 JIS C 5101-1				
		" Standard list ".	As per 4.5.1 JIS C 5101-3				
			Voltage : Rated voltage for 5min				
Capacitance tole	erance	Shall be satisfied allowance range.	As per 4.7 JIS C 5101-1				
		±20%	As per 4.5.2 JIS C 5101-3				
			Measuring frequency :120 ± 12Hz				
			Measuring voltage :0.5Vrms + 1.5V.DC				
			Measuring circuit :DC Equivalent series circui				
Tangent of loss	angle	Shall be satisfied the value on	As per 4.8 JIS C 5101-1				
(Df,tanδ)		" Standard list ".	As per 4.5.3 JIS C 5101-3				
			Measuring frequency :120 ± 12Hz				
			Measuring voltage :0.5Vrms + 1.5V.DC				
			Measuring circuit :DC Equivalent series circui				
Impedance		Shall be satisfied the value on	As per 4.10 JIS C 5101-1				
		" Standard list ".	As per 4.5.4 JIS C 5101-3				
			Measuring frequency :100 ± 10kHz				
			Measuring voltage :0.5Vrms or less				
			Measuring circuit :DC Equivalent series circui				
Resistance to	Appe-	There should be no significant	As per 4.14 JIS C 5101-1				
Soldering	arance	abnormality.	As per 4.6 JIS C 5101-3				
heat		The indications should be clear.	Dip in the solder bath				
	L.C.	Less than 200% of initial limit.	Solder temp $:240 \pm 5^{\circ}C$				
			Duration $:10 \pm 0.5s$				
	⊿C/C	Within +20/-30% of initial value.	Repetition :1				
			After the specimens, leave it at room temperature				
	DF	Less than 200% of initial limit.	for over 24h and then measure the sample.				
	(tanδ)						
Temperature	Appe-	There should be no significant	As per 4.16 JIS C 5101-1				
cycle	arance	abnormality.	As per 4.10 JIS C 5101-3				
		The indications should be clear.	Repetition : 5 cycles				
	L.C.	Less than 200% of initial limit.	(1 cycle : steps 1 to 4) without discontinuation.				
			Temp. Time				
	⊿C/C	Within ±30% of initial value.	1 -55±3℃ 30±3min				
			2 Room Temp. 3min or less				
	DF	Less than 200% of initial limit.	3 125±2℃ 30±3min				
	(tanδ)		4 Room Temp. 3min or less				
			After the specimens, leave it at room temperature				
			for over 24h and then measure the sample.				
			Initial value for $\angle$ C/C shall be the value after				
			mounted.				



Item		Performance	Test conditions (based on JIS C 5101-1 and JIS C 5101-3)			
Moisture	Appe-	There should be no significant	As per 4.22 JIS C 5101-1			
resistance	arance	abnormality.	As per 4.12 JIS C 5101-3			
resistance	arance	The indications should be clear.	After leaving the sample under such atmospheric			
	L.C.					
	L.C.	Less than 1000% of initial limit.	condition that the temperature and humidity are			
	10/0		60±2°C and 90 to 95% RH, respectively, for			
	⊿C/C	Within ±20% of initial value.	500+12/0h leave it at room temperature for			
			over 24h and then measure the sample.			
	DF	Less than 300% of initial limit.	Initial value for $\angle$ C/C shall be the value after			
	(tanδ)		mounted.			
Temperature	Temp.:-	55°C	As per 4.29 JIS C 5101-1			
Stability	⊿C/C	Within 0/-30% of initial value.	As per 4.13 JIS C 5101-3			
			Initial value for $\angle$ C/C shall be the value after			
	DF	Shall be satisfied the value on	mounted.			
	(tanδ)	" Standard list "				
	L.C.	-	-			
	Temp.: -					
	⊿C/C	Within +15/0% of initial value.				
	20/0					
	DF	Shall be satisfied the value on				
	(tanδ)	" Standard list "				
	L.C.	Less than 1000% of initial limit.				
	Temp.: -	-125°C				
	⊿C/C	Within +20/0% of initial value.				
	DF	Shall be satisfied the value on				
	(tanδ)	" Standard list "				
	L.C.	Less than 1250% of initial limit.				
Surge	Appe-	There should be no significant	As per 4.26JIS C 5101-1			
voltage	arance	abnormality.	As per 4.14JIS C 5101-3			
. enage	aranoo	The indications should be clear.	Apply the specified surge voltage via the serial			
	L.C.	Less than 200% of initial limit.	resistance of $1k\Omega$ ever 5±0.5 min. for 30±5 s.			
	L.O.		each time in the atmospheric condition of $30\pm0.5$			
	40/0	Within 2004 of initial value				
	⊿C/C	Within ±20% of initial value.	85±2°C. Repeat this procedure 1,000 times.			
			After the specimens, leave it at room temperature			
	DF	Less than 200% of initial limit.	for over 24h and then measure the sample.			
	(tanδ)		Initial value for $\angle$ C/C shall be the value after			
			mounted.			
Loading at	Appe-	There should be no significant	As per 4.23 JIS C 5101-1			
High	arance	abnormality.	As per 4.15 JIS C 5101-3			
temperature		The indications should be clear.	After applying the rated voltage for 1000+36/0 h			
	L.C.	Less than 200% of initial limit.	without discontinuation via the serial resistance			
			of $3\Omega$ or less at a temperature of $85\pm2^{\circ}$ C, leave			
	⊿C/C	Within +20/-30% of initial value.	the sample at room temperature / humidity for			
	20/0		over 24h and measure the value.			
	DF	Less than 300% of initial limit.	Initial value for $\angle$ C/C shall be the value after			
	(tanδ)		mounted.			



Item		Performance	Test conditions (based on JIS C 5101-1 and JIS C 5101-3)
Terminal	Capa-	The measured value should be	As per 4.35 JIS C 5101-1
strength	citance	stable.	As per 4.9 JIS C 5101-3
Sirengin	Appe-	There should be no significant	A force is applied to the terminal until it bends to
		-	
	arance	abnormality.	1mm and by a prescribed tool maintains the condition for 5s.
			(See the figure below)
			$50 \xrightarrow{20}$ F(Apply force)
			thickness=1.6mm 45 $45$ $45$ $1.0$ mm
Adhesiveness		The terminal should not come off.	As per 4.34 JIS C 5101-1
			As per 4.8 JIS C 5101-3
			Apply force of 2N in the two directions shown in
			the figure below for 10±1s after mounting the
			terminal on a circuit board.
			Apply force A circuit board
Dimensions		Refer to "External dimensions".	Measure using a caliper of JIS B 7507 Class
			2 or higher grade.
Resistance to		The indication should be clear.	As per 4.32 JIS C 5101-1
solvents			As per 4.18 JIS C 5101-3
			Dip in the isopropyl alcohol for 30±5s, at room
			temperature.
Solderability		3/4 or more surface area of the	As per 4.15.2 JIS C 5101-1
		solder coated terminal dipped in	As per 4.7 JIS C 5101-3
		the soldering bath should be	Dip speed=25±2.5mm / s
		covered with the new solder.	Pre-treatment (accelerated aging):
			Leave the sample on the boiling distilled water
			for 1h.
			Solder temp. : 245±5°C
			Duration : 3±0.5s
			Solder : M705
			Flux : Rosin 25% IPA 75%
Vibration	Capa-	Measure value should not fluctuate	As per 4.17 JIS C 5101-1
	citance	during the measurement.	Frequency : 10 to 55 to 10Hz/min.
	Appe-	There should be no significant	Amplitude : 1.5mm
	arance	abnormality.	Time : 2h each in X and Y directions
			Mounting : The terminal is soldered on a print
			Mounting . The terminal is solucied on a print



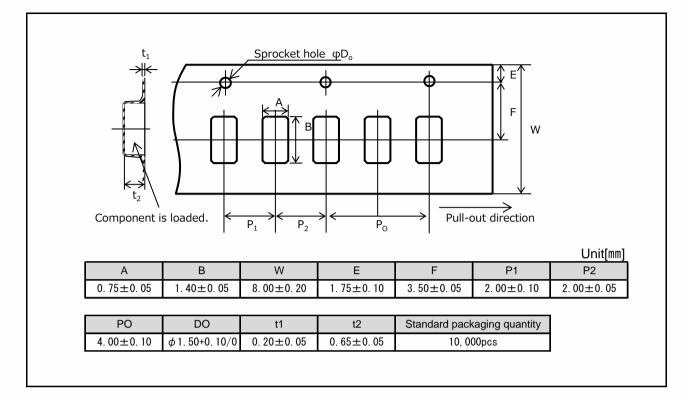
# Standard products list

	Rated	Category	Surge	Cap.	Tole-	Leakage		tanδ		Impedance
	voltage	voltage	voltage		rance	current		120Hz		
	85°C	125°C	85°C	120Hz		25℃				100kHz
Part No.						1WV	-55℃	25℃	125°C	
						5min				
	(V)	(V)	(V)	(µF)	(%)	(µA)	(%)	(%)	(%)	(Ω)
TCTU0E156M8R-V1	2.5	1.6	2.5	15	±20	7.5	90	50	60	25
TCTU0G475M8R	4	2.5	5	4.7	±20	1.9	35	20	25	20
TCTU0J474K8R	6.3	4	8	0.47	±10	0.5	35	20	25	35
TCTU0J105K8R	6.3	4	8	1	±10	0.7	35	20	25	20
TCTU0J225M8R	6.3	4	8	2.2	±20	1.4	35	20	25	20
TCTU0J475M8R-02	6.3	4	8	4.7	±20	3.0	90	50	60	25
TCTU1D334M8R	20	13	26	0.33	±20	0.7	35	20	25	30

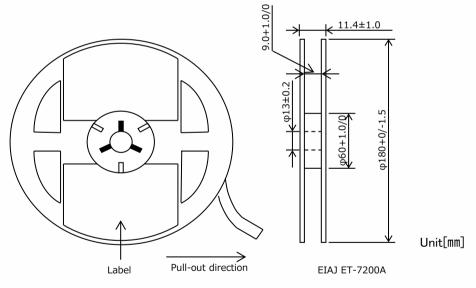




#### Packaging specifications



#### • Reel dimensions



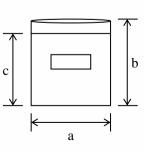
#### Damp proof package

①One reel is packed in aluminum bag.

- The size of aluminum bag is 240(a) x 250(b)mm.
- The size up to 230(c)mm is to zipper.
- ②A desiccant is packed with a reel.

③The aluminum bag is heat-sealed.

(4) The label of the same as the label on the reel is placed on the aluminum bag.





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