

# PI5C34X2245

32-Bit, 2-Port Bus Switch with 25Ω Series Resistor

## Features

- Near-Zero propagation delay
- $25\Omega$  series resistor termination
- $5\Omega$  switches connect inputs to outputs
- Direct bus connection when switches are ON
- Ultra-low quiescent power (0.2µA typical)
- Ideally suited for notebook applications
- Pin compatible with QS34X245
- Industrial operating temperature:  $-40^{\circ}$ C to  $+85^{\circ}$ C
- Packaging (Pb-free & Green available):

## – 80-pin 150 mil wide BQSOP (B)

## **Block Diagram**

## Description

Pericom Semiconductor's PI5C34X2245 is a 32-bit, 2-port bus switch. Four enable signals ( $\overline{\text{BEn}}$ ) turn the switches on. The bus switch creates no additional propagational delay or additional ground bounce noise. The device has a built-in 25 $\Omega$  resistor to reduce noise resulting from reflection, thus eliminating the need for an external terminating resistor.

## **Pin Configuration**



NC d		80 Vcc
		79 🛛 BE1
		79 🛛 BET 78 🗋 Bo
A1 L A2 D		78 🖵 🗗 77 🗍 B1
A2 L A3 [		76 🛛 B2
Аз Ц А4 П	6	76 🗆 B2 75 🗋 B3
A5 [		74 🛛 B4
A6 [		73 🛛 B5
A7 [		72 🛛 B6
		71 D B7
		70 Vcc
A8 [	12	69 D BE2
A9 🗖		68 D B8
A10		67 🛛 B9
A11 [		66 B10
A12 🛙		65 🛛 B11
A13 [		64 🛛 B12
A14 [	18	63 B13
A15 [	19	62 B14
GND E	20	61 B15
NC E	21	60 🖞 Vcc
A16 C	22	59 🛛 BE3
A17 [	23	58 🛛 B16
A18 [	24	57 B17
A19 [		56 B18
A20 [		55 B19
A21 [		54 B20
A22 [		53 B21
A23 [		52 B22
GND E		51 B23
		50 <u>Vcc</u>
A24		
A25 [		48 B24
A26		47 B25
A27 [		46 B26
A28 [		45 B27
A29 [		44 B28
A30 [		43 B29
	39	42 B30
	40	41 B31



### **Maximum Ratings**

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature65°C to +150°C	7
Ambient Temperature with Power Applied40°C to +85°C	2
Supply Voltage to Ground Potential0.5V to +7.0V	1
DC Input Voltage0.5V to +7.0V	1
DC Output Current	1
Power Dissipation	7

Note:

**Pin Description** Pin Name

BEx

A0-A31

B<sub>0</sub>-B<sub>31</sub>

I/0

Ι

I/O

I/O

Bus A

Bus B

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Description

Bus Enable Input (Active LOW)

## **Truth Table**<sup>(1)</sup>

Function	BEn	A0 - 31
Disconnect	Н	Hi-Z
Connect	L	B0-3

#### Notes:

H = High Voltage Level, L = Low Voltage Level 1.

Hi-Z = High Impedance

Parameters	Description	Test Conditions <sup>(1)</sup>	Min.	Typ <sup>(2)</sup>	Max.	Units
V <sub>IH</sub>	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			v
V <sub>IL</sub>	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	
I <sub>IH</sub>	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$			±1	
I <sub>IL</sub>	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$			±1	μA
I <sub>OZH</sub>	High Impedance Output Current	0 - A, B - V <sub>CC</sub>			±1	μΑ
V <sub>IK</sub>	Clamp Diode Voltage	$V_{CC} = Min., I_{IN} = -18mA$		-0.7	-1.2	V
IOS	Short Circuit Current <sup>(3)</sup>	$A(B) = 0V, B(A) = V_{CC}$	100			mA
R <sub>ON</sub> Switch On-Resistance <sup>(4)</sup>	Societale On Descience (4)	$V_{CC} = Min., V_{IN} = 0V, I_{ON} = 48mA$		28	40	Ω
	$V_{CC} = Min., V_{IN} = 2.4V, I_{ON} = 15mA$		35	48		

#### Notes:

For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type. 1.

Typical values are at  $V_{CC} = 5.0V$ ,  $T_A = 25^{\circ}C$  ambient and maximum loading. 2.

Not more than one output should be shorted at one time. Duration of the test should not exceed one second. 3.

Measured by the voltage drop between A and B pin at indicated current through the switch. On-Resistance is determined by the lower of the 4. voltages on the two (A,B) pins.

## **Capacitance** ( $T_A = 25^{\circ}C$ , f = 1 MHz)

Parameters <sup>(1)</sup>	Description	Test Conditions	Туре	Units
C <sub>IN</sub>	Input Capacitance		6	
C <sub>OFF</sub>	A/B Capacitance, Switch Off	$V_{IN} = 0V$	6	pF
C <sub>ON</sub>	A/B Capacitance, Switch On		12	

Notes:

1. This parameter is determined by device characterization but is not production tested.

## **Power Supply Characteristics**

Parameters	Description	Test Conditi	ons <sup>(1)</sup>	Min.	Typ <sup>(2)</sup>	Max.	Units
I <sub>CC</sub>	Quiescent Power Supply Current	$V_{CC} = Max.$	$V_{IN} = GND$ or $V_{CC}$		0.1	3.0	μΑ
$\Delta I_{CC}$	Supply Current per Input @ TTL HIGH	$V_{CC} = Max.$	$V_{\rm IN} = 3.4 V^{(3)}$			2.5	mA
I <sub>CCD</sub>	Supply Current per Input per MHz <sup>(4)</sup>	V <sub>CC</sub> = Max., A & B Pins Open BEn = GND, Control Input Tog- gling 50% Duty Cycle				0.25	mA/ MHz

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.

2. Typical values are at  $V_{CC} = 5.0V$ , +25°C ambient.

3. Per TTL driven input ( $V_{IN}$  = 3.4V, control inputs only); A and B pins do not contribute to I<sub>CC</sub>.

4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

## Switching Characteristics over Operating Range

Parameter	Description	Conditions	Co	Units	
			Min.	Max.	Units
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay <sup>(1,2)</sup> Ax to Bx, Bx to Ax			1.25	
t <sub>PZH</sub> t <sub>PZL</sub>	Bus Enable Time $\overline{BE}x$ to Ax or Bx	$C_{L} = 50 pF$ $R_{L} = 500 \Omega$	1.5	7.5	ns
t <sub>PHZ</sub> t <sub>PLZ</sub>	Bus Disable Time $\overline{BE}x$ to Ax or Bx		1.5	5.5	

Notes:

1. This parameter is guaranteed but not tested on Propagation Delays.

2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.



## Packaging Mechanical: 80-Pin 150 Mil Wide Plastic BQSOP (B)



## **Ordering Information**

Ordering Code	Package Code	Package Description
PI5C34X2245B	В	80-Pin 150 Mil Wide Plastic BQSOP
PI5C34X2245BE	В	Pb-free & Green, 80-Pin 150 Mil Wide Plastic BQSOP

Notes:

• Thermal characteristics can be found on the company web site at www.pericom.com/packaging/

• E = Pb-free & Green

• Adding an X suffix = Tape/Reel

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