

Model 536 High Stability HCMOS TCXO

Features

- Fundamental Crystal Design
- Frequency Range 10 50MHz *
- Operating Voltage +3.3V
- Frequency Stability, Overall ±4.6ppm [-40°C to +85°C]
- Operating Temperature Range to -40°C to +105°C
- Voltage Control Option for Frequency Tuning [VCTCXO]
- Enable Function Option Available
- Tape and Reel Packaging, EIA-481

Applications

- 5G, 4G, LTE
- Femtocells, RRU, BBU
- Military Radio [Manpack]
- Inflight Entertainment

- Autonomous Technologies
- Synchronous Ethernet
- IP Networking
- Medical Imaging

- Stratum 3
- IEEE 1588 Timing
- Wireless Communication
- Test and Measurement

Description

CTS Model 536 is a high performance Temperature Compensated Crystal Oscillator [TCXO] suitable for applications requiring tight stability, Stratum 3 performance and more. Employing IC technology with HCMOS output and analog temperature compensation engine; coupled with a fundamental quartz crystal M536 has excellent stability and low jitter/phase noise performance.

Ordering Information



Notes

1] Refer to document 016-1454-0, Frequency Code Tables. 3-digits for frequencies <100MHz, 4-digits for frequencies 100MHz or greater.

2] Frequency vs. Temperature only.

3] Available with stability code X2 and 05 only

Not all performance combinations and frequencies may be available. Contact your local CTS Representative or CTS Customer Service for availability.

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Page 1 of 7

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Part Dimensions: 5.0 × 3.2 × 2.0mm

Standard Frequencies – see Page 7 for developed frequencies. * Check with factory for availability of frequencies not listed.



Electrical Specifications

Operating Conditions

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNIT
Maximum Supply Voltage	V _{CC}	-	-0.5	-	4.6	V
Maximum Control Voltage	Vc	-	-0.3	-	V _{CC}	V
Supply Voltage	Vcc	±5%	3.14	3.3	3.47	V
Supply Current	I _{CC}	-	-	-	10	mA
Output Load	CL	-	-	-	15	pF
			-10		+70	
Operating Temperature	T _A	-	-40	+25	+85	°C
			-40		+105	
Storage Temperature	T _{STG}	-	-55	-	+125	°C

Frequency Stability

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNIT
Frequency Range	fo	fo Frequency stability ±0.10ppm		10 - 40		
		Frequency stability ±0.28ppm or ±0.50ppm		10 - 50		MHz
Frequency Stability		-10°C to +70°C & -40°C to +85°C				
Overall Frequency Stability	Ret	f. f _o , 20 Years Aging, ±0.28ppm over -40°C to +85°C	-4.6	-	4.6	ppm
Initial Calibration	∆f/f ₀	Initial Calibration @ +25°C, At Time of Shipment	-1.0	-	1.0	ppm
Temperature Only		[fmax - fmin]/2, Over Temperature Range		0.10, 0.28, 0.5	0	±ppm
Voltage Coefficient	$\Delta f/f_{25}$	Supply Voltage, ±5%	-0.2	-	0.2	
Load Coefficient		Load, ±10%	-0.2	-	0.2	ppm
	A E / E	First Year @ +25°C, nominal V_{CC} and V_{C}	-1.0	-	1.0	
Aging	∆f/f ₂₅	20 Years @ +25°C, nominal V_{CC} and V_{C}		-	3.0	ppm
Frequency Stability		-40°C to +105°C				
Overall Frequency Stability	Ref.	f. f ₀ , 20 Years Aging, ±0.28ppm over -40°C to +105°C		-	4.7	ppm
Initial Calibration	∆f/f ₀	Initial Calibration @ +25°C, At Time of Shipment	-0.9	-	0.9	ppm
Temperature Only		[fmax - fmin]/2, Over Temperature Range		0.28, 0.5		±ppm
Voltage Coefficient	$\Delta f/f_{25}$	Supply Voltage, ±5%	-0.2	-	0.2	
Load Coefficient		Load, ±10%		-0.2 - 0.2		ppm
A = i = =	A E / E	First Year @ +25°C, nominal V_{CC} and V_{C}	-1.0	-	1.0	
Aging	∆f/f ₂₅	20 Years @ +25°C, nominal V_{CC} and V_{C}		-	3.0	ppm



Electrical Specifications

Output Parameters

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNI
Output Type	-			HCMOS		-
	V _{OH}	Logic '1' Level, CMOS Load	$0.9V_{CC}$	-	-	V
Output Voltage Levels	V _{OL}	Logic '0' Level, CMOS Load	-	-	$0.1 V_{CC}$	V
Output Duty Cycle	SYM	@ 50% Level, output waveform	45	-	55	%
Rise and Fall Time	T _R , T _F	@ 10%/90% Levels, output waveform	-	-	8	ns
Start Up Time	Ts	Application of V_{CC}	-	2	5	ms
Enable Function						
Enable Input Voltage	VIH	Pin 3 Logic '1', Output Enabled	$0.8V_{CC}$	-	-	V
Disable Input Voltage	VIL	Pin 3 Logic '0', Output Disabled	-	-	$0.2V_{CC}$	V
Disable Current	I _{DIS}	Pin 3 Logic '0', Output Disabled	-	-	3.5	mA
Enable Time	T _{PLZ}	Pin 3 Logic '1'	-	-	5	ms
Phase Noise	-	See Typical Plots	-	-	-	-

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNIT
Control Voltage	Vc	V _{CC} = +3.3V	0.0	1.65	3.3	V
Frequency Tuning [VCTCXO Only]	$\Delta f/f_{O}$	Specified V_{C} Range		±5 to ±10		ppm
Input Impedance	Zvc	-	100	-	-	kOhms
Linearity	L	Best Straight Line Fit	-	±5	±10	%
Transfer Function	-	-		Positive		-

Test Circuit



DOC#008-0587-1 Rev.G

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Page 3 of 7



Electrical Specifications

Performance Data

Phase Noise [typical]

50MHz, V_{CC} = +3.3V, T_A = +25°C





Model 536 High Stability HCMOS TCXO

Mechanical Specifications





Package Drawing – 50MHz Only



Recommended Pad Layout 4.45



Pin Assignment Notes

- 1. Do not connect to Pin 1, if Voltage Control function is not used [TCXO].
- 2. Do not connect to Pin 3, if Output Enable function is not used.
- 3. Add 0.1µF capacitor between Pin 7 and ground.

Table I - Month Code

MONTH	1	2	3	4	5	6	7	8	9	10	11	12
MONTH	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	ост	NOV	DEC
MONTH CODE	1	2	3	4	5	6	7	8	9	Х	Y	Ζ

DOC#008-0587-1 Rev. G

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Page 5 of 7

Marking Information

- 1. xxxx Frequency Code, 4-digits. See Page 7.
- 2. – Pin 1 Identifier.
- 3. ** Manufacturing Site Code.
- 4. YM Date Code; Y year [last digit], M month.
- [See Table I for month codes.]

Area for Crystal Lot Code or Date Code. [Not provided for 50MHz.]

Notes

5.

- DO NOT make connections to non-labeled pins or castellations as they may have internal connections used in the manufacturing process.
- JEDEC termination code (e4). Barrier-plating is nickel [Ni] with gold [Au] flash plate.
- Reflow conditions per JEDEC J-STD-020; +260°C maximum, 10 seconds.
- 4. MSL = 1.

Pin Assignments

Pin	Symbol	Function
1	Vc	Voltage Control Note 1
2	-	Do Not Connect
3	EOH	Enable, Pin 3 [Optional] Note 2
4	GND	Circuit & Package
5	-	Do Not Connect
6	Output	HCMOS
7	-	Vcfilter
8	-	Do Not Connect
9	V _{CC}	Supply Voltage
10	-	Do Not Connect



Packaging - Tape and Reel



Reel Drawing



Notes

- 1. Device quantity is 500 pieces maximum per 180mm reel.
- 2. Complete CTS part number, frequency value and date code information must appear on reel and carton labels.



Addendum

Available Frequencies for Stability ±0.50ppm - MHz

FREQUENCY	ORDERING CODE	MARKING CODE	FREQUENCY	ORDERING CODE	MARKING CODE	FREQUENCY	ORDERING CODE	MARKING CODE
10.000000	100	1000	38.880000	388	3888			
19.200000	192	1920	40.000000	400	4000			
20.00000	200	2000						
25.000000	250	2500						
38.400000	384	3840						

Available Frequencies for Stability ±0.28ppm - MHz

FREQUENCY	ORDERING CODE	MARKING CODE	FREQUENCY	ORDERING CODE	MARKING CODE	FREQUENCY	ORDERING CODE	MARKING CODE
10.000000	100	1000	38.880000	388	3888			
19.200000	192	1920	40.000000	400	4000			
20.000000	200	2000						
25.000000	250	2500						
38.400000	384	3840						

Available Frequencies for Stability ±0.10ppm - MHz

FREQUENCY	ORDERING CODE	MARKING CODE	FREQUENCY	ORDERING CODE	MARKING CODE	FREQUENCY	ORDERING CODE	MARKING CODE
10.000000	100	1000						
20.000000	200	2000						
25.000000	250	2500						