

UG301: Si5332-12EX-EVB User's Guide

The Si5332-12EX-EVB is used for evaluating the Si5332 Low Jitter Any-Frequency Clock Generator. The Si5332 uses the patented Multisynth[™] technology to generate up to twelve independent clock frequencies each with 0 ppm synthesis error. The Si5332-12EX-EVB has three independent

input clocks. The Si5332-12EX-EVB can be controlled and configured using the Clock Builder Pro[™] (CB Pro[™]) software tool.

EVB FEATURES

- Powered from USB port or external power supply.
- Onboard 25 MHz XTAL allows free-run mode of operation on the Si5332 or up to 2 input clocks for synchronous clocking.
- CBPro[™] GUI programmable VDD supply allows device to operate from 3.3, 2.5, or 1.8 V.
- CBPro GUI programmable VDDO supplies allow each of the 10 outputs to have its own power supply voltage selectable from 3.3, 2.5, or 1.8 V.
- CBPro GUI-controlled voltage, current, and power measurements of VDD and all VDDO supplies.
- SMA connectors for input and output clocks.



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1. Functional Block Diagram

Below is a functional block diagram of the Si5332-12EX-EVB. This EVB can be connected to a PC via the main USB connector for programming, control and monitoring. See section "2. Quick start" or section "7. Installing CBPro Desktop Software" for more information.



Figure 1.1. Si5332-12EX-EVB Functional Block Diagram

2. Si5332 CBPro[™]

- The Si5332 is intended to be part of the CBPro software and this initial software release "showcases" that trait. This software contains:
 - 1. An EVB GUI that communicates and controls the EVB by allowing the user to set VDD supplies
 - 2. The ability to modify frequency plan (from the starting point CBPro file provided with this limited release) from an existing CBPro file.

CB ClockBuilder Pro Wizard - Silicon Labs					
	ClockBuilder Pro WizardImage: Constant SimpleWe Make Timing SimpleImage: Constant Simple				
Work With a Design	Quick Links				
Create New Design Open Design Project File	<u>Clock Generators & Jitter Attenuators</u> <u>Knowledge Base</u> <u>Custom Part Number Lookup</u> <u>ClockBuilder Go iOS App</u>				
ex <u>Open Sample Design</u>	Applications Documentation				
Evaluation Board Detected Si5332 EVB Open Default Plan EVB GUI	<u>10/40/100G Line Card White Paper</u> <u>Clock Generators for Cloud Data Centers White Paper</u> <u>Optimizing Si534x Jitter Performance App Note</u> <u>SyncE and IEEE 1588 App Note</u>				
	ClockBuilder Pro Documentation				
	CBPro Overview CBPro Tools & Support for In-System Programming UPDATE CLI User's Guide UPDATE Release Notes • Knowledge Base				
0,	Version 2.12.0.200 Built on 12/15/2016				

Figure 2.1. CBPro Start Screen

3. Si5332-12EX-EVB Schematics

The schematic and layout files are provided in the here: schematics and layouts.

Please review the files, especially the DUT page in order to get familiar with using the EVB through CBPro[™].

4. Si5332 CBPro[™] EVB GUI

The EVB GUI can be used to communicate the part for register access:

The first page shows the board's identity.

🟮 SIS332-GM3 EVB - ClockBuilder Pro							
File Help							
Info DUT Re	gister Editor	Regulators GPIO	Ŧ				
Board Identifi	cation:						
Board ID (Code: 1	(Si5332)					
Board SN:	0	0-00-1A-BB-8E-70					
Package II	. 0	FN-48					
Log							
Filtered	Filtered 📱 Auto Scroll: On 📱 Insert Marker Clear Copy to Clipboard Pause						
Timestamp							
			A				
17:29:35.434	EVB	Starting Read_Voltage_Level(regulator=VDDO_C)					
17:29:35.442	EVB	finished Read_Voltage_Level(regulator=VDDO_C) => V3P30	v				
Si5332OFN-48		ClockBu	uilder Pro v2.15.4.200 [2017-06-19]				

Figure 4.1. Board ID Page

The other pages for for register access, VDD control, and GPIO control.

CB Si5332-GM3 EVB - ClockBuilder Pro							
File Help							
Info DUT Register Editor Regulators GPIO	v						
Register Peek/Poke							
Hex Decimal							
Address: 0x0000 0							
# Bytes: 1 Read Write							
Unsigned Int: 0							
Hex:							
15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0							
Binary: 000000000000000000000							
(binary edit is only supported with 16 bits or less)							
Log Filtered Auto Scroll: On Insert Marker Clear Copy to Clipboard	Pause						
	rause						
Timestamp Source Message							
17:29:35.434 EVB finished Read_Voltage_Level(regulator=VDD_DIG) => V3P30 17:29:35.434 EVB Starting Read_Voltage_Level(regulator=VDDO_C)	A						
17:29:35.442 EVB starting Read_Voltage_Level(regulator=VDDO_C) 17:29:35.442 EVB finished Read_Voltage_Level(regulator=VDDO_C) => V3P30							
Si5332QFN-48							
313332QF14*40	ClockBuilder Pro v2.15.4.200 [2017-06-19]						

Figure 4.2. Register Access

5. Installing ClockBuilderPro (CBPro) Desktop Software

To install the CBOPro software on any Windows 7 (or above) PC:

Go to https://www.silabs.com/products/development-tools/software/clockbuilder-pro-software and download ClockBuilderPro software. Both installation instructions and User's Guide for ClockBuilderPro can be found at this link. Please follow the instructions as indicated.



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