

1700 V 300 A

CAS300M17BM2

1700 V, 8.0 mΩ, Silicon Carbide, Half-Bridge Module

Technical Features

- Industry Standard 62mm Footprint
- Ultra Low Loss, High-Frequency Operation
- Zero Reverse Recovery from Diodes
- Zero Turn-off Tail Current from MOSFET
- Normally-off, Fail-safe Device Operation
- Copper Baseplate and Aluminum Nitride Insulator



 \mathbf{V}_{DS}

I_{DS}

Applications

- HF Resonant Converters/Inverters
 - Solar and Wind Inverters
- UPS and SMPS
- Motor Drive
- Traction

System Benefits

- Enables Compact and Lightweight Systems
- High Efficiency Operation
- Mitigates Over-voltage Protection
- Reduced Thermal Requirements
- Reduced System Cost

Maximum Parameters (Verified by Design)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conditions	Note
Drain-Source Voltage	V _{DS}			1700			
Gate-Source Voltage, Maximum Values	V _{GS max}	-10		+25	v		
Gate-Source Voltage, Recommended Values	V _{GS op}	-5		+20			
			325			$V_{GS} = 20 \text{ V}, T_{C} = 25 \text{ °C}$	F i= 20
DC Continuous Drain Current	ID		225			$V_{GS} = 20 \text{ V}, \text{T}_{C} = 90 ^{\circ}\text{C}$	- Fig. 26
			556		A	$V_{GS} = -5 V$, $T_{C} = 25 °C$	
DC Source-Drain Current (Body Diode)	I _{SD BD}		353			$V_{GS} = -5 V$, $T_{C} = 90 °C$	
Maximum Pulsed Drain-Source Current	I _{D (pulsed)}			900		Pulse width limited by T _{VJ(max)}	
Maximum Virtual Junction Temperature under Switching Conditions	T _{VJ op}	-40		150	°C		

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MOSFET Characteristics (Per Position) (T_{vJ} = 25 °C unless otherwise specified)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conditions	Note
Drain-Source Breakdown Voltage	V _{(BR)DSS}	1700				$V_{GS} = 0 V, I_{DS} = 2 mA$	Fig. 29
Gate Threshold Voltage	V _{GS(th)}	1.8	2.5		V	$V_{DS} = V_{GS}, I_{DS} = 104 \text{ mA}$	Fig. 7
			0.7	2		$V_{GS} = 0 V, V_{DS} = 1700 V$	
Zero Gate Voltage Drain Current	I _{DSS}		1.5	4	mA	$V_{GS} = 0 V, V_{DS} = 1700 V, T_{VJ} = 150^{\circ}C$	
Gate-Source Leakage Current	I _{GSS}		1	600	nA	$V_{GS} = 25 V, V_{DS} = 0 V$	
Drain-Source On-State Resistance			8.0	10.0		$V_{GS} = 20 \text{ V}, I_D = 300 \text{ A}$	Fig. 4
(MOSFET Only)	R _{DS(on)}		16.2	20.0	mΩ	$V_{GS} = 20 \text{ V}, \text{ I}_{D} = 300 \text{ A}, \text{ T}_{VJ} = 150 \text{ °C}$	Fig. 5 Fig. 6
-			133			$V_{DS} = 20 \text{ V}, \text{ I}_{D} = 300 \text{ A}$	E. 0
Transconductance	g _{fs}		131		S	$V_{DS} = 20 \text{ V}, \text{ I}_{D} = 300 \text{ A}, \text{ T}_{VJ} = 150 \text{ °C}$	- Fig. 8
Turn-On Switching Energy	E _{on}		13.0			$V_{DD} = 900 \text{ V}, I_D = 300 \text{ A},$ $V_{GS} = -5 \text{ V}/+20 \text{ V},$ $R_{G(ON)} = 2.5 \Omega, R_{G(OFF)} = 2.5 \Omega,$	Fig. 22
Turn-Off Switching Energy	E _{off}		10.0		mJ	Fig $_{VJ} = 150 \text{ °C}$ Note: IEC 60747-8-4 Definitions	Fig. 22
Internal Gate Resistance	R _{G(int)}		3.7		Ω	f = 1 MHz, V _{AC} = 25 mV	
Input Capacitance	C _{iss}		20		~ F		
Output Capacitance	C _{oss}		2.5		nF	$V_{DS} = 1000 \text{ V}, V_{AC} = 25 \text{ mV}$ f = 200 kHz	Fig. 16 Fig. 17
Reverse Transfer Capacitance	C _{rss}		80		pF	1 - 200 KHZ	
Gate to Source Charge	Q _{GS}		273				
Gate to Drain Charge	Q _{GD}		324		nC	$V_{DS} = 900 \text{ V}, V_{GS} = -5 \text{ V}/+20 \text{ V},$ $I_D = 300 \text{ A}, \text{ Per JEDED24 pg 27}$	Fig. 15
Total Gate Charge	Q _G		1076			10 - 300 A, I CI 3EDED24 98 21	
Turn-on Delay Time	t _{d(on)}		105			$V_{DD} = 900V, V_{GS} = -5/+20V,$	
Rise Time	tr		72			$I_{D} = 300 \text{ A}, R_{G(ext)} = 2.5 \Omega,$	
Turn-off Delay Time	t _{d(off)}		211		ns	Timing relative to V _{DS} Note: IEC 60747-8-4, pg 83	Fig. 23
Fall Time	t _f		56			Inductive load	
MOSFET Thermal Resistance, Junction to Case	$R_{th\text{-}JCM}$		0.067	0.071	°C/W		Fig. 27

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Diode Characteristics (Per Position) (T_{vJ} = 25 °C unless otherwise specified)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conditions	Notes
Dady Diada Famuard Valtage	N		1.7	2.0	N	V _{GS} = 0 V, I _{SD} = 300 A	Fig. 10
Body Diode Forward Voltage	V _{SD}		2.2	2.5	V	$V_{GS} = 0 \text{ V}, I_{SD} = 300 \text{ A}, T_{VJ} = 150 \text{ °C}$	Fig. 11
Total Capacitive Charge	Qc		4.4		μC	$I_{SD} = 300 \text{ A}, V_{DS} = 900 \text{ V}, T_{VJ} = 25^{\circ}\text{C},$ $di_{SD}/dt = 9 \text{ kA}/\mu\text{s}, V_{GS} = -5 \text{ V}$	
DIODE Thermal Resistance, Junction to Case	R _{th-JCD}		0.060	0.065	°C/W		Fig. 28

Module Physical Characteristics

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conditions
Stray Inductance	L _{Stray}		15		nH	Between terminals 2 & 3
Case Temperature	Tc	-40		125	°C	
Mounting Torque	Ms		5.0		N-m	To heatsink and terminals
Weight	W		300		g	
Case Isolation Voltage	V _{Isol}	5.0			kV	AC, 50 Hz, 1 minute
Clearance Distance		9				Terminal to terminal
Creepage Distance		30			mm	Terminal to terminal
		40				Terminal to baseplate





Figure 1. Output Characteristics for T_{VJ} = 40 °C



Figure 3. Output Characteristics for T_{VJ} = 150 °C







Figure 2. Output Characteristics for T_{VJ} = 25 °C



Figure 4. Normalized On-Resistance vs. Temperature





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Figure 7. Threshold Voltage vs. Temperature











Figure 8. Transfer Characteristic for Various Junction Temperatures



Figure 10. Diode Characteristic at T_{VJ} = 25 °C



Figure 12. 3^{rd} Quadrant Characteristic at T_{VJ} = -40 °C

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Figure 13. 3rd Quadrant Characteristic at T_{VJ} = 25 °C



Figure 15. Gate Charge Characteristics



Figure 17. Capacitances vs. Drain-Source Voltage (0 - 1 kV)



Figure 14. 3^{rd} Quadrant Characteristic at T_{VJ} = 150 °C



Figure 16. Capacitances vs. Drain-Source Voltage (0 - 200 V)



Figure 18. Output Capacitor Stored Energy

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Figure 19. Inductive Switching Energy vs. Drain Current For V_{DS} = 900 V, R_{G} = 2.5 Ω



Figure 21. Inductive Switching Energy vs. $R_{G(ext)}$



Figure 23. Timing vs. $R_{G(ext)}$



Figure 20. Inductive Switching Energy vs. Drain Current For V_{DS} = 1200 V, R_{G} = 2.5 Ω



Figure 22. Inductive Switching Energy vs. Temperature



Figure 24. Resistive Switching Time Description

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Timing Characteristics



Figure 25. Maximum Power Dissipation (MOSFET) Derating vs. Case Temperature



Figure 27. MOSFET Junction to Case Thermal Impedance



Figure 29. Safe Operating Area



Figure 26. Continuous Drain Current Derating vs Case Temperature



Figure 28. Diode Junction to Case Thermal Impedance

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Schematic



Package Dimension (mm)





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Supporting Links & Tools

Evaluation Tools & Support

- KIT-CRD-CIL17N-BM: Dynamic Performance Evaluation Board for the 62 mm Module
- SpeedFit 2.0 Design Simulator™
- Technical Support Forum

Dual-Channel Gate Driver Board

- CGD1700HB2P-BM2: Dual Channel Differential Isolated Half Bridge Gate Driver Board
- CGD12HB00D: Differential Transceiver Daughter Board Companion Tool for Differential Gate Drivers

Application Notes

- 62 mm Module Mounting Guide
- 62 mm Module Thermal Interface Material Guide



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