# BFG10; BFG10/X

## **NPN 2 GHz RF power transistor**

Rev. 05 — 22 November 2007

**Product data sheet** 

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BFG10; BFG10/X

#### **FEATURES**

- · High power gain
- · High efficiency
- · Small size discrete power amplifier
- 1.9 GHz operating area
- Gold metallization ensures excellent reliability.

#### **APPLICATIONS**

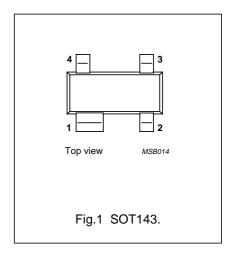
 Common emitter class-AB operation in hand-held radio equipment at 1.9 GHz.

#### **DESCRIPTION**

NPN silicon planar epitaxial transistor encapsulated in plastic, 4-pin dual-emitter SOT143 package.

#### **PINNING**

PIN	DESCRIPTION
BFG10 (se	e Fig.1)
1	collector
2	base
3	emitter
4	emitter
BFG10/X (	see Fig.1)
1	collector
2	emitter
3	base
4	emitter



#### **MARKING**

TYPE NUMBER	CODE
BFG10	%MS
BFG10/X	%MT

### **QUICK REFERENCE DATA**

RF performance at T<sub>amb</sub> = 25 °C in a common-emitter test circuit (see Fig.7).

MODE OF OPERATION	f (GHz)	V <sub>CE</sub> (V)	P <sub>L</sub> (mW)	G <sub>p</sub> (dB)	η <sub>c</sub> (%)
Pulsed, class-AB, duty cycle: < 1:8	1.9	3.6	200	≥5	≥50

#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	20	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	8	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	2.5	V
I <sub>C</sub>	collector current (DC)		_	250	mA
I <sub>C(AV)</sub>	average collector current		_	250	mA
P <sub>tot</sub>	total power dissipation	up to T <sub>s</sub> = 60 °C; see Fig.2; note 1	_	400	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
T <sub>j</sub>	junction temperature		_	175	°C

#### Note

1. T<sub>s</sub> is the temperature at the soldering point of the collector pin.

## NPN 2 GHz RF power transistor

BFG10; BFG10/X

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-s</sub>	thermal resistance from junction to	up to $T_s = 60 ^{\circ}\text{C}$ ; note 1;	290	K/W
	soldering point	$P_{tot} = 400 \text{ mW}$		

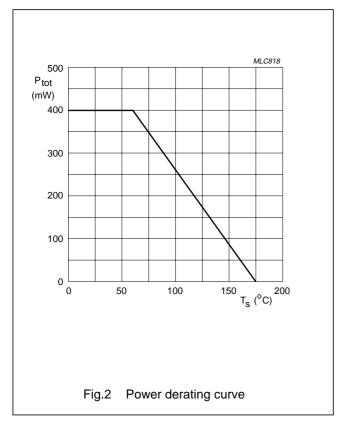
#### Note

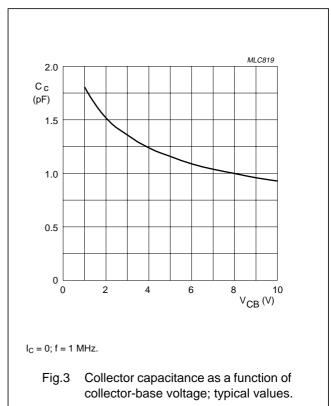
1.  $T_s$  is the temperature at the soldering point of the collector pin.

### **CHARACTERISTICS**

 $T_i = 25$  °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	open emitter; I <sub>C</sub> = 0.1 mA	20	_	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	open base; I <sub>C</sub> = 5 mA	8	_	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	open collector; I <sub>E</sub> = 0.1 mA	2.5	_	V
I <sub>CES</sub>	collector leakage current	V <sub>CE</sub> = 5 V; V <sub>BE</sub> = 0	_	100	μΑ
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = 50 mA; V <sub>CE</sub> = 5 V	25	_	
C <sub>c</sub>	collector capacitance	$I_E = i_e = 0$ ; $V_{CB} = 3.6 \text{ V}$ ; $f = 1 \text{ MHz}$	_	3	pF
C <sub>re</sub>	feedback capacitance	I <sub>C</sub> = 0; V <sub>CE</sub> = 3.6 V; f = 1 MHz	_	2	pF





## NPN 2 GHz RF power transistor

BFG10; BFG10/X

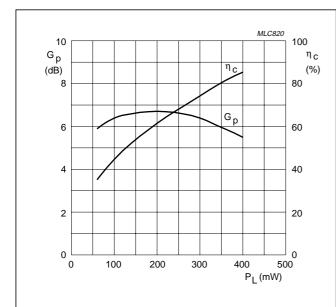
#### **APPLICATION INFORMATION**

RF performance at  $T_{amb}$  = 25 °C in a common-emitter test circuit (see Fig.7).

MODE OF OPERATION	f (GHz)	V <sub>CE</sub> (V)	I <sub>CQ</sub> (mA)	P <sub>L</sub> (mW)	G <sub>p</sub> (dB)	η <sub>c</sub> (%)
Pulsed, class-AB, duty cycle: < 1 : 8	1.9	3.6	1	200	>5 typ. 7	>50 typ. 60

### Ruggedness in class-AB operation

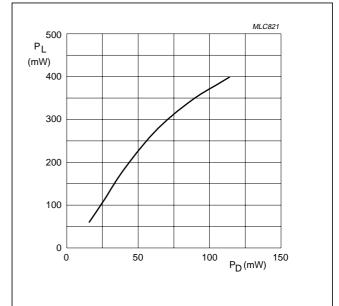
The BFG10 is capable of withstanding a load mismatch corresponding to VSWR = 8:1 through all phases, at rated output power under pulsed conditions up to a supply voltage of 7 V, f = 1.9 GHz and a duty cycle of 1:8.



Pulsed, class-AB operation.

 $V_{CE}$  = 3.6 V;  $V_{BE}$  = 0.65 V; f = 1.9 GHz; duty cycle < 1 : 8. Circuit optimized for  $P_L$  = 200 mW.

Fig.4 Power gain and efficiency as functions of load power; typical values.



Pulsed, class-AB operation.

 $V_{CE}=3.6$  V;  $V_{BE}=0.65$  V; f = 1.9 GHz; duty cycle < 1 : 8. Circuit optimized for  $P_L=200$  mW.

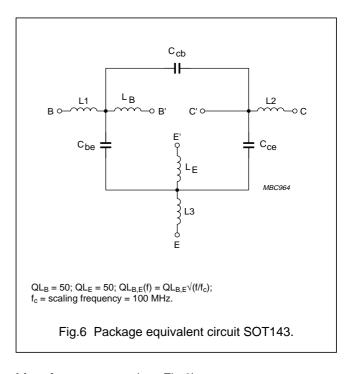
Fig.5 Load power as a function of drive power; typical values.

## NPN 2 GHz RF power transistor

BFG10; BFG10/X

## SPICE parameters for the BFG10 crystal

SEQUENCE No.	PARAMETER	VALUE	UNIT
1	IS	2.714	fA
2	BF	102.8	_
3	NF	0.998	_
4	VAF	28.12	V
5	IKF	6.009	Α
6	ISE	403.2	рА
7	NE	2.937	_
8	BR	31.01	_
9	NR	0.999	_
10	VAR	2.889	V
11	IKR	0.284	Α
12	ISC	1.487	fA
13	NC	1.100	_
14	RB	3.500	Ω
15	IRB	1.000	μΑ
16	RBM	3.500	Ω
17	RE	0.217	Ω
18	RC	0.196	Ω
19 <sup>(1)</sup>	XTB	0.000	_
20 <sup>(1)</sup>	EG	1.110	eV
21 <sup>(1)</sup>	XTI	3.000	_
22	CJE	5.125	pF
23	VJE	0.600	V
24	MJE	0.367	_
25	TF	12.07	ps
26	XTF	99.40	_
27	VTF	7.220	V
28	ITF	3.950	Α
29	PTF	0.000	deg
30	CJC	2.327	pF
31	VJC	0.668	V
32	MJC	0.398	_
33	XCJC	0.160	_
34 <sup>(1)</sup>	TR	0.000	ns
35 <sup>(1)</sup>	CJS	0.000	F
36 <sup>(1)</sup>	VJS	750.0	mV
37 <sup>(1)</sup>	MJS	0.000	_
38	FC	0.652	_



## List of components (see Fig.6)

DESIGNATION	VALUE	UNIT
C <sub>be</sub>	84	fF
C <sub>cb</sub>	17	fF
C <sub>ce</sub>	191	fF
L1	0.12	nH
L2	0.21	nH
L3	0.06	nH
L <sub>B</sub>	0.95	nH
LE	0.40	nH

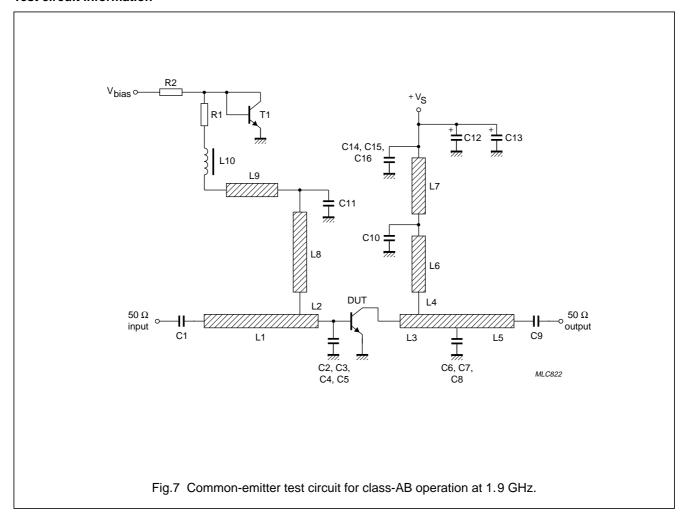
#### Note

1. These parameters have not been extracted, the default values are shown.

## NPN 2 GHz RF power transistor

BFG10; BFG10/X

#### **Test circuit information**



## NPN 2 GHz RF power transistor

BFG10; BFG10/X

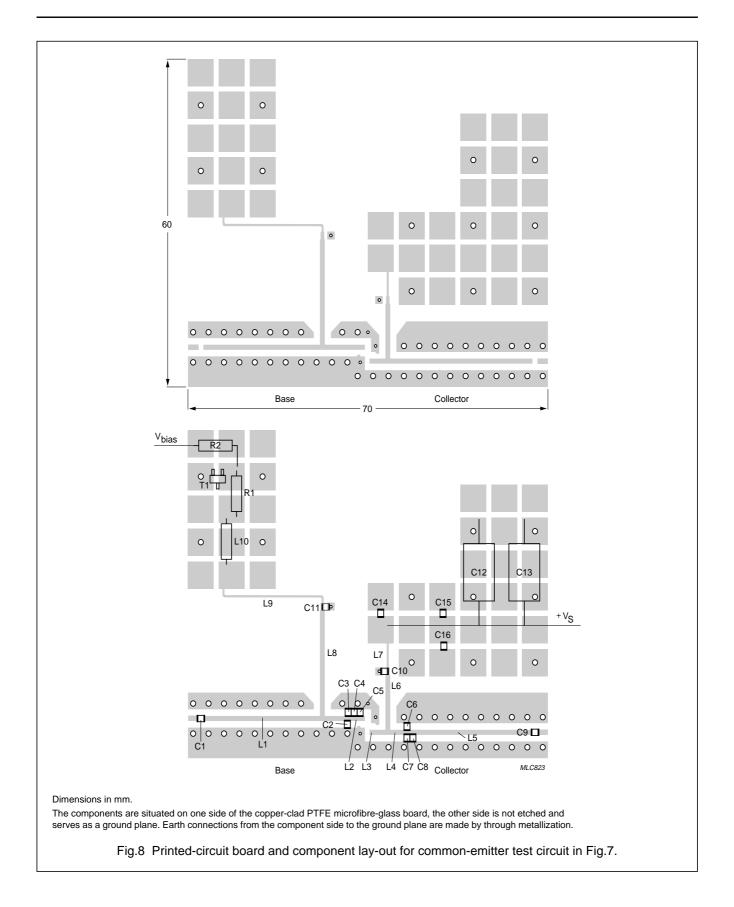
## List of components used in test circuit (see Fig.7)

COMPONENT	DESCRIPTION	VALUE	DIMENSIONS	CATALOGUE No.
C1, C9, C10, C11	multilayer ceramic chip capacitor; note 1	24 pF		
C2, C3, C4, C5, C6, C7	multilayer ceramic chip capacitor; note 1	0.86 pF		
C8	multilayer ceramic chip capacitor; note 1	1.1 pF		
C12, C13	electrolytic capacitor	470 μF; 10 V		2222 031 34471
C14, C15, C16	multilayer ceramic chip capacitor; note 1	10 nF		
L1	stripline; note 2		length 28.5 mm width 0.93 mm	
L2	stripline; note 2		length 2.3 mm width 0.93 mm	
L3	stripline; note 2		length 3.1 mm width 0.93 mm	
L4	stripline; note 2		length 3.3 mm width 0.93 mm	
L5	stripline; note 2		length 16.3 mm width 0.93 mm	
L6	stripline; note 2		length 10 mm width 0.93 mm	
L7	stripline; note 2		length 4.4 mm width 0.4 mm	
L8	stripline; note 2		length 19.3 mm width 0.93 mm	
L9	stripline; note 2		length 19.7 mm width 0.4 mm	
L10	micro choke			
T1	BD228			
R1	metal film resistor	20 Ω; 0.4 W		2322 157 10209
R2	metal film resistor	530 Ω; 0.4 W		2322 157 15301

## Notes

- 1. American Technical Ceramics (ATC) capacitor, type 100A or other capacitor of the same quality.
- 2. The striplines are on a  $\frac{1}{32}$  inch double copper-clad printed-circuit board with PTFE fibre-glass dielectric ( $\epsilon_r = 6$ ).

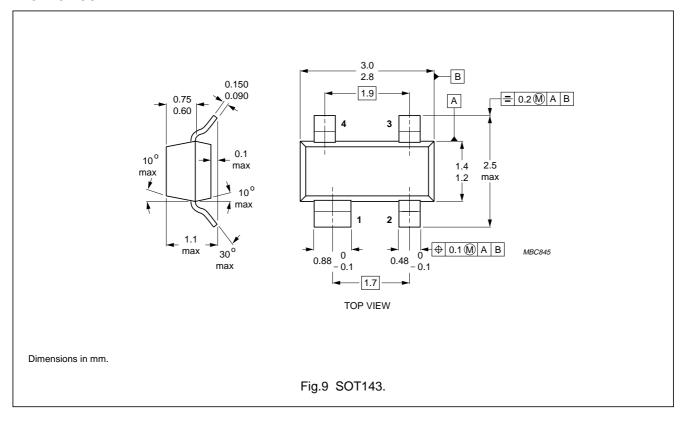
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BFG10; BFG10/X

### **PACKAGE OUTLINE**



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#### **Data sheet status**

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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## **Revision history**

### **Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes
BFG10X_N_5	20071122	Product data sheet	-	BFG10X_4
Modifications:	<ul> <li>Marking tabl</li> </ul>	e on page 2; changed code		
BFG10X_4	19950831	Product specification	-	BFG10X_3
BFG10X_3	19950307	-	-	BFG10X_2
BFG10X_2	-	-	-	BFG10X_1
BFG10X_1	-	-	-	-

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