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### FAIRCHILD

SEMICONDUCTOR TM

# DM7446A, DM7447A BCD to 7-Segment Decoders/Drivers

#### **General Description**

The DM7446A and DM7447A feature active-LOW outputs designed for driving common-anode LEDs or incandescent indicators directly. All of the circuits have full ripple-blanking input/output controls and a lamp test input. Segment identification and resultant displays are shown on a following page. Display patterns for BCD input counts above nine are unique symbols to authenticate input conditions.

All of the circuits incorporate automatic leading and/or trailing-edge, zero-blanking control (RBI and RBO). Lamp test (LT) of these devices may be performed at any time when the BI/RBO node is at a HIGH logic level. All types contain an overriding blanking input (BI) which can be used to control the lamp intensity (by pulsing) or to inhibit the outputs.

#### **Features**

- All circuit types feature lamp intensity modulation capability
- Open-collector outputs drive indicators directly
- Lamp-test provision
- Leading/trailing zero suppression

#### **Ordering Code:**

Order Number	Package Number	Package Description
DM7446AN	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide
DM7447AN	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide

#### **Connection Diagram**



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#### **Function Table**

Decimal or			Inp	uts			BI/RBO			C	Output	s			Note
Function	LT	RBI	D	С	В	Α	(Note 1)	а	b	С	d	е	f	g	Note
0	Н	Н	L	L	L	L	Н	L	L	L	L	L	L	Н	
1	н	х	L	L	L	н	н	н	L	L	н	н	н	н	
2	Н	Х	L	L	Н	L	Н	L	L	Н	L	L	Н	L	
3	н	Х	L	L	н	н	Н	L	L	L	L	н	н	L	
4	Н	Х	L	Н	L	L	Н	Н	L	L	Н	Н	L	L	
5	н	Х	L	н	L	н	Н	L	н	L	L	н	L	L	
6	Н	Х	L	Н	Н	L	Н	Н	Н	L	L	L	L	L	
7	Н	Х	L	н	н	Н	н	L	L	L	Н	Н	н	Н	(Note 2)
8	Н	Х	Н	L	L	L	Н	L	L	L	L	L	L	L	
9	Н	Х	Н	L	L	Н	н	L	L	L	Н	Н	L	L	
10	Н	Х	Н	L	Н	L	Н	Н	Н	Н	L	L	Н	L	
11	Н	Х	н	L	н	Н	Н	н	н	L	L	н	н	L	
12	Н	Х	Н	Н	L	L	Н	Н	L	Н	Н	Н	L	L	
13	Н	Х	н	н	L	Н	Н	L	н	н	L	н	L	L	
14	Н	Х	Н	Н	Н	L	Н	Н	Н	Н	L	L	L	L	
15	Н	Х	Н	н	н	Н	н	Н	н	Н	Н	Н	н	Н	
BI	Х	Х	Х	Х	Х	Х	L	Н	Н	Н	Н	Н	Н	Н	(Note 3)
RBI	Н	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	(Note 4)
LT	L	Х	Х	Х	Х	Х	Н	L	L	L	L	L	L	L	(Note 5)

 $H = HIGH \text{ level}, \qquad L = LOW \text{ level}, \qquad X = Don't \text{ Care}$ 

Note 1: BI/RBO is a wire-AND logic serving as blanking input (BI) and/or ripple-blanking output (RBO).

Note 2: The blanking input (BI) must be OPEN or held at a HIGH logic level when output functions 0 through 15 are desired. The ripple-blanking input (RBI) must be OPEN or HIGH if blanking of a decimal zero is not desired.

Note 3: When a LOW logic level is applied directly to the blanking input (BI), all segment outputs are HIGH regardless of the level of any other input.

Note 4: When ripple-blanking input (RBI) and inputs A, B, C, and D are at a LOW level with the lamp test input HIGH, all segment outputs go H and the ripple-blanking output (RBO) goes to a LOW level (response condition).

Note 5: When the blanking input/ripple-blanking output (BI/RBO) is OPEN or held HIGH and a LOW is applied to the lamp-test input, all segment outputs are L.

#### Logic Diagram



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#### Absolute Maximum Ratings(Note 6)

Supply Voltage	7V
Input Voltage	5.5V
Operating Free Air Temperature Range	$0^{\circ}C$ to $+70^{\circ}C$
Storage Temperature Range	$-65^{\circ}C$ to $+150^{\circ}C$

Note 6: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

# **Recommended Operating Conditions**

Symbol	Parameter	Min	Nom	Max	Units
DM7446A			•		
V <sub>CC</sub>	Supply Voltage	4.75	5	5.25	V
V <sub>IH</sub>	HIGH Level Input Voltage	2			V
V <sub>IL</sub>	LOW Level Input Voltage			0.8	V
V <sub>OH</sub>	HIGH Level Output Voltage (a thru g)			30	V
I <sub>OH</sub>	HIGH Level Output Current (BI/RBO)			-0.2	μΑ
I <sub>OL</sub>	LOW Level Output Current (a thru g)			40	mA
I <sub>OL</sub>	LOW Level Output Current (BI/RBO)			8	mA
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C
DM7447A	- <b>-</b>				
V <sub>CC</sub>	Supply Voltage	4.75	5	5.25	V
V <sub>IH</sub>	HIGH Level Input Voltage	2			V
V <sub>IL</sub>	LOW Level Input Voltage			0.8	V
V <sub>OH</sub>	HIGH Level Output Voltage (a thru g)			15	V
I <sub>OH</sub>	HIGH Level Output Current (BI/RBO)			-0.2	μΑ
I <sub>OL</sub>	LOW Level Output Current (a thru g)			40	mA
I <sub>OL</sub>	LOW Level Output Current (BI/RBO)			8	mA
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C

# DM7446A, DM7447A

Symbol	Parameter	perature range (unless otherwise	itions	Min	Typ (Note 7)	Max	Unite
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -12 mA$				-1.5	V
V <sub>OH</sub> HIGH Level Output		V <sub>CC</sub> = Min	2.4	3.7		v	
	Voltage (BI/RBO)	I <sub>OH</sub> = Max	I <sub>OH</sub> = Max				v
I <sub>CEX</sub>	HIGH Level Output	$V_{CC} = Max, V_O = 30V$				250	μA
	Current (a thru g)	$V_{IL} = Max, V_{IH} = Min$			250	μΛ	
V <sub>OL</sub>	LOW Level	V <sub>CC</sub> = Min, I <sub>OL</sub> = Max			0.3	0.4	v
	Output Voltage	$V_{IH} = Min, V_{IL} = Max$			0.5	0.4	v
I <sub>I</sub>	Input Current @ Max	$V_{CC} = Max, V_I = 5.5V$				1	mA
	Input Voltage	(Except BI/RBO)				I	IIIA
I <sub>IH</sub>	HIGH Level Input	$V_{CC} = Max, V_I = 2.4V$				40	μA
	Current	(Except BI/RBO)				40	μΑ
IIL	LOW Level Input	V <sub>CC</sub> = Max	BI/RBO			-4	mA
	Current	$V_I = 0.4V$	Others			-1.6	
I <sub>OS</sub>	Short Circuit	V <sub>CC</sub> = Max	•	1		-4	mA
	Output Current	(BI/RBO)				-4	mA
I <sub>CC</sub>	Supply Current	V <sub>CC</sub> = Max			60	103	mA
		(Note 8)			00	103	mA

Note 7: All typicals are at V<sub>CC</sub> = 5V,  $T_A = 25^{\circ}C$ .

Note 8:  $\mathrm{I}_{\mathrm{CC}}$  is measured with all outputs OPEN and all inputs at 4.5V.

# **DM7446A Switching Characteristics**

at  $V_{CC}=5V$  and  $T_A=25^\circ C$ 

Symbol	Parameter	Conditions	Min	Max	Units
t <sub>PLH</sub>	Propagation Delay Time LOW-to-HIGH Level Output	$C_L = 15 \text{ pF}$ $R_L = 120\Omega$		100	ns
t <sub>PHL</sub>	Propagation Delay Time HIGH-to-LOW Level Output			100	ns



Symbol	Parameter	Cond	litions	Min	Typ (Note 9)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -12 \text{ mA}$			-1.5	V	
V <sub>OH</sub>	HIGH Level Output Voltage (BI/RBO)	V <sub>CC</sub> = Min I <sub>OH</sub> = Max	2.4	3.7		V	
ICEX	HIGH Level Output Current (a thru g)	$V_{CC} = Max, V_O = 15V$ $V_{IL} = Max, V_{IH} = Min$			250	μΑ	
V <sub>OL</sub>	LOW Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min, V_{IL} = Max$		0.3	0.4	V	
I <sub>I</sub>	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$			1	mA	
I <sub>IH</sub>	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.4V$				40	μA
IIL	LOW Level Input Current	$V_{CC} = Max$ $V_I = 0.4V$	BI/RBO Others			-4 -1.6	mA
I <sub>OS</sub>	Short Circuit Output Current	V <sub>CC</sub> = Max (BI/RBO)			-4	mA	
I <sub>CC</sub>	Supply Current	V <sub>CC</sub> = Max (Note 10)		60	103	mA	

Note 9: All typicals are at  $V_{CC} = 5V$ ,  $T_A = 25^{\circ}C$ .

Note 10:  $\mathrm{I}_{\mathrm{CC}}$  is measured with all outputs OPEN and all inputs at 4.5V.

# DM7447A Switching Characteristics

at  $V_{CC} = 5V$  and  $T_A = 25^{\circ}C$ 

Symbol	Parameter	Conditions	Min	Max	Units
t <sub>PLH</sub>	Propagation Delay Time	C <sub>L</sub> = 15 pF		100	ns
	LOW-to-HIGH Level Output	$R_L = 120\Omega$		100	115
t <sub>PHL</sub>	Propagation Delay Time			100	ns
	HIGH-to-LOW Level Output			100	115



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