

Product brief

EiceDRIVER™ 1EDN7550 and 1EDN8550

1-channel non-isolated gate-driver IC family with truly differential inputs Prevents false triggering of power MOSFETs and enables highest power density

Overview:

Challenge

1EDNx550 non-isolated gate-driver ICs are available in small 6-pin SOT-23 and TSNP packages. Their truly differential inputs enable cost-effective solutions with exceptional power density in:

- > Boost-PFC with Kelvin-source MOSFET
- > Synchronous rectification stages
- > Designs with long distance between control IC and gate-driver IC
- > Buck-boost converters

Parasitic ground inductance

Parasitic source inductance

Ultimate power density

- > Low- and medium-voltage half-bridges
- > High density 48 V to 12 V intermediate bus converter

Key features

- Configurable common-mode robustness
- Separate low impedance outputs:
 Source: 4 A/0.85 Ω
 - Sink: 8 A/0.325 Ω
- > +10 ns/-7 ns propagation delay
- > 4 V/8 V UVLO options
- > 6-pin package options:
 - SOT-23: 2.9 mm x 2.8 mm
 - TSNP: 1.5 mm x 1.1 mm

| 1EDNx550 | advantages |
|----------|------------|
|----------|------------|

- > Short R&D time
- > Cost-effectiveness
- > Highest level of power density
- > Design flexibility
- > Increased power density

The common-mode robustness is configurable with resistors connected to the differential inputs:

Application example

- 1- and 2-layer PCBs

Hard-switching applications

- Synchronous rectification

Boost-PFC with Kelvin-source MOSFETs

> 48 V to 12 V intermediate bus conveter

| In put voltage | Common-mode resistor | | | DC common-mode robustness | AC common-mode robustness | | |
|----------------|----------------------|----------|-------------|---------------------------|---------------------------|--|--|
| Input voltage | value | accuracy | form factor | DC common-mode robustness | AC common-mode robustness | | |
| 2.5 V | 24 kΩ | 0.1% | ≥0402 | -54 V / +63 V | ±150 V | | |
| 3.3 V | 33 kΩ | 0.1% | ≥0603 | -72 V / +84 V | ±150 V | | |
| 5 V | 51 kΩ | 1% | ≥0603 | -60 V / +60 V | ±150 V | | |
| 5 V | 51 kΩ | 0.1% | ≥0805 | -108 V / +126 V | ±200 V | | |
| 12 V | 127 kΩ | 0.1% | ≥1206 | -200 V / +200 V | ±400 V | | |

> Long distance between controller IC and gate-driver IC

- Control IC on daughter card, e.g., interleaved PFC

Cascaded switched capacitor topology at 1.2 MHz

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Typical application for 4-pin Kelvin source CoolMOS[™] with 1EDN8550B as a low-side gate driver



Product portfolio

| Part number | Package | UVLO | OPN | IN- | 1 | 6 | OUT_SNK | |
|-------------|--------------|------|----------------|-----|------------|----------|---------|--|
| 1EDN7550B | 6-pin SOT-23 | 4 V | 1EDN7550BXTSA1 | GND | 2 1EDN7550 | 5 | OUT_SRC | |
| 1EDN8550B | 6-pin SOT-23 | 8 V | 1EDN8550BXTSA1 | IN+ | | 1EDN8550 | 18550 | |
| 1EDN7550U | 6-pin TSNP | 4 V | 1EDN7550UXTSA1 | | | 4 | VDD | |

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