

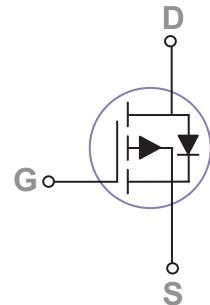
P-Channel Enhancement Mode Power MOSFET

Description

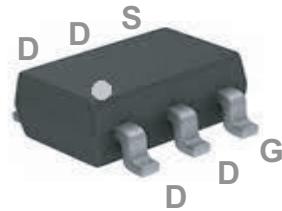
The RM5A1P30S6 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

General Features

- $V_{DS} = -30V, I_D = -5.1A$
- $R_{DS(ON)} < 46m\Omega @ V_{GS}=-4.5V$
- $R_{DS(ON)} < 32m\Omega @ V_{GS}=-10V$
- High power and current handling capability
- Lead free product is acquired
- Surface mount package



Schematic diagram



SOT-23-6 top view

Application

- PWM applications
- Load switch
- Power management
- P/N suffix V means AEC-Q101 qualified, e.g:RM5A1P30S6V
- Halogen-free

Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
5A1P30	RM5A1P30S6	SOT-23-6	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	-5.1	A
Drain Current-Pulsed (Note 1)	I_{DM}	-20.4	A
Maximum Power Dissipation	P_D	1.56	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	80	°C /W
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Electrical Characteristics ($T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V I_D=-250\mu A$	-30	-	-	V

Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-24V, V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±10V, V _{DS} =0V	-	-	±100	nA
On Characteristics ^(Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-1.2	-1.6	-2.2	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-4A	-	27	32	mΩ
		V _{GS} =-4.5V, I _D =-2A	-	38	46	mΩ
Forward Transconductance	g _{FS}	V _{DS} =-10V, I _D =-3A	-	9	-	S
Dynamic Characteristics ^(Note 4)						
Input Capacitance	C _{iss}	V _{DS} =-15V, V _{GS} =0V, F=1.0MHz	-	757	1280	PF
Output Capacitance	C _{oss}		-	122	210	PF
Reverse Transfer Capacitance	C _{rss}		-	88	175	PF
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =-15V, I _D =-1A V _{GS} =-10V, R _{GEN} =6Ω	-	4.6	9	nS
Turn-on Rise Time	t _r		-	14	26	nS
Turn-Off Delay Time	t _{d(off)}		-	34	58	nS
Turn-Off Fall Time	t _f		-	18	35	nS
Total Gate Charge	Q _g	V _{DS} =-15V, I _D =-5A, V _{GS} =-4.5V	-	8	15	nC
Gate-Source Charge	Q _{gs}		-	3.3	6	nC
Gate-Drain Charge	Q _{gd}		-	2.3	5	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage ^(Note 3)	V _{SD}	V _{GS} =0V, I _S =-1A	-	-	-1	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production

RATING AND CHARACTERISTICS CURVES (RM5A1P30S6)

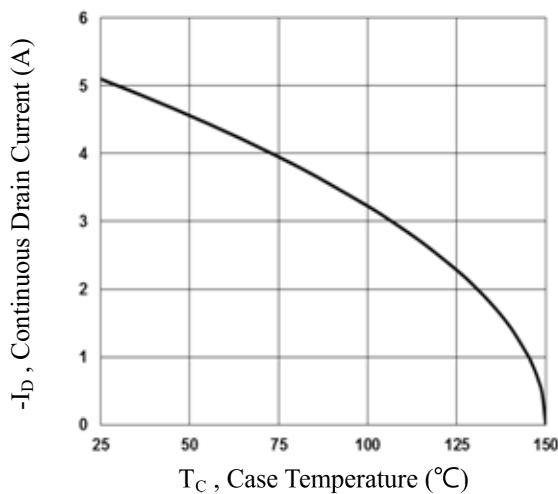


Fig.1 Continuous Drain Current vs. T_c

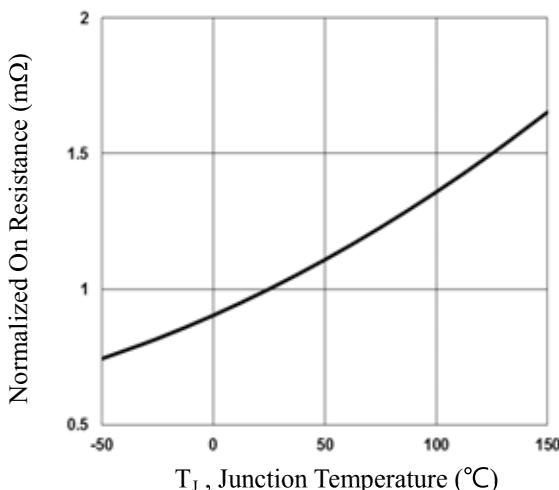


Fig.2 Normalized RDSON vs. T_j

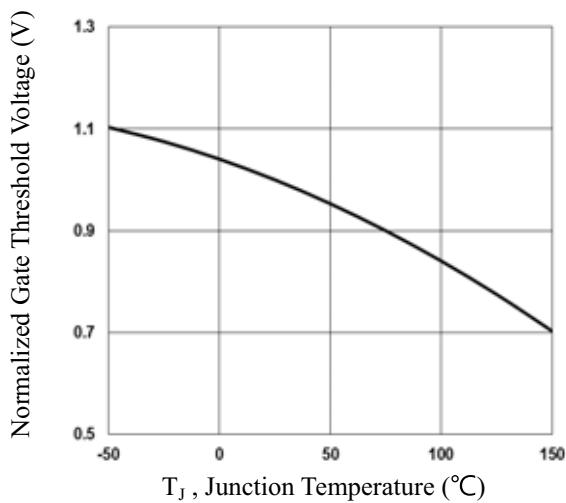


Fig.3 Normalized V_{th} vs. T_j

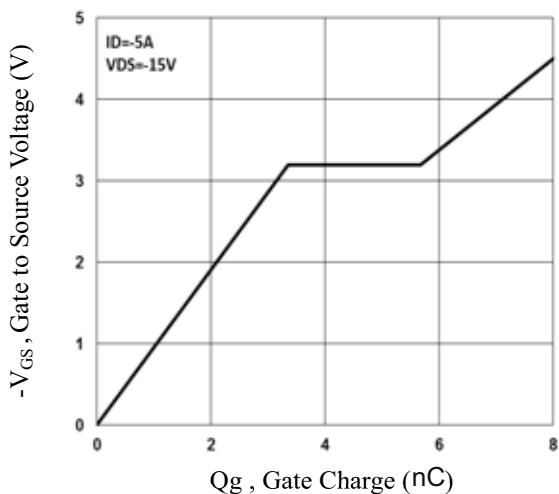


Fig.4 Gate Charge Waveform

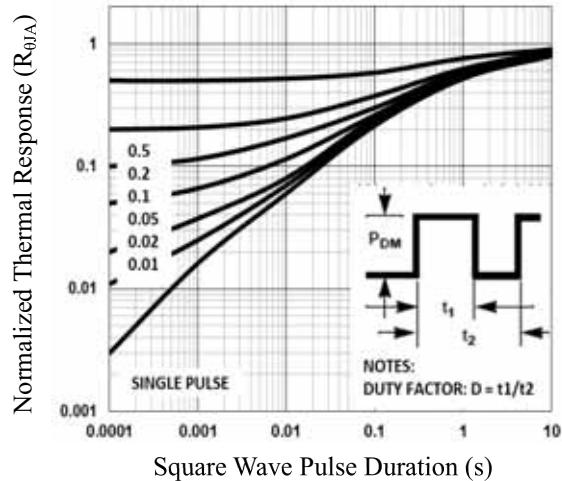


Fig.5 Normalized Transient Impedance

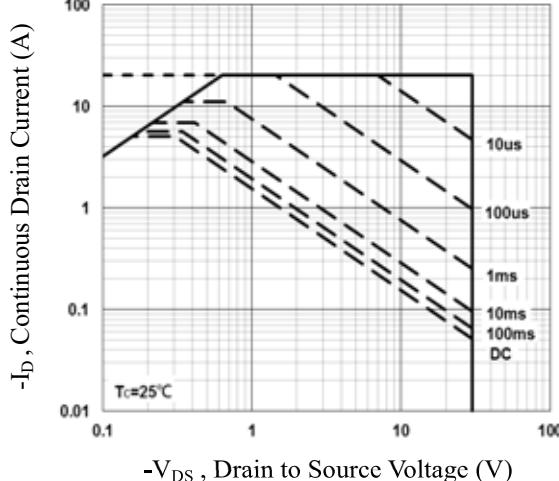


Fig.6 Maximum Safe Operation Area

RATING AND CHARACTERISTICS CURVES (RM5A1P30S6)

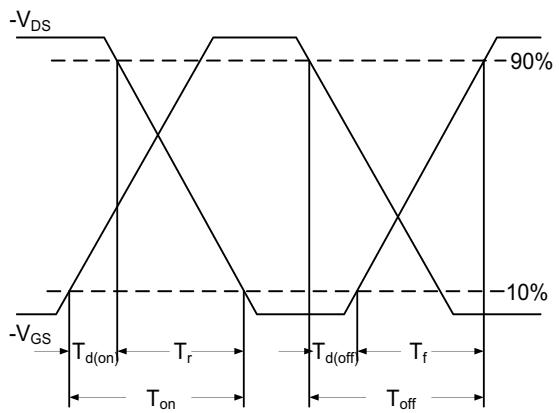


Fig.7 Switching Time Waveform

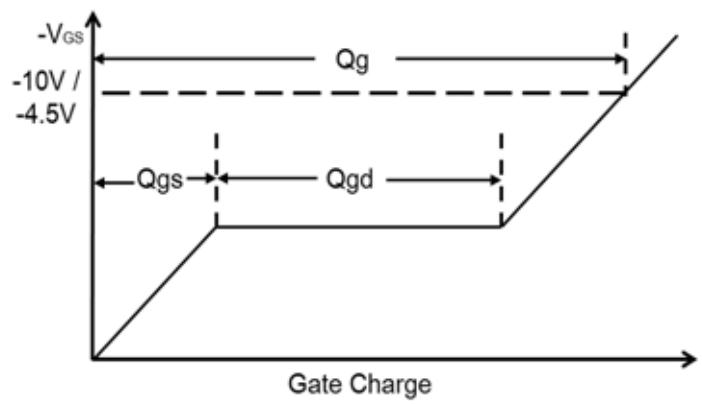
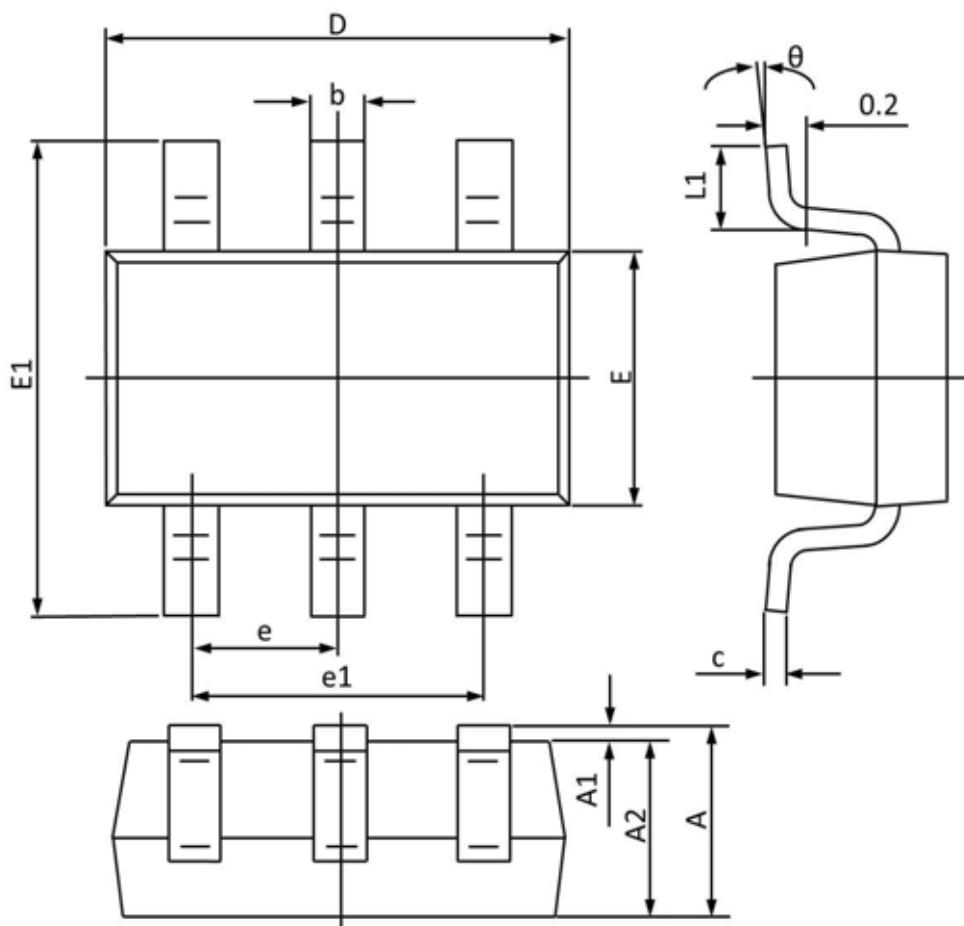


Fig.8 Gate Charge Waveform

SOT23-6 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	1.450	-	0.057	-
A1	0.100	0.000	0.004	0.000
A2	1.300	1.050	0.051	0.041
b	0.500	0.300	0.020	0.012
c	0.200	0.100	0.008	0.004
D	3.100	2.700	0.122	0.106
E	1.800	1.400	0.071	0.055
E1	3.000	2.600	0.118	0.102
e	0.95BSC		0.037BSC	
e1	2.000	1.800	0.079	0.071
L1	0.600	0.300	0.024	0.012
θ	10°	0°	10°	0°

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