

Figure 1. Photo of ATIA202KY

#### **FEATURES**

Isolated Power Outputs

Small Size: 4 Channels/Inch Low

Uncommitted Input Amplifier

**○** High CMR: 130dB (Gain = 100V/V)

➡ High Accuracy: ±0.01% Max Nonlinearity

⇒ High CMV Isolation: ±2000V Continuous

#### APPLICATIONS

It can be applied for multichannel data acquisition, current shunt measurements motor controls, process signal isolation, high voltage instrumentation amplifier, etc.

#### DESCRIPTION

## **Upgraded Drop-in Replacement for AD202KY**

The ATIA202KY is a high voltage isolation amplifier designed for multiple applications where input signals are measured, processed, or transmitted without a galvanic connection. These isolation amplifiers in SIP package offer a signal and power isolation function.

With internal transformer-coupling, the ATIA202KY provides total galvanic isolation between the input and output stages of the isolation amplifier. These amplifiers eliminate the need for an external DC-DC converter, which allows the designer to minimize the necessary circuit overhead, thus reducing the overall design and component costs.

The ATIA202KY is powered directly from a 15V DC power supply, featuring small size, high accuracy, low power, wide bandwidth, excellent performance, flexible input, isolated power, etc.

#### **INSIDE THE ATIA202KY**

The ATIA202KY uses an amplitude modulation technique to permit transformer coupling of signals down to dc (Figure 2). It also contains an uncommitted input op amp and a power transformer that provides isolated power to the op amp, the modulator, and any external load. The power transformer primary is driven by a 20kHz, 15V<sub>P-P</sub> square wave generated internally.

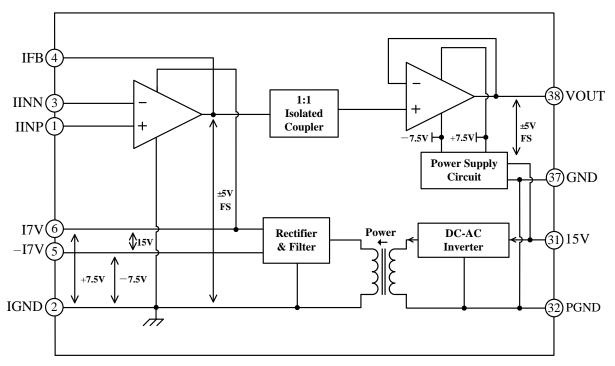


Figure 2. ATIA202KY Functional Block Diagram



# **SPECIFICATIONS**

Table 1. Electrical characteristics. (Typical @ 25  $^{\circ}$ C and  $V_S = 15V$  unless otherwise noted.)

Model	ATIA202KY	
GAIN		
Range	1V/V-100 V/V	
Error	±0.5% typ ( ±4% max)	
vs. Temperature	±20ppm/℃ typ (±45ppm/℃ max)	
vs. Time	±50 ppm/1000 Hours	
vs. Supply Voltage	±0.01%/V	
Nonlinearity ( $G = 1V/V$ )	±0.01 max	
Nonlinearity vs. Isolated Supply Load	±0.0015%/mA	
	±5.0013 /0/11111	
INPUT VOLTAGE RATINGS	.577	
Input Voltage Range	±5V	
Max Isolation Voltage (Input to Output)	150077	
AC, 60Hz, Continuous	1500Vms	
Continuous (AC and DC)	±2000V Peak	
CMRR (Common-Mode Rejection Ratio)*	-74dB	
CMTC(Common-Mode Transfer Coefficient)*	$-0.2 \times 10^3$	
$RS \le 100\Omega$ (HI and LO Inputs) $G = 1V/V$	105dB	
G = 100V/V	130dB	
RS $\leq 1 \text{ k}\Omega$ (Input HI, LO, or Both) G = $1\text{V/V}$	100dB min	
G = 100V/V	110dB min	
Leakage Current Input to Output @ 240Vrms, 60 Hz	2μA rms max	
INPUT IMPEDANCE		
Differential ( $G = 1V/V$ )	$10^{12}\Omega$	
,		
Common-Mode	2GΩl4.5pF	
INPUT BIAS CURRENT		
Initial, @ 25 ℃	±30pA	
vs. Temperature (0 $^{\circ}$ C to 70 $^{\circ}$ C)	±10nA	
INPUT DIFFERENCE CURRENT		
Initial, @ 25 ℃	±5pA	
vs. Temperature (0 $^{\circ}$ C to 70 $^{\circ}$ C)	±2nA	
-		
INPUT NOISE	10 1/	
Voltage, 0.1Hz to 10Hz	$1.8 \mu V_{P-P}$	
f > 100Hz	$10.8 \text{nV}/\sqrt{\text{Hz}}$	
FREQUENCY RESPONSE		
Bandwidth ( $V_0 \le 10V_{P-P}$ , $G = 1V - 50V/V$ )	20kHz	
Settling Time, to $\pm 10$ mV (10V Step)	1ms	
OFFSET VOLTAGE (RTI)		
Initial, @ 25 °C Adjustable to Zero	$(\pm 5 \pm 5/G)$ mV max	
•	`	
vs. Temperature (0 $^{\circ}$ C to 70 $^{\circ}$ C)	$[\pm 10 \pm \frac{10}{G}] \mu V/ C$	
RATED OUTPUT	-	
	±5V	
Voltage (Out HI to Out LO)		
Voltage at Out HI or Out LO	±6.5V	
Output Resistance	7kΩ	
Output Ripple, 100kHz Bandwidth	$10 \text{mV}_{\text{P-P}}$	
5kHz Bandwidth	0.5mV rms	
ISOLATED POWER OUTPUT		
Voltage, No Load	±7.5V	
Accuracy	±10%	
Current	400 µA Total	
Regulation, No Load to Full Load	5%	
Ripple	100mV <sub>P-P</sub>	
• • • • • • • • • • • • • • • • • • • •	- 50m · FaF	
POWER SUPPLY	1537 .50/	
Voltage, Rated Performance	15V±5%	
Voltage, Operating	15V±10%	
Current, No Load ( $V_S = 15V$ )	5mA	
TEMPERATURE RANGE		
Rated Performance	0 ℃ to 70 ℃	
Operating	-40 °C to +85 °C	
Storage	-40 °C to +85 °C	
PACKAGE DIMENSIONS		
	2.08">0.250">0.625"	
SIP Package (N)	2.08"×0.250"×0.625"	

<sup>\*</sup>Test Schematic Figure 3 @ 100Hz Sine Wave @ $v_S(t) = 1000V$ .

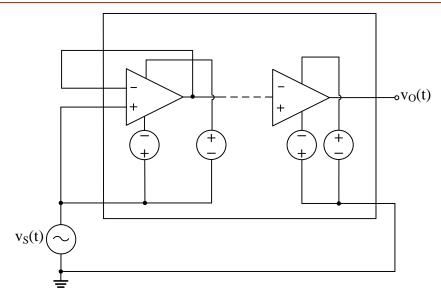


Figure 3. CMRR & CMTC Test Schematic

# PIN DESIGNATIONS

Block	Pin#	Pin Name	Туре	Function Description
	1	IINP	Isolated analog input	Isolated positive (Non-inverting) input
	2	IGND	Isolated analog ground	Isolated ground
	3	IINN	Isolated analog input	Isolated negative (inverting) input
Isolated	4	IFB	Isolated analog output	Isolated op amp output as a feedback signal
Block	5	5 -I7V	Isolated power output	Isolated negative power supply output, approximately -7.0V,
	3			referenced to pin 2 IGND
	6	171/	I7V Isolated power output	Isolated positive power supply output, +7.5V, referenced to
	0	1 / V		pin 2 IGND
Local Block	31	15V	Analog input	Positive 15V power supply input
	32	PGND	Analog input	Power supply return, internally connected to pin 18 LO
	37	GND	Analog ground	Output voltage ground reference, internally connected to pin 22 PGND
	38	VOUT	Analog output	Op amp output, equals to the voltage difference between IFB and IGND



# MECHANICAL DIMENSIONS

The dimensions of ATIA202KY in SIP package are shown in Figure 3.

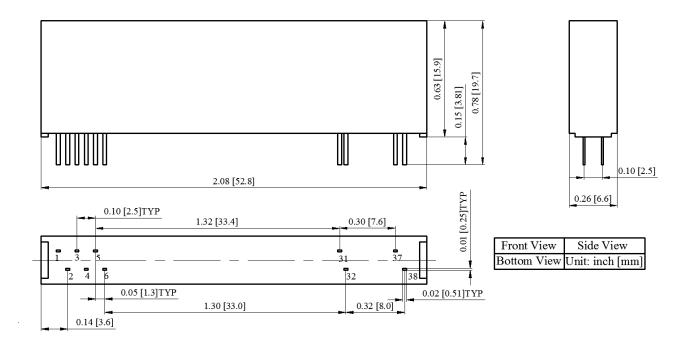


Figure 3. Dimensions of ATIA202KY SIP Package

# High Voltage Isolation Amplifier



ATIA202KY

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