Spec No.TQ3C-8EAF0-E1YAH40-01DateOctober 16, 2014

TYPE : TCG104SVLPAAFA-AA20

< 10.4 inch SVGA transmissive color TFT with LED backlight, constant current circuit for LED backlight and touch panel>

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KYOCERA DISPLAY CORPORATION

This specification is subject to change without notice. Consult Kyocera before ordering.

ſ	Original	Designed by:	Engineering de	pt.	Confirmed by:	QA dept.
	Issue Date	Prepared	Checked	Approved	Checked	Approved
	July 13, 2012	K. Janimuka	Y. Yamazaki	W. Yano	0. Sato	1. Haman S



SPEC

Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



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1. Application

This document defines the specification of TCG104SVLPAAFA-AA20. (RoHS Compliant)

2. Construction and outline

LCD	: Transmissive color dot matrix type TFT
Backlight system	: LED
Polarizer	Anti-Glare treatment
Interface	: LVDS
Additional circuit	: Timing controller, Power supply (3.3V input) Constant current circuit for LED Backlight(12V input)
Touch panel	: Analog type, Anti-Glare treatment

3. Mechanical specifications

3-1. LCD

Item	Specification	Unit
Outline dimensions 1)	240.7(W)×(180.2)(H)×10.8(D)	mm
Active area	211.2(W)×158.4(H) (26.4cm/10.4 inch(Diagonal))	mm
Dot format	800×(R,G,B)(W)×600(H)	dot
Dot pitch	0.088(W)×0.264(H)	mm
Base color 2)	Normally White	-
Mass	585	g

1) Projection not included. Please refer to outline for details.

2) Due to the characteristics of the LCD material, the color varies with environmental temperature.

3-2. Touch panel

Item	Specification	Unit
Input	Radius-0.8 stylus or Finger	-
Actuation Force	$0.05 \sim 0.8$	Ν
Transmittance	Тур. 80	%
Surface hardness	Pencil hardness 2H or more according	-

4. Absolute maximum ratings

	Item	Symbol	Min.	Max.	Unit	
Supply volta	V _{DD}	-0.3	4.0	V		
Supply volta	VIN	-0.3	14.0	V		
	RxINi+, RxINi-(i=0,1,2,3)	1)	VII	-0.3	2.8	V
Input signal	CK IN+, CK IN-	V _{I2}	-0.3	2.8	V	
voltage	SELLVDS		V _{I3}	-0.3	V_{DD} +0.5	V
	BLBRT, BLEN		V _{I4}	-0.3	VIN	V
Supply voltage for touch panel		V_{TP}	0	6	V	
Input curren	Input current of touch panel			0	0.5	mA

4-1. Electrical absolute maximum ratings

1) V_{DD} must be supplied correctly within the range described in 5-1.

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	1)	Top	-20	70	°C
Storage temperature	2)	Tsto	-30	80	°C
Operating humidity	3)	Hop	10	4)	%RH
Storage humidity	3)	Hsto	10	4)	%RH
Vibration		-	5)	5)	-
Shock		-	6)	6)	-

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Temp. = -30°C<48h, Temp. = 80°C<168h
 Store LCD at normal temperature/humidity. Keep them free from vibration and shock.
 An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.
 (Please refer to "Precautions for Use" for details.)
- 3) Non-condensing
- 4) Temp.≦40°C, 85%RH Max. Temp.>40°C, Absolute humidity shall be less than 85%RH at 40°C.
- 5)

Frequency	$10{\sim}55~{ m Hz}$	Acceleration value
Vibration width	0.15mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

 6) Acceleration: 490 m/s², Pulse width: 11 ms 3 times in each direction: ±X, ±Y, ±Z EIAJ ED-2531



5. Electrical characteristics

5-1. LCD

						Temp. =	-20~70°C
Item		Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage	1)	V _{DD}	-	3.0	3.3	3.6	V
Current consumption		Idd	2)	-	270	330	mA
Permissive input ripple volt	age	V_{RP}	V _{DD} =3.3V	-	-	100	mVp-p
T 4 . 1 14	0)	VIL	"Low" level	0	-	0.8	V
Input signal voltage	3)	VIH	"High" level	2.0	-	V_{DD}	V
To solve a la constant		Iol	V _{I3} =0V	-10	-	10	μ A
Input reek current		Іон	V13=3.3V	-	-	400	μ A
LVDS Input voltage	4)	VL	-	0	-	1.9	V
Differential input voltage	4)	VID	-	250	350	450	mV
Differential input	4) 5)	V_{TL}	"Low" level	Vсм-100	-	-	mV
threshold voltage	4) 3)	VTH	"High" level	-	-	V _{CM} +100	mV
Terminator		\mathbf{R}_1	-	-	100	-	Ω
		t1	-	0.1	-	10	ms
		t2	-	0	-	-	ms
		t3	-	0	-	-	ms
X 7 , 1·,·	1)	t4	-	1.0	-	-	s
V _{DD} -turn-on conditions	1)	t5	-	200		-	ms
		t6	-	200	-	-	ms
		t7	-	0	-	10	s
		t8	-	0	-	-	ms

1) V_{DD}-turn-on conditions





2) Display pattern:

```
V_{DD} = 3.3V, Temp. = 25°C
```



- 3) Input signal : SELLVDS
- 4) Input signal : RxIN3+, RxIN3-, RxIN2+, RxIN2-, RxIN1+, RxIN1-, RxIN0+, RxIN0-CK IN+, CK IN-



5) V_{CM} : LVDS Common mode voltage (V_{CM} =1.25V)

5-2. Constant current circuit for LED Backlight

Temp. = $-20 \sim 70^{\circ}$ C Item Symbol Condition Min. Max. Unit Typ. 1) -V Supply voltage VIN 10.8 12.013.2Current consumption \mathbf{I}_{IN} 2) -290450mА V_{IN}=12.0V -100 Permissive input ripple voltage V_{RP_BL} mVp-p "Low" level V $V_{\rm IL_BLBRT}$ 0 -0.8**BLBRT** Input signal voltage V VIH_BLBRT "High" level 2.3-VIN BLBRT Input pull-down resistance 100 300 500 $R_{\rm IN_BLBRT}$ kΩ $V_{\rm IL_BLEN}$ "Low" level 0 V 0.8 -BLEN Input signal voltage -VIH_BLEN "High" level 2.3VIN V BLEN Input pull-down resistance 100 500 $R_{\rm IN_BLEN}$ 300 kΩ -**PWM Frequency** 3) **f**_{PWM} -200 -10k Hzfpwm=200Hz 1 -100% % **PWM** Duty ratio 3) f_{PWM}=2kHz 10 100 D_{PWM} fpwm=10kHz 100 50% --Operating life time 4), 5) Т Temp.=25°C 100,000 h

1) V_{IN} -turn-on conditions



2) $V_{IN} = 12V$, Temp. = 25°C, $D_{PWM} = 100\%$

3) PWM Timing Diagram



ton, toff $\geq 50 \,\mu$ s.

In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

- 4) When brightness decrease 50% of minimum brightness.The average life of a LED will decrease when the LCD is operating at higher temperatures.
- 5) Life time is estimated data.(Condition : IF=60mA, Ta= 25° C in chamber).

5-2. Touch panel

Item	Specification
Supply voltage for touch panel	$5.0\mathrm{V}$
Terminal resistance	$xL\sim xR: 362\Omega\sim 845\Omega$
	$yU\sim yL:229\Omega\sim 533\Omega$
Linearity	less than $\pm 2.0\%$
Insulation resistance	$100 \mathrm{M}\Omega$ or more at $\mathrm{DC25V}$

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6. Optical characteristics

Measuring spot = ϕ 6.0mm, Temp. = 25°C

		1			Suring Spot	, ,	1
Item		Symbol	Condition	Min.	Тур.	Max.	Unit
D (Rise	τr	$\theta = \phi = 0^{\circ}$	-	8	-	ms
Response time	Down	τ _d	$\theta = \phi = 0^{\circ}$	-	22	-	ms
		heta upper		-	80	-	1
Viewing angle View direction	-	θ lower	CD > 10	-	60	-	deg.
÷ 6 o'cloc		ϕ left	$CR \ge 10$	-	80	-	1
(Gray in	version)	ϕ right		-	80	-	deg.
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	350	500	-	-
Brightness		L	IF=60mA/Line	250	360	-	cd/m ²
	D. 1	x	$\theta = \phi = 0^{\circ}$	0.555	0.605	0.655	
	Red	У	$\theta = \phi = 0$	0.300	0.350 0.40	0.400	
	0	х	0 1 00	0.285	0.335	0.385	
Chromaticity	Green	У	$\theta = \phi = 0^{\circ}$	0.515	0.565	0.615	
coordinates	DI	x	0 - 1 -08	0.105	0.155	0.205	-
	Blue	У	$\theta = \phi = 0^{\circ}$	0.065	0.115	0.165	
	11 71 · 4	х		0.250	0.300	0.350	
	White	У	$\theta = \phi = 0^{\circ}$	0.265	0.315	0.365	

6-1. Definition of contrast ratio

6-2. Definition of response time





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6-4. Brightness measuring points



- 1) Rating is defined as the white brightness at center of display screen(3).
- 2) 5 minutes after LED is turned on. (Ambient Temp.= 25° C)



7. Interface signals

7-1. Interface signals

No.	Symbol	Description	Note
1	GND	GND	
2	SELLVDS	Mode select signal(LVDS Data mapping)	
3	GND	GND	
4	GND	GND	
5	RxIN3+	LVDS receiver signal CH3(+)	LVDS
6	RxIN3-	LVDS receiver signal CH3(-)	LVDS
7	GND	GND	
8	CK IN+	LVDS receiver signal CK(+)	LVDS
9	CK IN-	LVDS receiver signal CK(-)	LVDS
10	GND	GND	
11	RxIN2+	LVDS receiver signal CH2(+)	LVDS
12	RxIN2-	LVDS receiver signal CH2(-)	LVDS
13	GND	GND	
14	RxIN1+	LVDS receiver signal CH1(+)	LVDS
15	RxIN1-	LVDS receiver signal CH1(-)	LVDS
16	GND	GND	
17	RxIN0+	LVDS receiver signal CH0(+)	LVDS
18	RxIN0-	LVDS receiver signal CH0(-)	LVDS
19	GND	GND	
20	GND	GND	
21	VDD	+3.3V power supply	
22	V _{DD}	+3.3V power supply	
23	GND	GND	
24	BLBRT	PWM signal(Brightness adjustment)	
25	BLEN	ON/OFF terminal voltage	
26	GND	GND	
27	VIN	+12V power supply	
28	VIN	+12V power supply	
29	GND	GND	
30	GND	GND	

LCD connector	:	FI-X30SSLA-HF	(JAE)
Matching connector	:	FI-X30HL	(JAE)
	:	FI-X30HL-T	(JAE)
	:	FI-X30C2L-NPB	(JAE)
	:	FI-X30C2L-T-NPB	(JAE)

LVDS receiver	:	Embedded in ASIC
Matching LVDS transmitter	:	THC63LVDM83R(THine Electronics) or compatible



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7-2. Data mapping(6bit RGB input)

1) Location of SELLVDS	(THC63LVDM83R(THine Electronics) or c	ompatible)
	m	OD', OFLIVDO	

Transmitter		2Pin SF	2Pin SELLVDS		
Pin No.	Data	= L(GND) or OPEN	= H(3.3V)		
51	TA0	—	R0(LSB)		
52	TA1	_	R1		
54	TA2	_	R2		
55	TA3	_	R3		
56	TA4	—	R4		
3	TA5	_	R5(MSB)		
4	TA6	_	GO(LSB)		
6	TB0	_	G1		
7	TB1	_	G2		
11	TB2	_	G3		
12	TB3	_	G4		
14	TB4	_	G5(MSB)		
15	TB5	—	B0(LSB)		
19	TB6	_	B1		
20	TC0	_	B2		
22	TC1	_	B3		
23	TC2	—	B4		
24	TC3	_	B5(MSB)		
27	TC4	_	(HS)		
28	TC5	_	(VS)		
30	TC6	—	DE		
50	TD0	—	GND		
2	TD1	—	GND		
8	TD2	_	GND		
10	TD3	_	GND		
16	TD4	_	GND		
18	TD5	-	GND		
25	TD6	_	GND		

SELLVDS=H(3.3V)



 $\begin{array}{l} \textbf{DE} : \textbf{DATA ENABLE} \\ \textbf{HS} : \textbf{H}_{\text{SYNC}} \end{array}$

 $VS:V_{\mathrm{SYNC}}$

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2) Block Diagram

SELLVDS=H(3.3V)



When using "6-bit Transmitter", please note that you are required to do the process of "surplus receiver" as following chart.





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7-3. Data mapping(8bit RGB input)

1) Location of SELLVDS	(THC63LVDM83R(THine Electronics) or con	mpatible)
	Transmittan	OD: CELLVDC	

Transmitter		2Pin Sl	ELLVDS
Pin No.	Data	= L(GND) or OPEN	= H(3.3V)
51	TA0	R0(LSB)	R2
52	TA1	R1	R3
54	TA2	R2	R4
55	TA3	R3	R5
56	TA4	R4	R6
3	TA5	R5	R7(MSB)
4	TA6	G0(LSB)	G2
6	TB0	G1	G3
7	TB1	G2	G4
11	TB2	G3	G5
12	TB3	G4	G6
14	TB4	G5	G7(MSB)
15	TB5	B0(LSB)	B2
19	TB6	B1	B3
20	TC0	B2	B4
22	TC1	B3	B5
23	TC2	B4	B6
24	TC3	B5	B7(MSB)
27	TC4	(HS)	(HS)
28	TC5	(VS)	(VS)
30	TC6	DE	DE
50	TD0	R6	R0(LSB)
2	TD1	R7(MSB)	R1
8	TD2	G6	G0(LSB)
10	TD3	G7(MSB)	G1
16	TD4	B6	B0(LSB)
18	TD5	B7(MSB)	B1
25	TD6	(NA)	(NA)

SELLVDS=L(GND) or OPEN





2) Block Diagram

SELLVDS=L(GND) or OPEN





SELLVDS=H(3.3V)



7-4. Touch panel

No.	Symbol	Description			
1	xR	x-Right terminal			
2	yL	y-Lower terminal			
3	xL	x-Left terminal			
4	уU	y-Upper terminal			

Touch panel side connector	:	1mm pitch	
Recommended matching connector	:	Series 9616	(IRISO)
		Series 9610	(IRISO)
		Series FMS	



8. Input timing characteristics

	Item	Symbol	Min.	Тур.	Max.	Unit	Note
Clock (CK)	Frequency	1/Tc	30	40	48	MHz	
	Harizontal Daviad	Th	860	1056	1395	Тс	
	Horizontal Period	Th	24.0	26.4	-	$\mu \ s$	1)
Enable signal (DE)	Horizontal display period	Thd		800		Тс	
	Vertical Period	Tv	610	628	1024	Th	
	Vertical display period	Tvd		600		Th	
Refresh rate		fv	50	60	70	Hz	2)

8-1. Timing characteristics

1) Please set a clock frequency, a vertical dormant period, and the horizontal dormant period so that the Horizontal Period should not reach less than Min. value.

2) If the refresh rate reach less than Min. value, the deterioration of the display quality, flicker etc., may occur.(fv=1/Tv)



Vertical Timing Diagram

8-2. Input Data Signals and Display position on the screen





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- 9-1. Electrical (In customer's design, please remember the following considerations.)
 - 1) Do not use the current regulated circuit.
 - 2) Keep the current limit with top and bottom layer.
 - (Please refer to "Electrical absolute maximum ratings" for details.)
 - 3) Analog touch panel can not sense two points touching separately.
 - 4) A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the touch panel position data.
 - 5) Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

9-2. Software

- 1) Do the "User Calibration".
- 2) "User Calibration" may be needed with long term using. Include "User Calibration" menu in your software.
- 3) When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

9-3. Mounting on display and housing bezel

- 1) Do not use an adhesive tape to bond it on the front of touch panel and hang it to the housing bezel.
- 2) Never expand the touch panel top layer (PET-film) like a balloon by internal air pressure. The life of the touch panel will be extremely short.
- 3) If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur. This will cause sometimes a short circuit.
- 4) Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.



10. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

- No1. No5. above indicate
 - 1. Year code
 - 2. Month code
 - 3. Date
 - 4. Version Number
 - 5. Country of origin (Japan or China)

Year	2012	2013	2014	2015	2016	2017
Code	2	3	4	5	6	7

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	Х	Y	Z

11. Warranty

11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

11-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



12. Precautions for use

- 12-1. Installation of the LCD
- 1) Please ground either of the mounting (screw) holes located at each corner of an LCD, in order to stabilize brightness and display quality.
- 2) The LCD shall be installed so that there is no pressure on the LSI chips.
- 3) The LCD shall be installed flat, without twisting or bending.
- Please design the housing window so that its edges are between the active area and the effective area of the LCD screen.
 Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode

Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.

12-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

12-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

12-4. Storage

- The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

12-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3) When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4) Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



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12. Reliability test data

Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect
Point Activation life	Silicon rubber, Tip: R = 4.0 Hitting force 3N Hitting speed 2 time/s	one million times	Terminal resistance Insulation resistance Linearity Actuation Force	 No defect No defect No defect No defect

1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

2) The LCD is tested in circumstances in which there is no condensation.

3) The reliability test is not an out-going inspection.

 The result of the reliability test is for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.









Spec No.	TQ3C-8EAF0-E2YAH40-01
Date	October 16, 2014

KYOCERA INSPECTION STANDARD

TYPE : TCG104SVLPAAFA-AA20

KYOCERA DISPLAY CORPORATION

Original	Designed by :	Engineering de	ept.	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved	
July 13, 2012	K. Janimuka	Y. Yamajaki	W. Yano	O. Sato	1-Hamar S	



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		1 c	hange	e "Definition of	inspection	iter	n" Bright dot	defect	



Visuals specification

1)	Mata
IJ	Note

1) Note			Note					
General	 Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent. This inspection standard about the image quality shall be applied to any defect within the effective viewing area and shall not be applicable to outside of the area. 							
	Lumina	ion distance	: 500 Lux min. : 300 mm. : 25 ± 5°C					
Definition of inspection item	Direction Dot defect	Bright dot defect	: Directly above The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool: 5% Transparency neutral density filter. Count dot: If the dot is visible through the filter. Don't count dot: If the dot is not visible through the filter. RGBRGBRGB RGBRGBRGB RGBRGBRGB Add drawing> The dot is constantly "off" when power applied to the					
		Black dot defect White dot (Circular/foreign particle) Adjacent dot	The dot is constantly "off" when power applied to the LCD, even when all "White" data sent to the screen. Similar size compared to bright dot. Pixel works electrically, however, circular/foreign particle makes dot appear to be "on" even when all "Black" data is sent to the screen. Adjacent dot defect is defined as two or more bright dot defects or black dot defects. RGBRGBRGB RGBRGBRGB dot defect					
	External inspection Others	Bubble, Scratch, Foreign particle (Polarizer, Cell, Backlight) Appearance inspection CFL wires	Visible operating (all pixels "Black" or "White") and non operating. Does not satisfy the value at the spec. Damaged to the CFL wires, connector, pin, functional					
	Definition of size	Definition of cir d = (a + b)	failure or appearance failure. rcle size Definition of linear size					



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2) Standard

2) Standa		1						
Classification		Inspection item		Judgement standard				
Defect	Dot	Bright dot defect		Acceptable number		:4	: 4	
(in LCD	defect			Bright dot spacing : 5 mm		or more		
glass)		Black dot defect		Acceptable number : 5				
				Black dot spacing		: 5 mm or more		
		2 dot join	Bright dot defect	Acceptable number : 2				
			Black dot defect	Acceptable number		: 3		
		3 or more dots join		Acceptable number : 0				
		Total dot d	efects	Acceptable number		÷5 Ma	x	
	Others	White dot, Dark dot		-				
0 11010		(Circle)		Size (mm)		Acceptable number		
				$d \leq 0.2$		(Neglected)		
				$\begin{array}{c c} \mathbf{d} \equiv 0.2 \\ \hline 0.2 < \mathbf{d} \leq 0.4 \end{array}$		5		
				0.4 < d \leq		3		
				0.5< m d			0	
			~ • • •			-		
	inspection	Polarizer (Scratch)			<u> </u>		
	(Defect on			Width (mm)	Length (1	mm)	Acceptable number	
Polarizer or				$W \leq 0.1$			(Neglected)	
between Polarizer				$0.1 < W \leq 0.3$	$L \leq 5.0$		(Neglected)	
and LCD	glass)			0.0 < W	5.0 < L		0	
				0.3 < W	_		0	
			Bubble)					
				Size (mm)		Acceptable number		
				d \leq 0.2		(Neglected)		
				$0.2 < d \leqq 0.3$		5		
				$0.3 < \mathrm{d} \leq 0.5$		3		
				0.5< m d			0	
		Foreign pa	rticle					
		(Circular shape)		Size (mm)		Acceptable number		
				d \leq 0.2		(Neglected)		
				$0.2 < d \leq 0.4$		5		
				$0.4 < \mathrm{d} \le 0.5$		3		
				0.5< m d		0		
		Foreign particle						
		(Linear shape) Scratch		Width (mm) Length		(mm) Acceptable number		
				$W \leq 0.03$		(11111)	(Neglected)	
					$L \leq 2.0$		(Neglected)	
				$0.03 < W \leq 0.1$	$2.0 < L \leq 4.0$		3	
					4.0 < L = 4.0		0	
				0.1 < W	-		(According to	
							circular shape)	
							onapo,	



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Inspection item	Judgement standard								
Scratch,	(W = Width, L = Length, D = Diameter = (major axis + minor axis)/2)								
Foreign particle	Item	Width(mm)	Length(mm)	Acc	Acceptable number				
(Touch screen		W \leq 0.03	$L \leq 20$	Neglected					
portion)	Scratch			-	2pces within φ20mm				
	Deraten	$0.05 < W \leq 0.08$	$L \leq 6$		2pces within φ20mm				
		$0.08 < W \leq 0.1$	$L \leq 4$	1pce	1pces within φ30mm				
	Foreign	$W \leq 0.05$	Neglected		Neglected				
	(line like)	$0.05 < W \leq 0.1 \qquad L \leq 5$		2pce	2pces within φ30mm				
	Foreign	$D \leq 0.2$			Neglected				
	(circle like)				es within φ30mm				
~	Above are applied to the visible area. Unless there are foreign particle and damage affected seriously to the electrical performance out of the active area, we approve of this product.								
Glass crack (Touch screen portion)	Item	Size (m	ım)		Acceptable number				
portion)	Corner		2 X	≦3	2 pcs /panel				
	crack		Y	≤ 3					
			Z	< t					
	Crack in	×		≤ 5	2 pcs /side				
	other area		Y	≤ 1.5					
	than in								
	corner	4	Z	<t					
	Progressive crack		5/		0 pcs (NG even 1pcs)				

