74AHCV17A

Hex buffer Schmitt trigger Rev. 1 — 6 December 2016

Product data sheet

General description 1.

The 74AHCV17A is a hex buffer with Schmitt-trigger inputs, capable of transforming slowly changing input signals into sharply defined, jitter-free output signals.

Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

This device is fully specified for partial power down applications using I_{OFF}. The I_{OFF} circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

Features and benefits 2.

- Wide supply voltage range from 1.8 V to 5.5 V
- Typical t_{pd} of 3.2 ns at 5 V
- Typical $V_{OL(p)}$ < 0.8 V at V_{CC} = 3.3 V, T_{amb} = 25 °C
- Typical $V_{OH(v)} > 2.3 \text{ V}$ at $V_{CC} = 3.3 \text{ V}$, $T_{amb} = 25 ^{\circ}\text{C}$
- Supports mixed-mode voltage operation on all ports
- I_{OFF} circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 250 mA per JESD 78 Class II
- ESD protection:
 - HBM ANSI/ESDA/JEDEC JS-001 Class 2 exceeds 3 kV
 - MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101E exceeds 2 kV
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

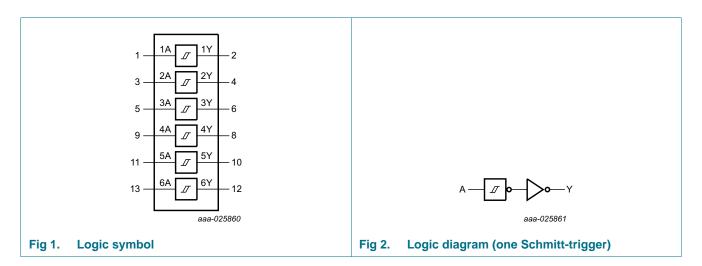
Ordering information 3.

Table 1. **Ordering information**

Type number	Package										
	Temperature range	Name	Description	Version							
74AHCV17APW	−40 °C to +125 °C	TSSOP14	plastic thin shrink small outline package; 14 leads; body width 4.4 mm	SOT402-1							

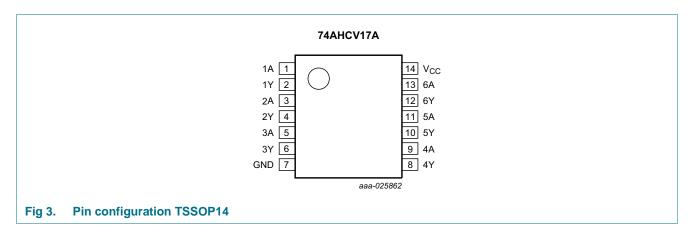


4. Functional diagram



5. Pinning information

5.1 Pinning



5.2 Pin description

Table 2. Pin description

Symbol	Pin	Description
1A, 2A, 3A, 4A, 5A, 6A	1, 3, 5, 9, 11, 13	data input
1Y, 2Y, 3Y, 4Y, 5Y, 6Y	2, 4, 6, 8, 10, 12	data output
GND	7	ground (0 V)
Vcc	14	supply voltage

6. Functional description

Table 3. Function table[1]

Input	Output
nA	nY
L	L
Н	Н

^[1] H = HIGH voltage level;

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+7.0	V
V _I	input voltage		[1]	-0.5	+7.0	V
Vo	output voltage	output HIGH or LOW state	[2][3]	-0.5	V _{CC} + 0.5	V
		output power-down	[2]	-0.5	+7.0	V
I _{IK}	input clamping current	V _I < 0 V		-50	-	mA
I _{OK}	output clamping current	V _O < 0 V		-50	-	mA
I _O	output current	$V_O = 0 \text{ V to } V_{CC}$		-	±50	mA
I _{CC}	supply current			-	100	mA
I_{GND}	ground current			-100	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +125 ^{\circ}\text{C}$	[4]	-	500	mW

^[1] The minimum input voltage ratings may be exceeded if the input current ratings are observed.

L = LOW voltage level.

^[2] The output voltage ratings may be exceeded if the output current ratings are observed.

^[3] This value is limited to 7 V maximum.

^[4] For TSSOP14 packages: above 75 $^{\circ}$ C the value of P_{tot} derates linearly at 7 mW/K.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		1.8	5.0	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage	output HIGH or LOW state	0	-	V _{CC}	V
		output power-down	0	-	5.5	V
T _{amb}	ambient temperature		-40	+25	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 2.3 V to 2.7 V	-	-	50	ms/V
T _{amb} ar		V _{CC} = 3.0 V to 3.6 V	-	-	20	ms/V
		V _{CC} = 4.5 V to 5.5 V	-	-	1	ms/V

9. Static characteristics

Table 6. Static characteristics

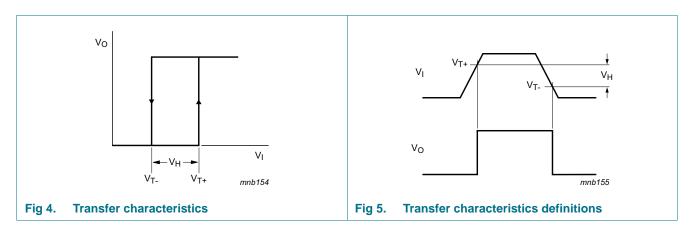
Voltages are referenced to GND (ground = 0 V).

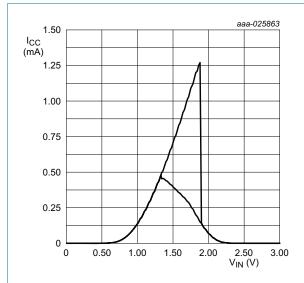
Symbol	Parameter	Conditions		25 °C		-40 °C t	o +85 °C	-40 °C to	+125 °C	Unit
			Min	Тур	Max	Min	Max	Min	Max	
V_{T+}	positive-going	V _{CC} = 1.8 V	-	-	1.65	-	1.65	-	1.65	V
	threshold	V _{CC} = 2.3 V	-	-	1.85	-	1.85	-	1.85	V
	voltage	V _{CC} = 3.0 V	-	-	2.2	-	2.2	-	2.2	V
		V _{CC} = 4.5 V	-	-	3.15	-	3.15	-	3.15	V
		V _{CC} = 5.5 V	-	-	3.85	-	3.85	-	3.85	V
V_{T-}	negative-going	V _{CC} = 1.8 V	0.15	-	-	0.15	-	0.15	-	V
	threshold	V _{CC} = 2.3 V	0.45	-	-	0.45	-	0.45	-	V
	voltage	V _{CC} = 3.0 V	0.9	-	-	0.9	-	0.9	-	V
		V _{CC} = 4.5 V	1.35	-	-	1.35	-	1.35	-	V
		V _{CC} = 5.5 V	1.65	-	-	1.65	-	1.65	-	V
V_{H}	hysteresis	V _{CC} = 1.8 V	0.15	-	1.05	0.15	1.05	0.15	1.05	V
	voltage	V _{CC} = 2.3 V	0.2	-	1.1	0.2	1.1	0.2	1.1	V
		V _{CC} = 3.0 V	0.3	-	1.2	0.3	1.2	0.3	1.2	V
		V _{CC} = 4.5 V	0.4	-	1.4	0.4	1.4	0.4	1.4	V
		V _{CC} = 5.5 V	0.5	-	1.6	0.5	1.6	0.5	1.6	V
V _{OH}	HIGH-level	$V_I = V_{T+}$ or V_{T-}								V
	output voltage	$I_O = -50 \mu A$; $V_{CC} = 1.8 \text{ V}$	1.7	1.8	-	1.7	-	1.7	-	V
		$I_O = -50 \mu A$; $V_{CC} = 3.0 \text{ V}$	2.9	3.0	-	2.9	-	2.9	-	V
		$I_O = -50 \mu A; V_{CC} = 4.5 V$	4.4	4.5	-	4.4	-	4.4	-	V
		$I_{O} = -8 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.58	-	-	2.48	-	2.48	-	V
		$I_{O} = -16 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.94	-	-	3.80	-	3.80	-	

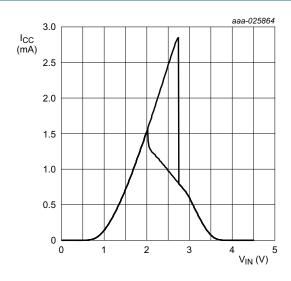
Table 6. Static characteristics ...continued Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C to	o +85 °C	–40 °C to	+125 °C	Unit
			Min	Тур	Max	Min	Max	Min	Max	
V _{OL} LOW-level		$V_I = V_{T+}$ or V_{T-}								
	output voltage	$I_O = 50 \mu A; V_{CC} = 1.8 V$	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 50 \mu A; V_{CC} = 3.0 V$	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 50 \mu A; V_{CC} = 4.5 V$	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 8 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.36	-	0.44	-	0.44	V
		$I_O = 16 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	-	0.44	-	0.55	-	0.55	V
I _{OFF}	power-off leakage current	V_I or $V_O = GND$ to 5.5 V; $V_{CC} = 0$ V	-	-	0.5	-	5	-	5	μΑ
II	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 0$ V to 5.5 V	-	-	±0.1	-	±1	-	±1	μΑ
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	2	-	20	-	20	μΑ

9.1 Transfer characteristics waveforms

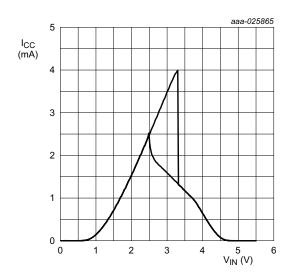






a. $V_{CC} = 3.0 \text{ V}$





c. $V_{CC} = 5.5 \text{ V}$

Fig 6. Typical transfer characteristics

10. Dynamic characteristics

Table 7. Dynamic characteristics

GND = 0 V. For test circuit see Figure 8.

Symbol	Parameter	Conditions		25 °C		-40 °C	to +85 °C	-40 °C t	o +125 °C	Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
t _{pd}	propagation	nA to nY; see Figure 7 [2]								
	delay	V _{CC} = 2.3 V to 2.7 V								
		C _L = 15 pF	-	5.3	19.7	1	22	1	23.6	ns
		C _L = 50 pF	-	7.3	24	1	27	1	29.0	ns
		V _{CC} = 3.0 V to 3.6 V								
		C _L = 15 pF	-	4.1	12.8	1	15	1	16.2	ns
		C _L = 50 pF	-	5.7	16.3	1	18.5	1	20.0	ns
		V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	3.2	8.6	1	10	1	10.7	ns
		C _L = 50 pF	-	4.5	10.6	1	12	1	12.9	ns
Cı	input capacitance	$V_I = V_{CC}$ or GND; $V_{CC} = 3.3 \text{ V}$	-	2	6	-	6	-	6	pF
Co	output capacitance	$V_O = V_{CC}$ or GND; $V_{CC} = 3.3 \text{ V}$	-	5	-	-	-	-	-	pF
C _{PD}	power dissipation capacitance	per buffer; $V_{CC} = 5 \text{ V}$; $C_L = 0 \text{ pF}$; $f = 10 \text{ MHz}$; $V_I = \text{GND to } V_{CC}$	-	15	-	-	-	-	-	pF

^[1] Typical values are measured at T_{amb} = 25 °C and V_{CC} = 2.5 V, 3.3 V, and 5 V respectively, unless otherwise specified.

 $P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

 $f_o = output frequency in MHz;$

C_L = output load capacitance in pF;

 V_{CC} = supply voltage in Volts.

^[2] t_{pd} is the same as t_{PLH} and t_{PHL} .

^[3] C_{PD} is used to determine the dynamic power dissipation P_D (μW).

 Table 8.
 Noise characteristics

GND = 0 V. For test circuit see Figure 8.

Symbol	Parameter	Conditions	T	T _{amb} = 25 °C					
			Min	Тур	Max				
V _{CC} = 3.3	V; C _L = 50 pF		<u> </u>						
$V_{OL(p)}$	LOW-level output voltage (peak)		-	0.3	0.8	V			
V _{OL(v)}	LOW-level output voltage (valley)		-0.8	-0.1	-	V			
V _{OH(v)}	HIGH-level output voltage (valley)		-	3.0	-	V			
V _{IH(AC)}	AC HIGH-level input voltage (dynamic)		2.31	-	-	V			
V _{IL(AC)}	AC LOW-level input voltage (dynamic)		-	-	0.99	V			
V _{CC} = 5.0	V; C _L = 50 pF		, ,						
V _{OL(p)}	LOW-level output voltage (peak)		-	0.6	-	V			
V _{OL(v)}	LOW-level output voltage (valley)		-	-0.4	-	V			
V _{OH(v)}	HIGH-level output voltage (valley)		-	4.5	-	V			
V _{IH(AC)}	AC HIGH-level input voltage (dynamic)		3.5	-	-	V			
V _{IL(AC)}	AC LOW-level input voltage (dynamic)		-	-	1.5	V			

11. Waveforms

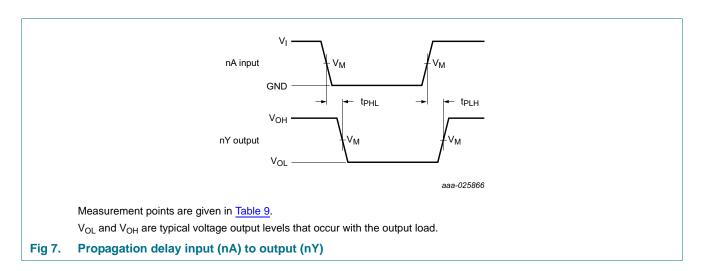
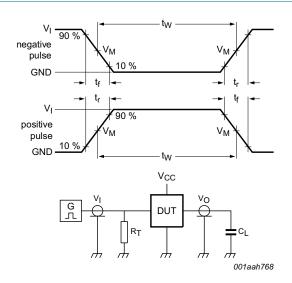


Table 9. Measurement points

Input	Output
V_{M}	V_{M}
0.5V _{CC}	0.5V _{CC}



Test data is given in Table 10.

Definitions test circuit:

 R_T = Termination resistance should be equal to output impedance Z_0 of the pulse generator

 C_L = Load capacitance including jig and probe capacitance

S1 = Test selection switch

Fig 8. Test circuit for measuring switching times

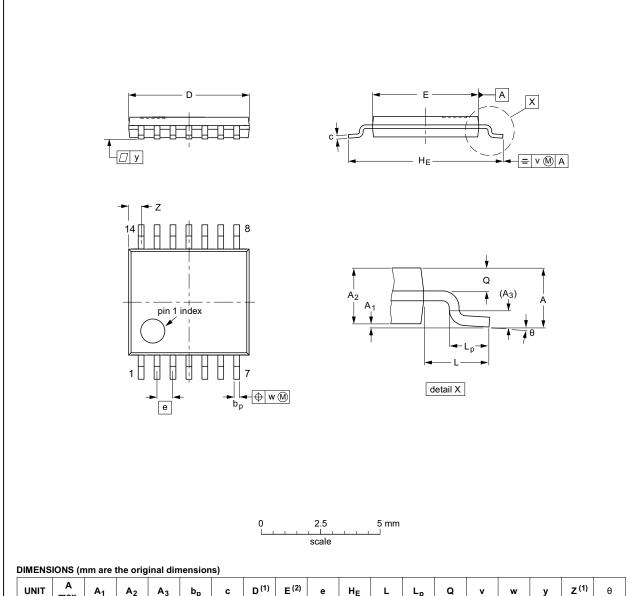
Table 10. Test data

Input		Load	Test		
VI	t _r , t _f	CL			
GND to V _{CC}	3.0 ns	15 pF, 50 pF	t _{PLH} , t _{PHL}		

12. Package outline

TSSOP14: plastic thin shrink small outline package; 14 leads; body width 4.4 mm

SOT402-1



UNIT	A max.	A ₁	A ₂	A ₃	bp	С	D ⁽¹⁾	E (2)	е	HE	L	Lp	Q	٧	w	у	Z ⁽¹⁾	θ
mm	1.1	0.15 0.05	0.95 0.80	0.25	0.30 0.19	0.2 0.1	5.1 4.9	4.5 4.3	0.65	6.6 6.2	1	0.75 0.50	0.4 0.3	0.2	0.13	0.1	0.72 0.38	8° 0°

Notes

- 1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
- 2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

OUTLINE	REFERENCES				EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT402-1		MO-153				99-12-27 03-02-18

Fig 9. Package outline SOT402-1 (TSSOP14)

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13. Abbreviations

Table 11. Abbreviations

Acronym	Description	
CDM	Charge Device Model	
DUT	Device Under Test	
ESD	ElectroStatic Discharge	
НВМ	Human Body Model	
MM	Machine Model	

14. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AHCV17A v.1	20161206	Product data sheet	-	-

15. Legal information

15.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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