# LI1806E101R-10

# UNCONTROLLED DOCUMENT

### PHYSICAL DIMENSIONS:

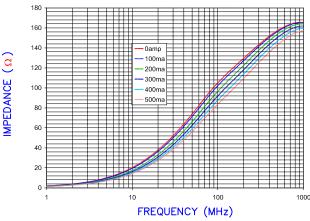
A 4.50 [.177] ± 0.25 [.010]

B 1.60 [.063] ± 0.25 [.010]

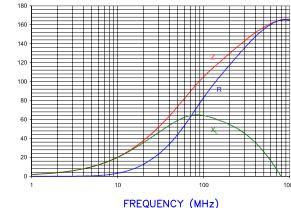
 $C 1.60 [.063] \pm 0.25 [.010]$ 

D 0.51 [.020] ± 0.25 [.010]

## Z vs FREQUENCY IMPEDANCE UNDER DC BIAS

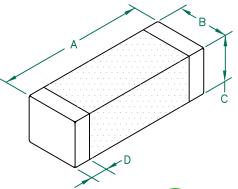






AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 3124

IMPEDANCE (Ω)



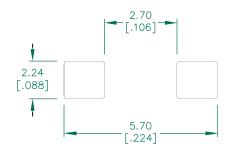


ELECT	ELECTRICAL CHARACTERISTICS:					
Z @ 100MHz (Ω)		DCR (Ω)	Rated Current			
Nominal	100					
Minimum	75					
Maximum	125	0.30	500 mA			

NOTES: UNLESS OTHERWISE SPECIFIED

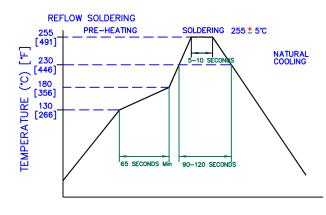
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 2000 PCS/REEL.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATING TEMP. RANGE: -40°C~+125°C. (INCLUDING SELF-HEATING)

#### LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [.030] to this dimension.)

#### RECOMMENDED SOLDERING CONDITIONS





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I	DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird							
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ŀ					written consent of Laird Tech. All	ı						
ŀ					rights to design or invention are							
ŀ					reserved.							
D	n	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	PROJECT/PART NUMBER:	REV	PART TYPE	E:	DRAWN BY:			
					LI1806E101R-10	ח	CO-F	IDE	TMR			
ſ	С	CHANGE REEL QTY FROM 3K TO 2K	11/05/08	JRK	LITOUGLIOTK	1	00-	IKE	IMD			
Ī	В	UPDATE COMPANY LOGO ADD ROHS SYM	07/22/08	JRK	DATE: 04/05/04 S	CALE: N	LE: NTS S					
Ì	A	ORIGINAL DRAFT	04/05/04	ТМВ	01/00/04	00L #		2	of 2			
İ	REV	DESCRIPTION	DATE	INT	LI1806E101R-10-D "	out #	-					